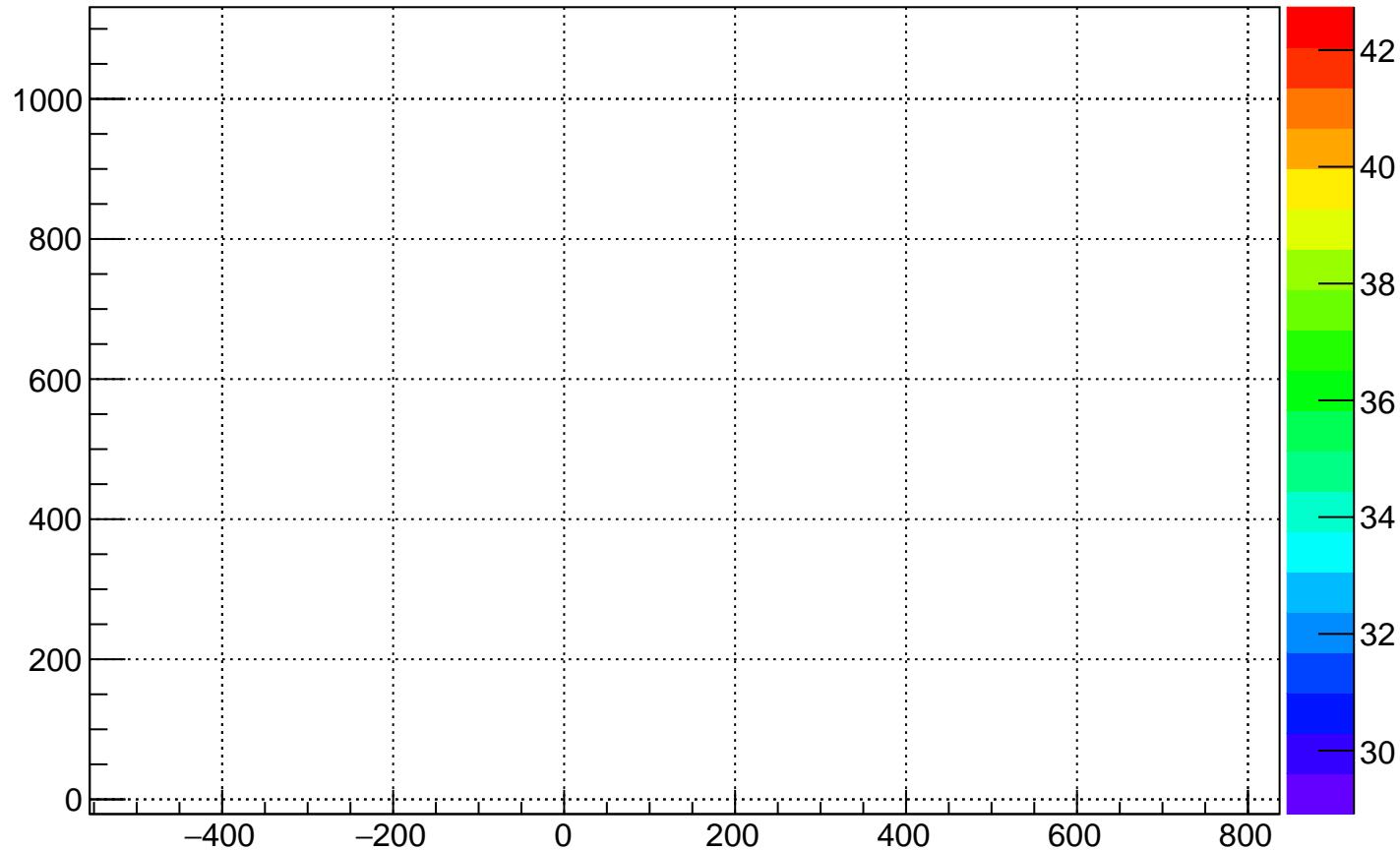
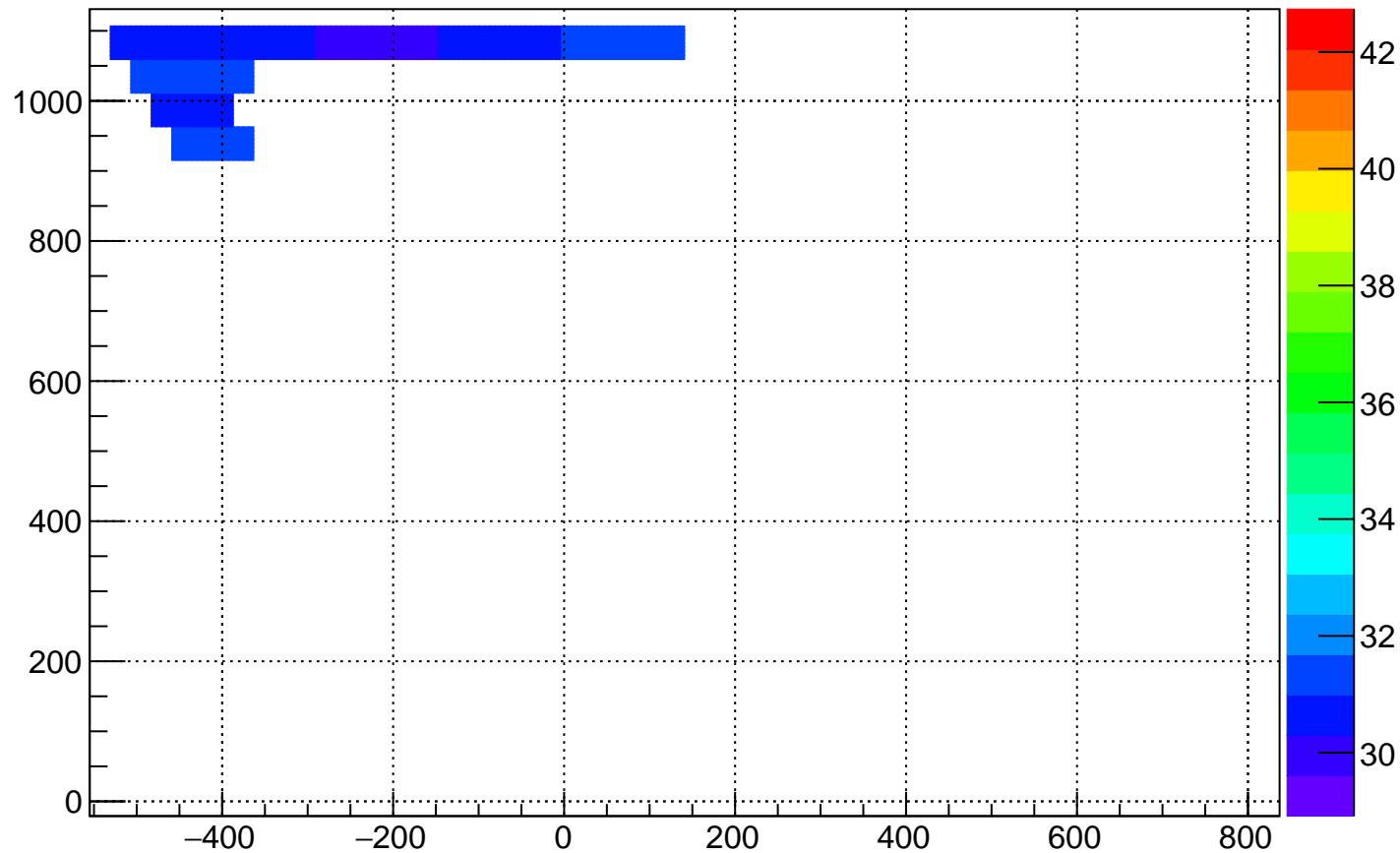


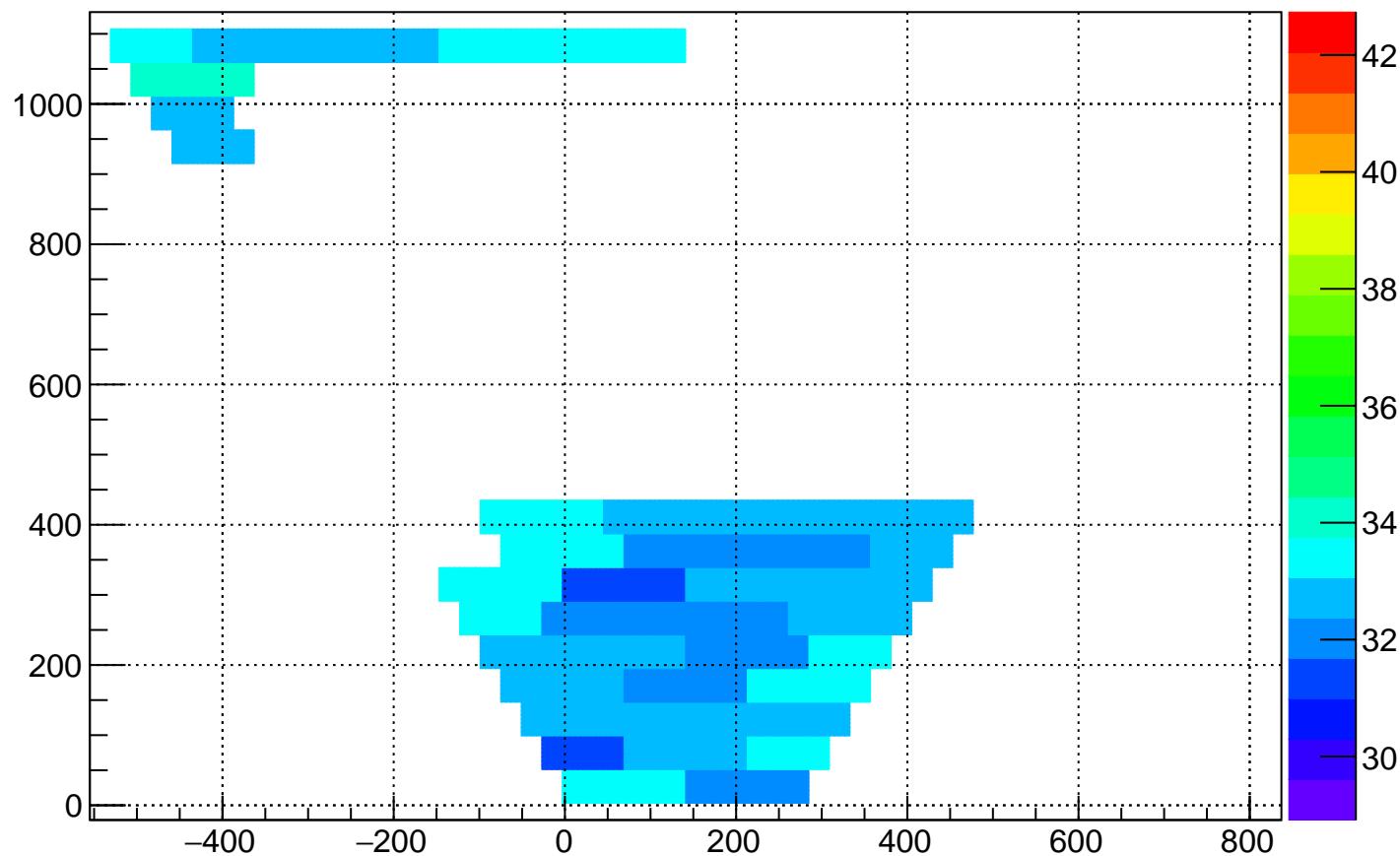
FPGA Temperature map, T= 0.0 sec



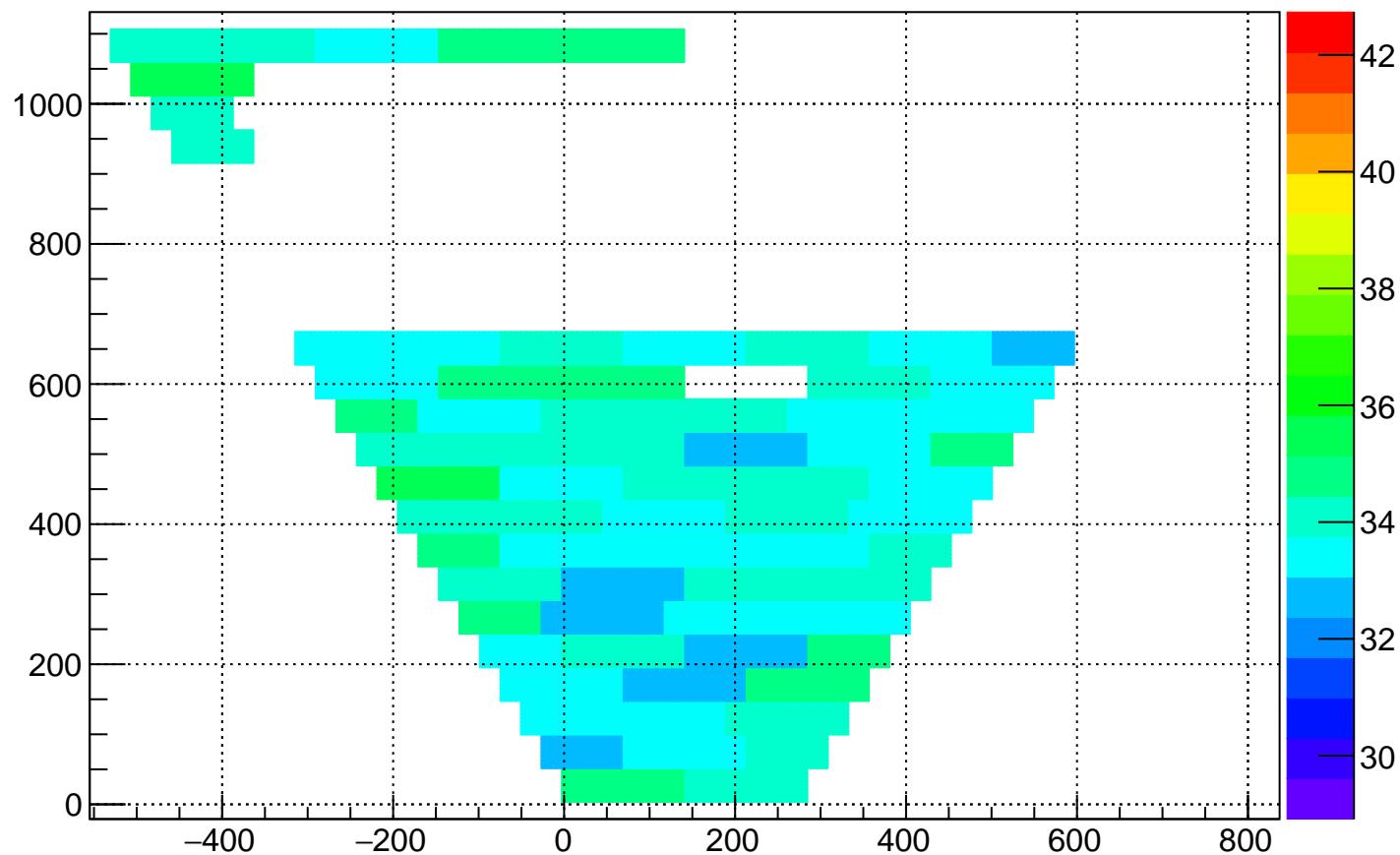
FPGA Temperature map, T= 5.0 sec



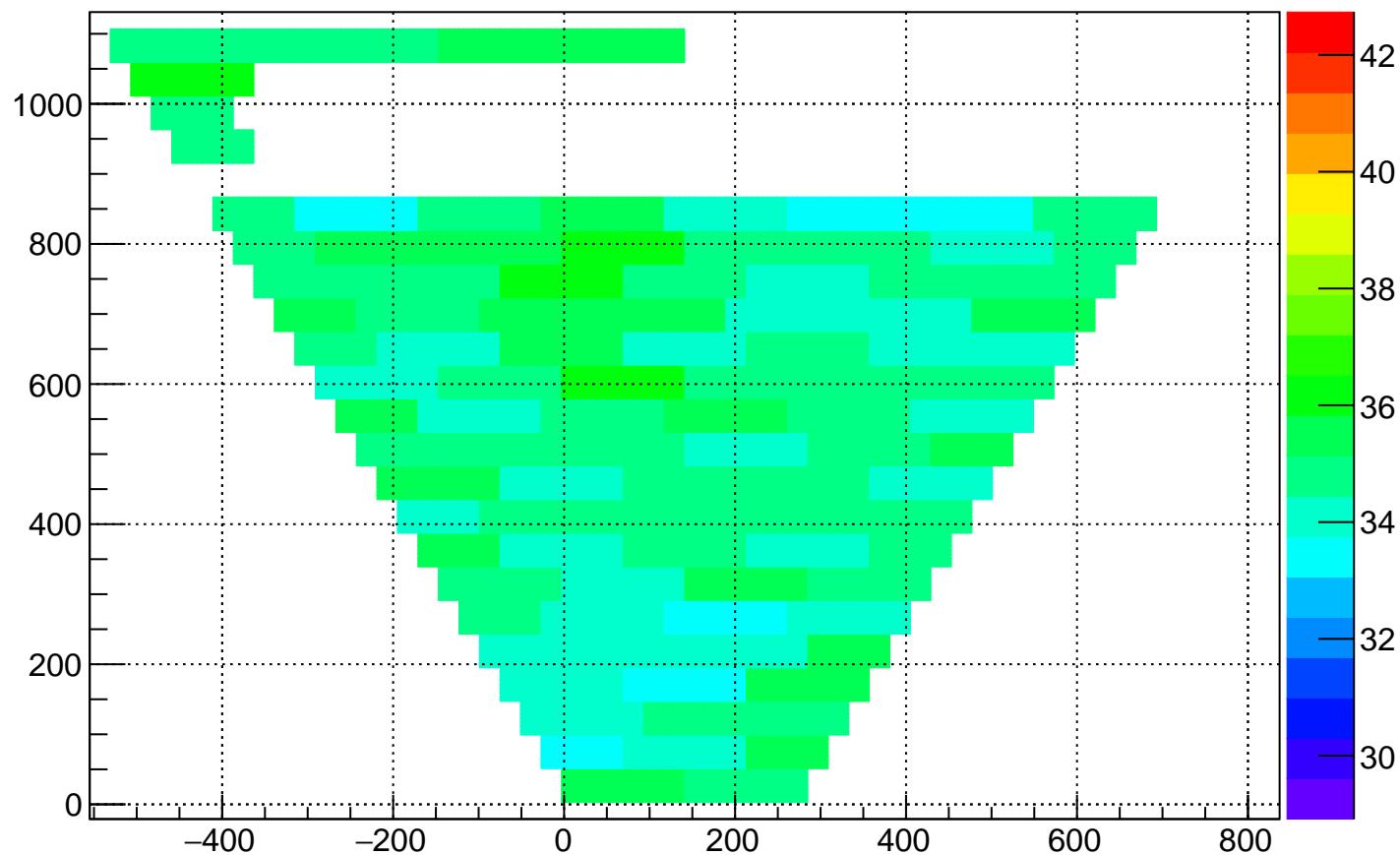
FPGA Temperature map, T= 10.0 sec



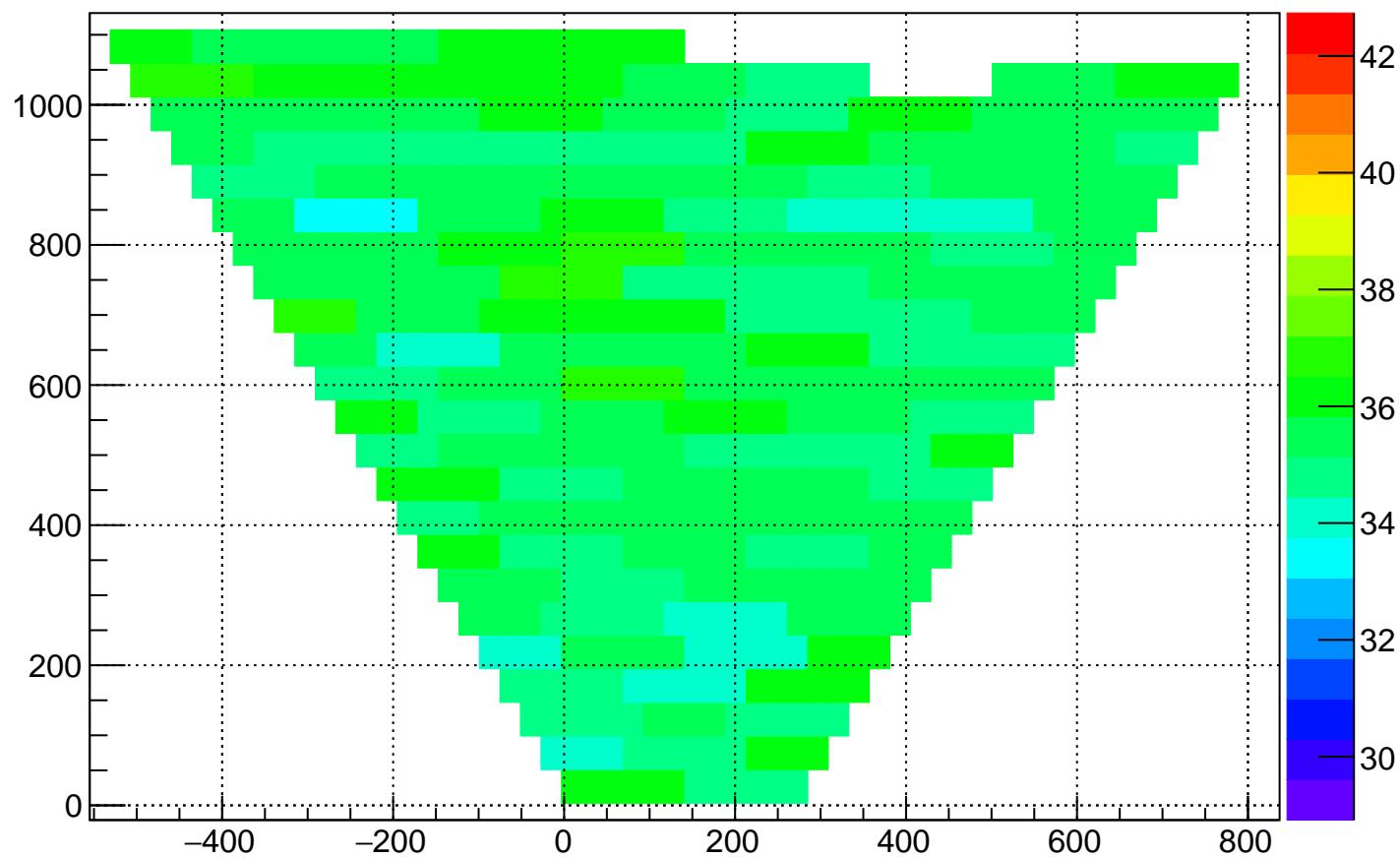
FPGA Temperature map, T= 15.0 sec



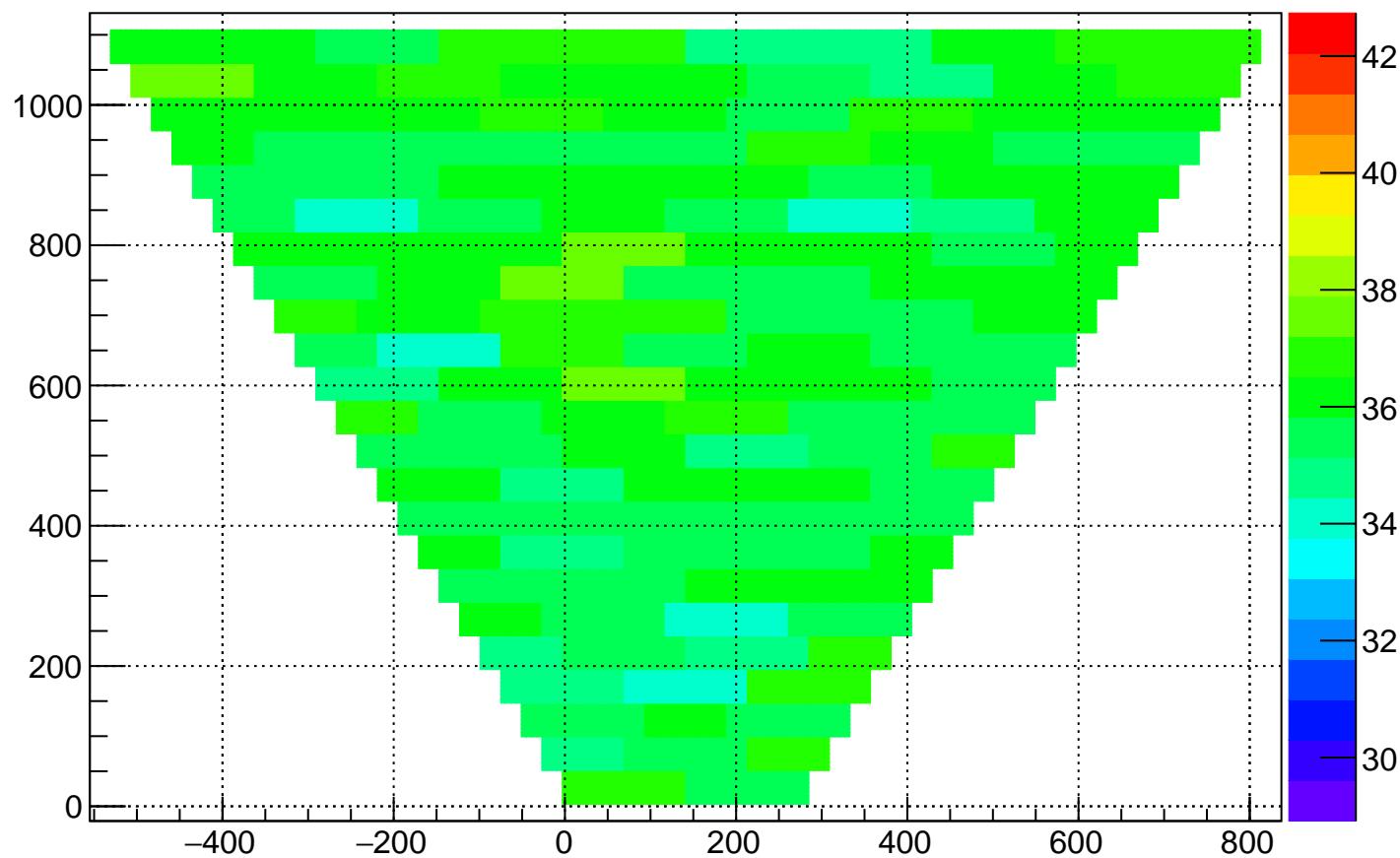
FPGA Temperature map, T= 20.0 sec



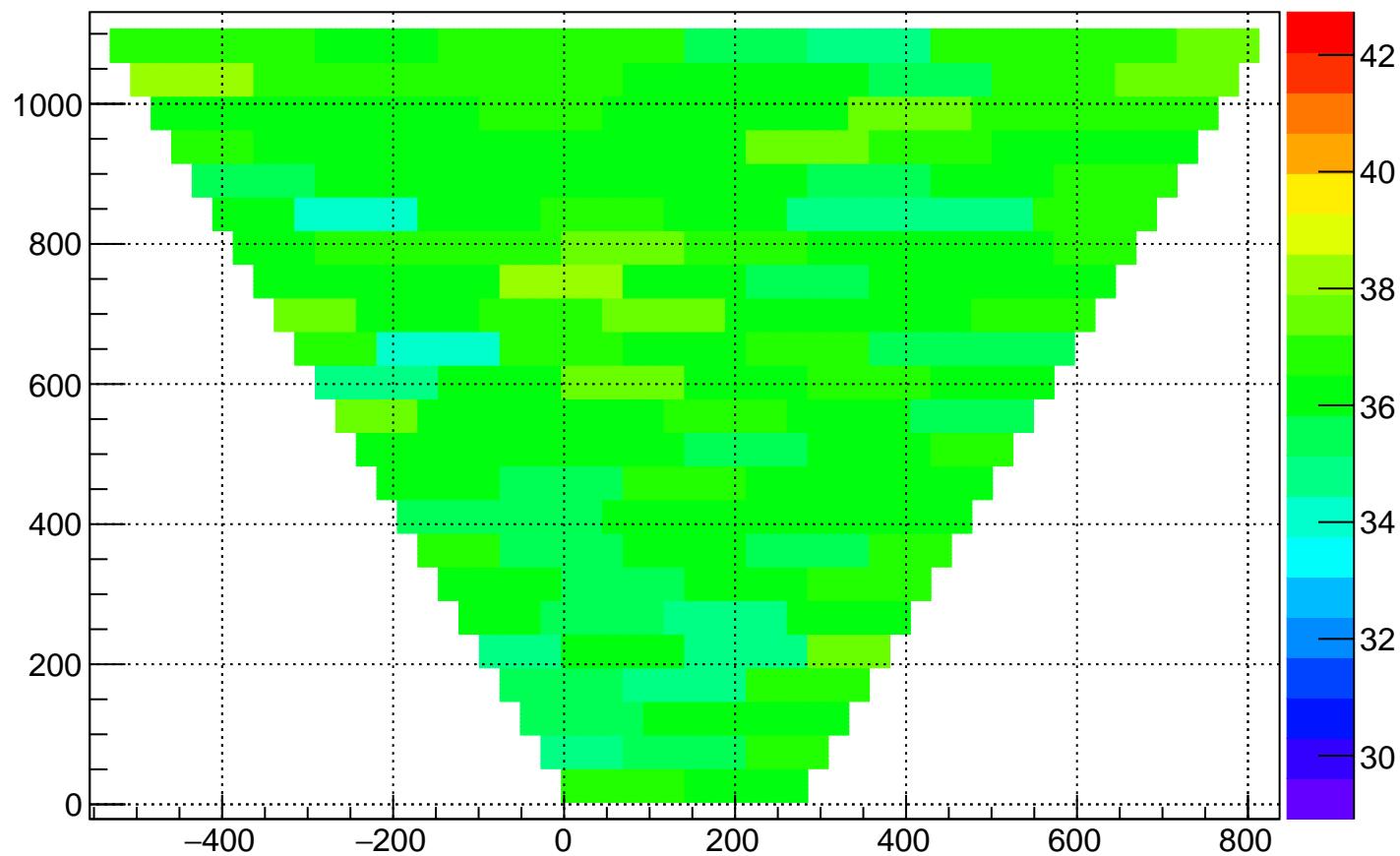
FPGA Temperature map, T= 25.0 sec



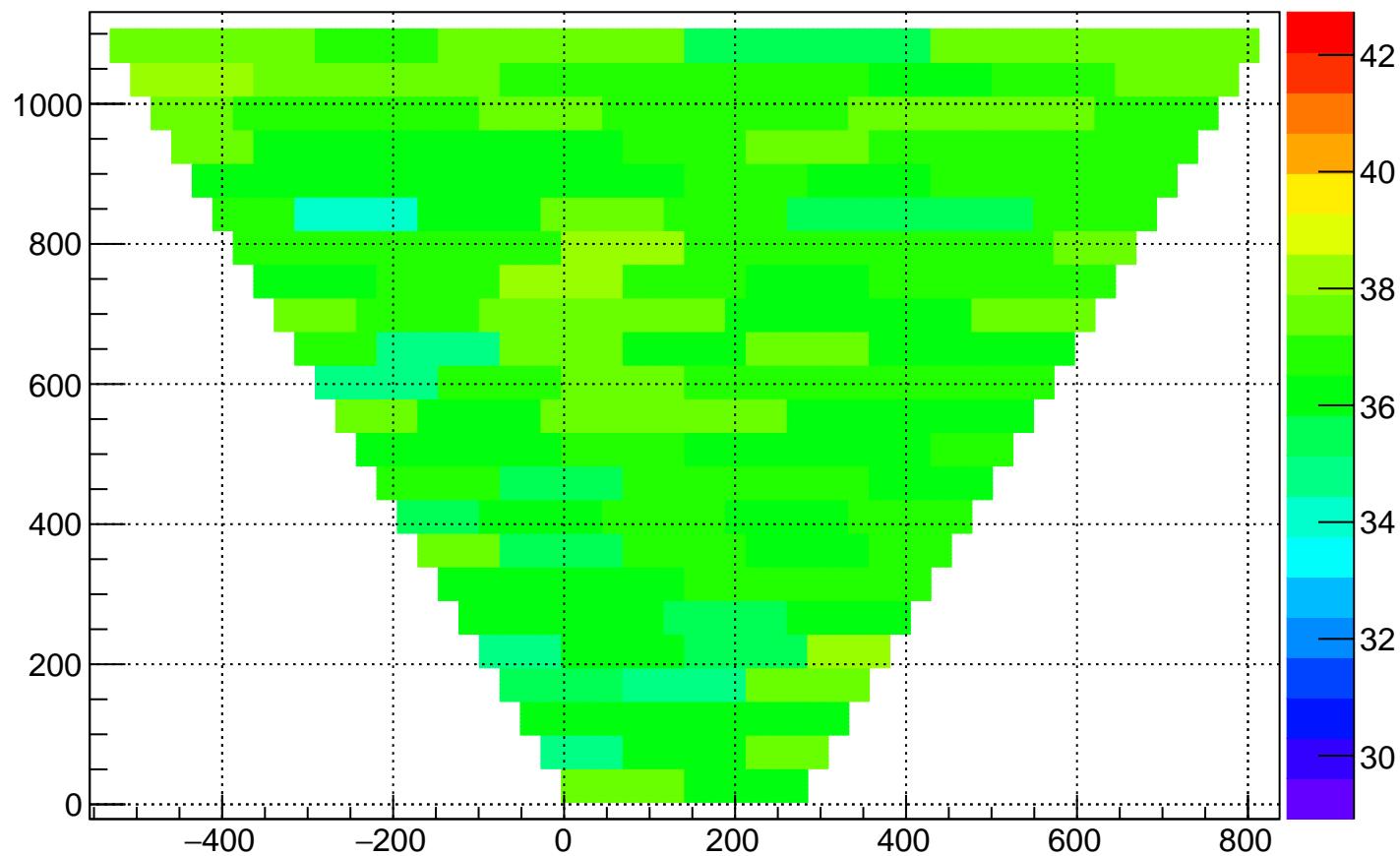
FPGA Temperature map, T= 30.0 sec



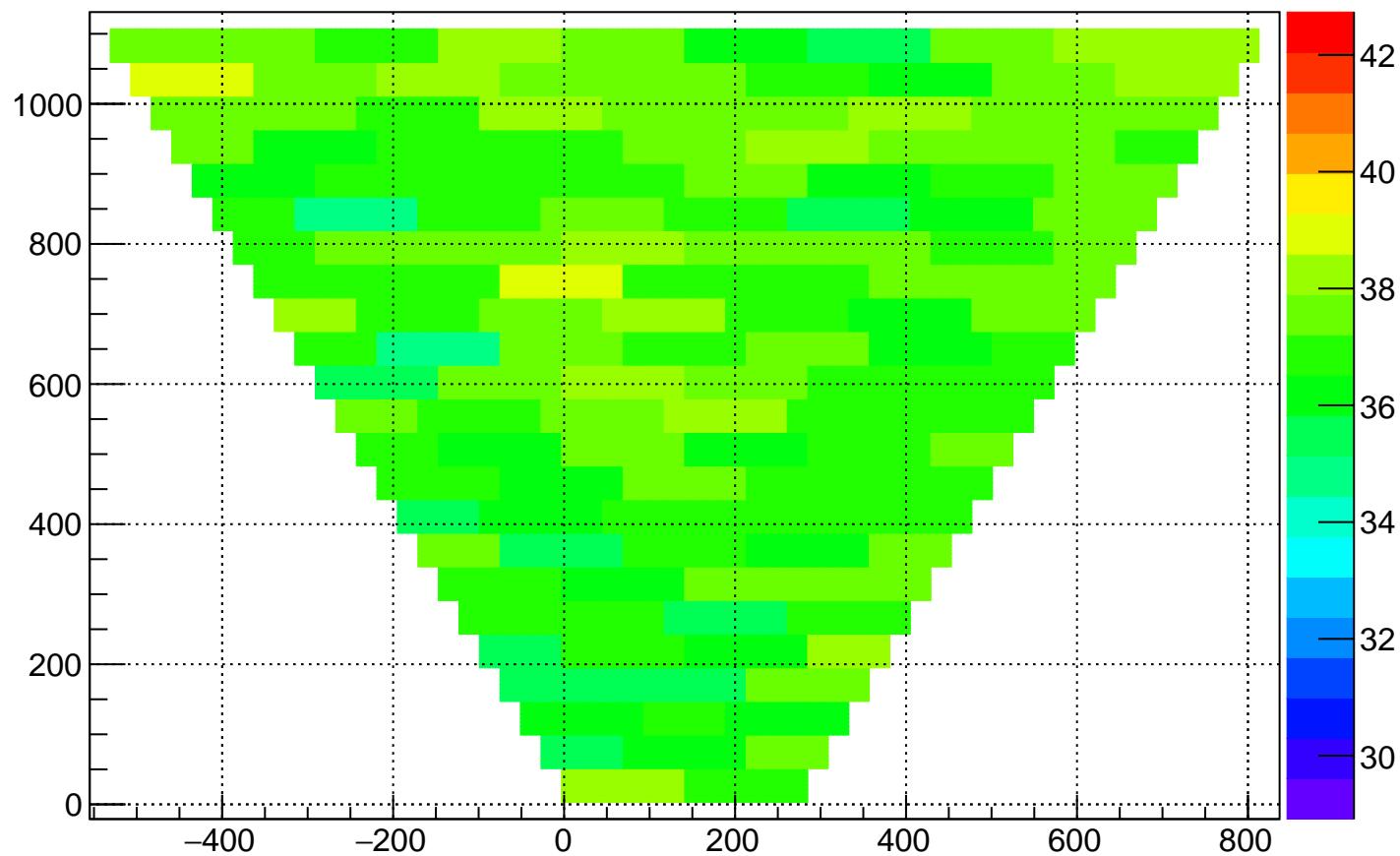
FPGA Temperature map, T= 35.0 sec



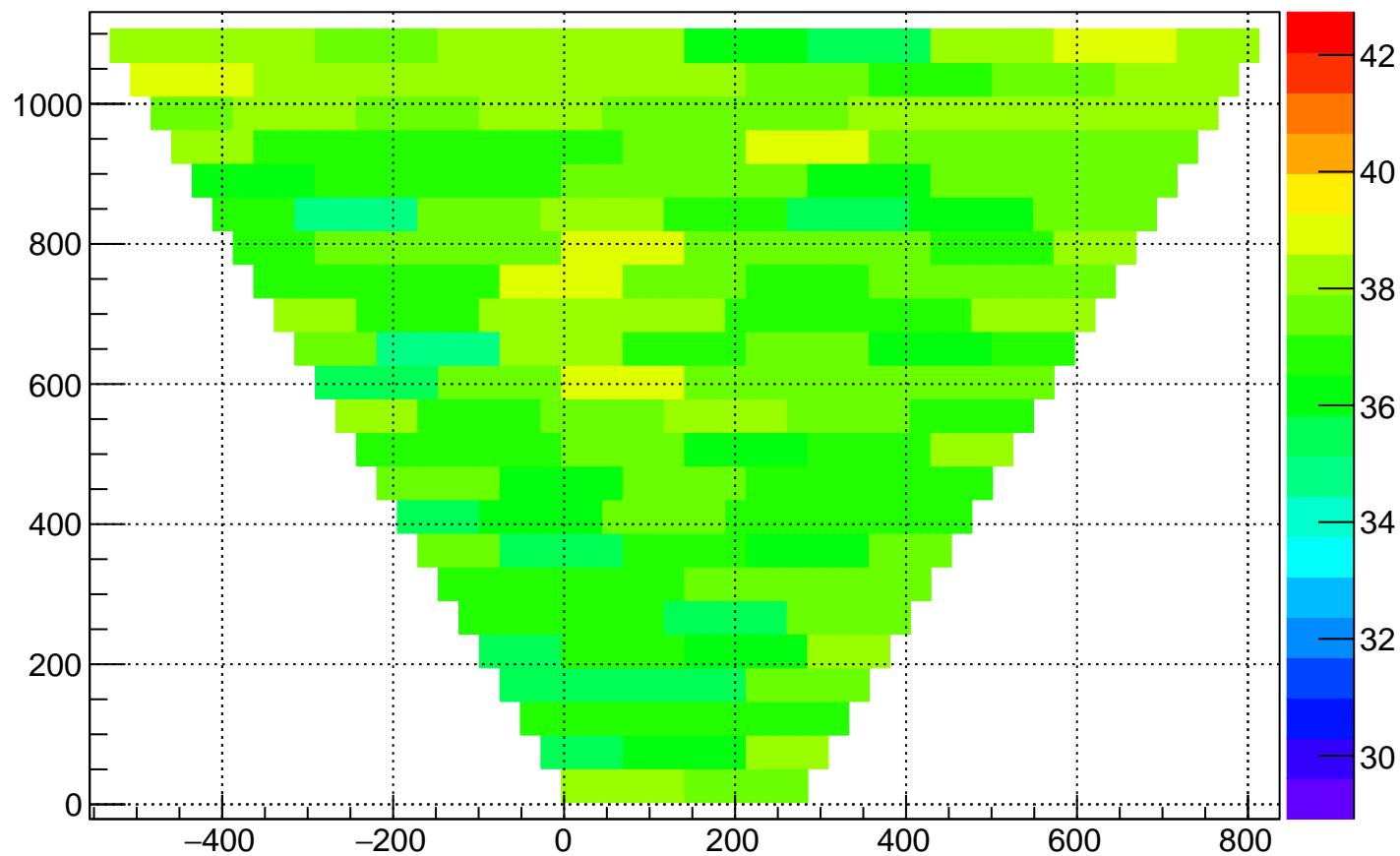
FPGA Temperature map, T= 40.0 sec



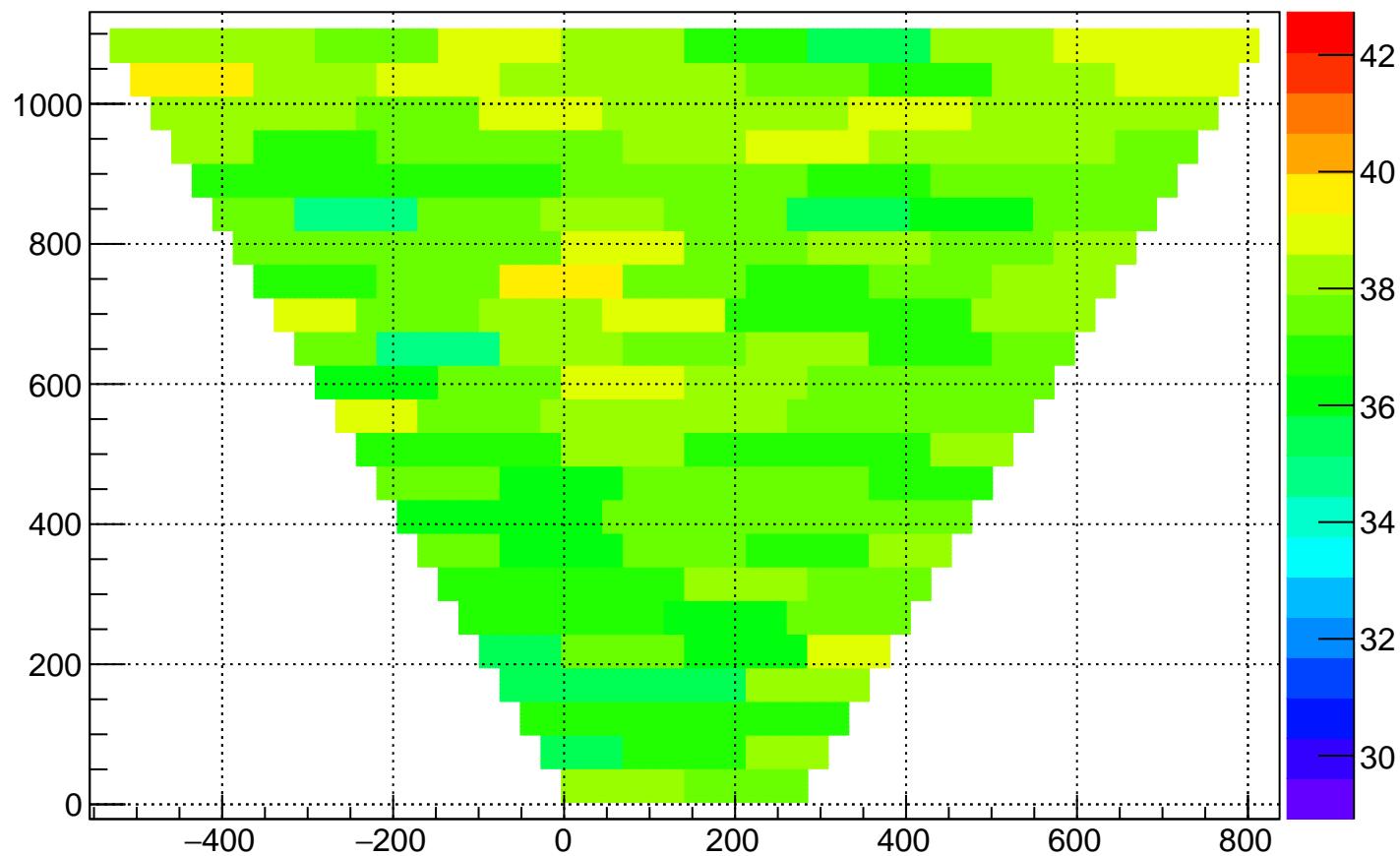
FPGA Temperature map, T= 45.0 sec



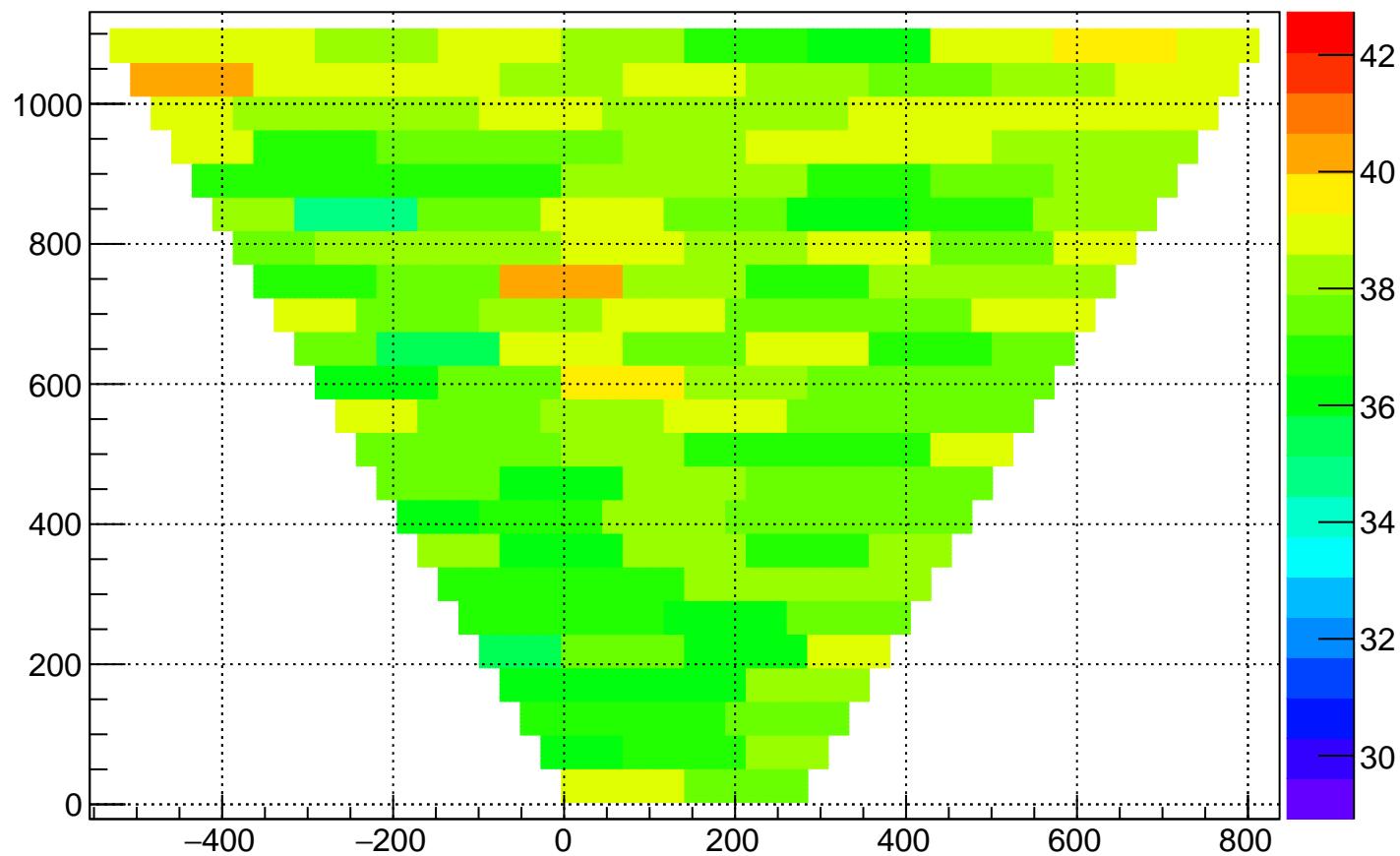
FPGA Temperature map, T= 50.0 sec



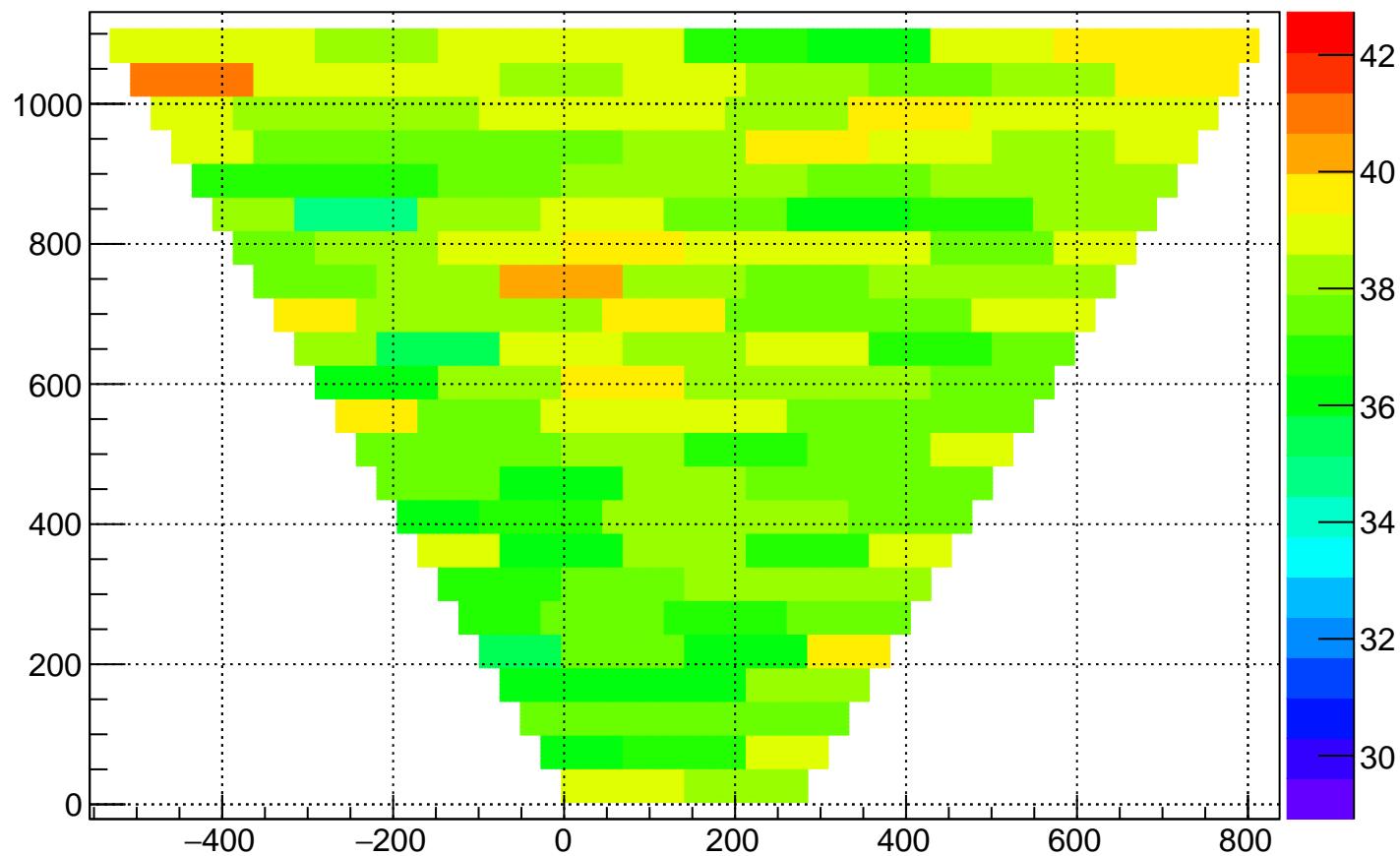
FPGA Temperature map, T= 55.0 sec



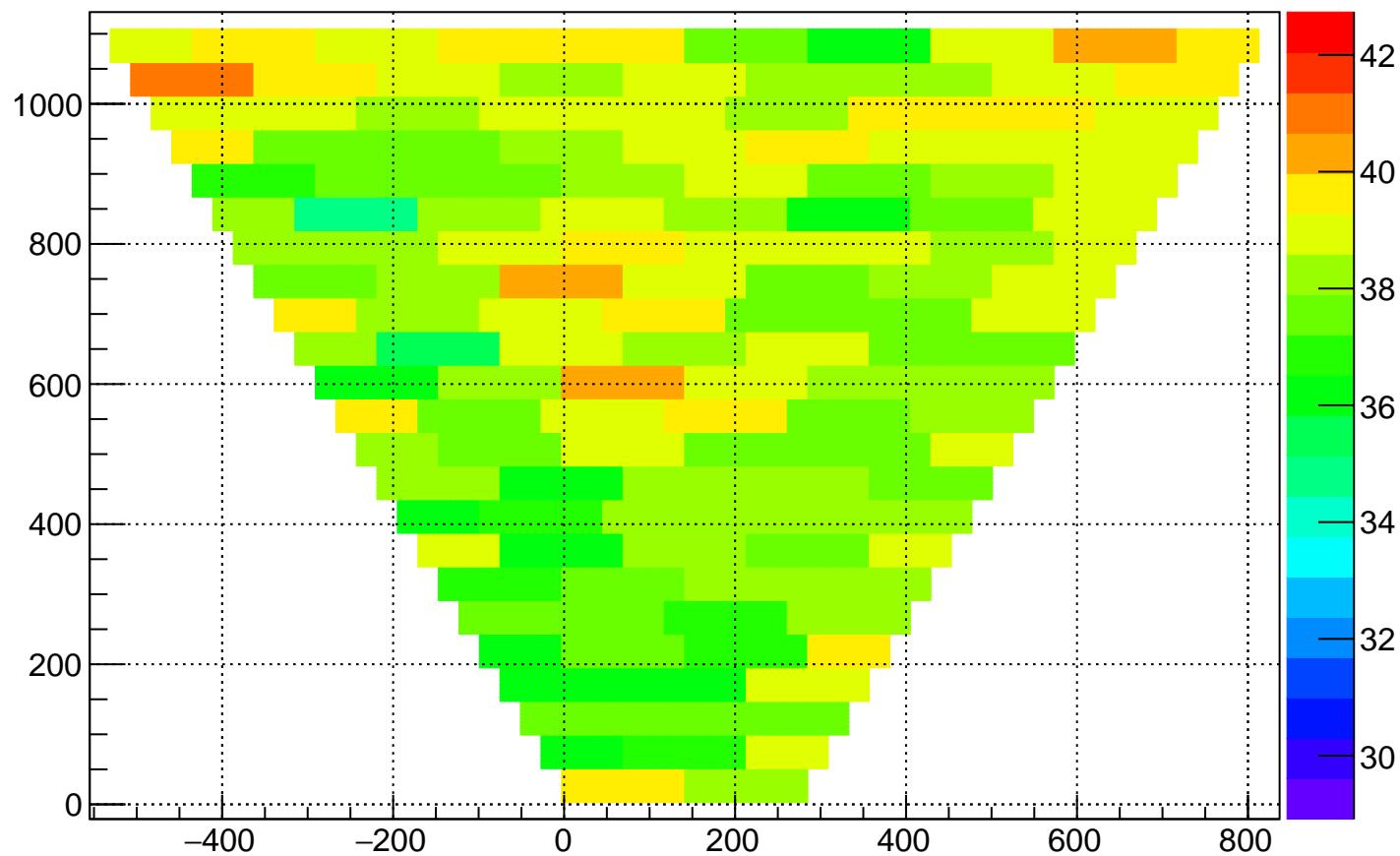
FPGA Temperature map, T= 60.0 sec



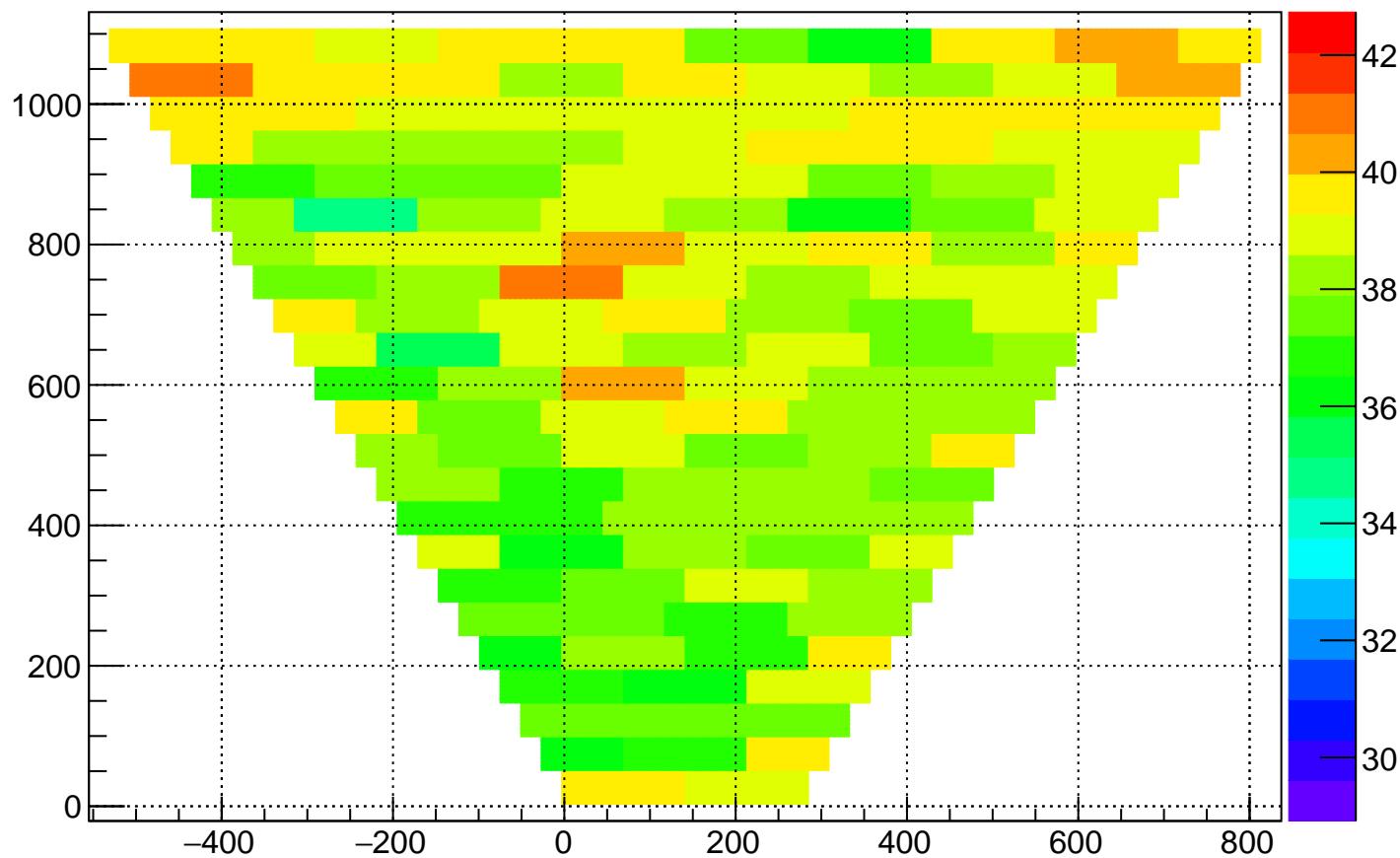
FPGA Temperature map, T= 65.0 sec



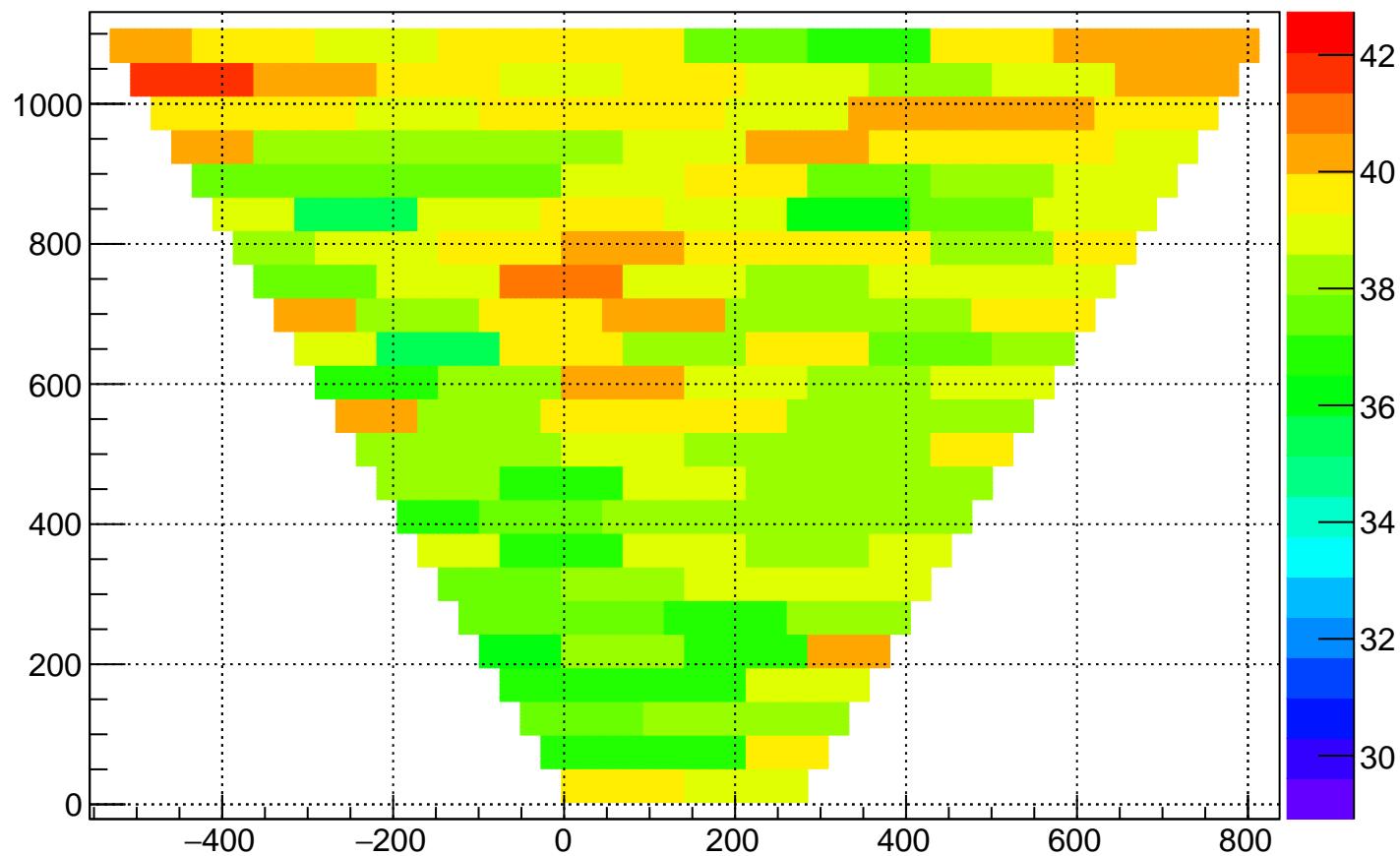
FPGA Temperature map, T= 70.0 sec



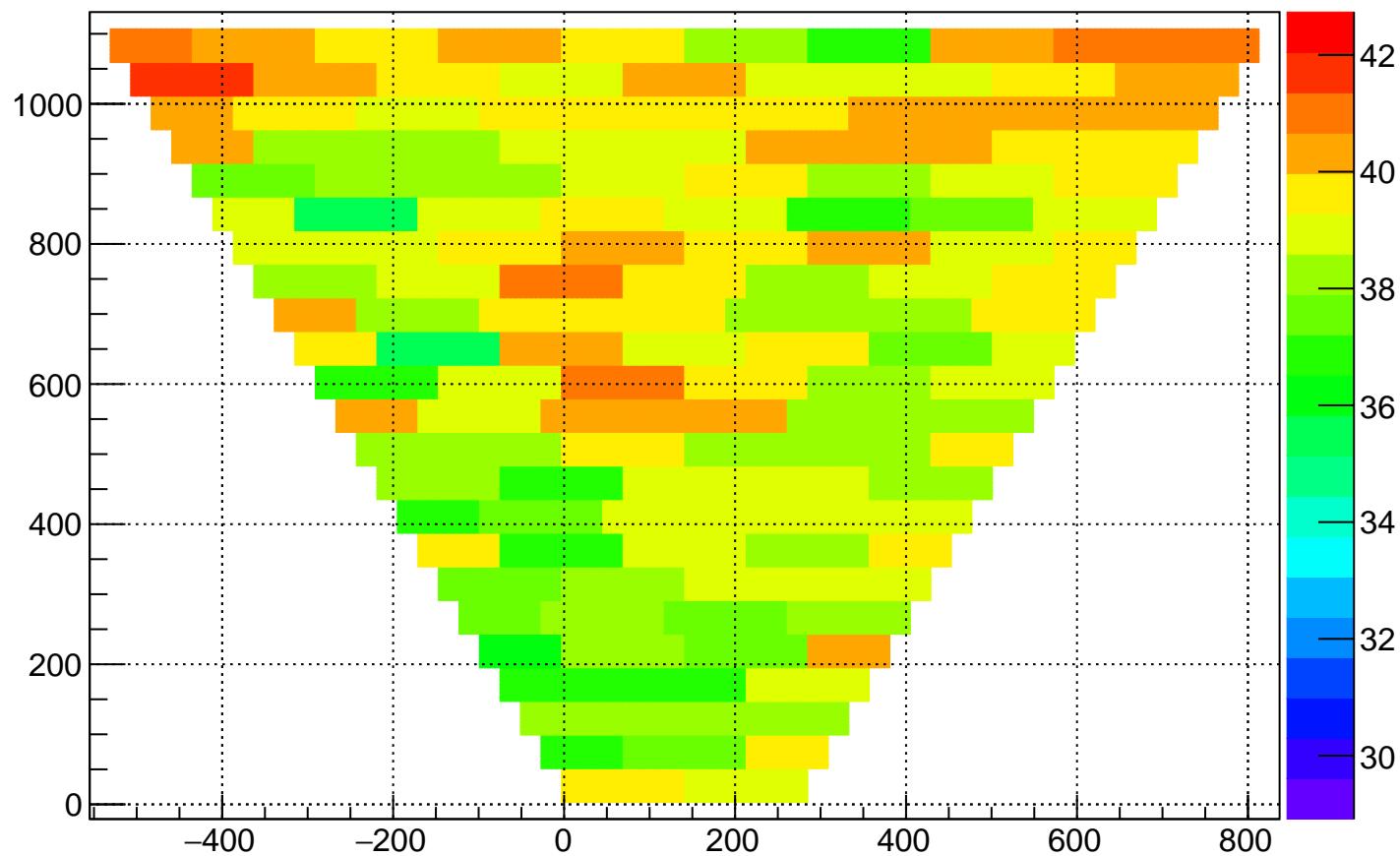
FPGA Temperature map, T= 75.0 sec



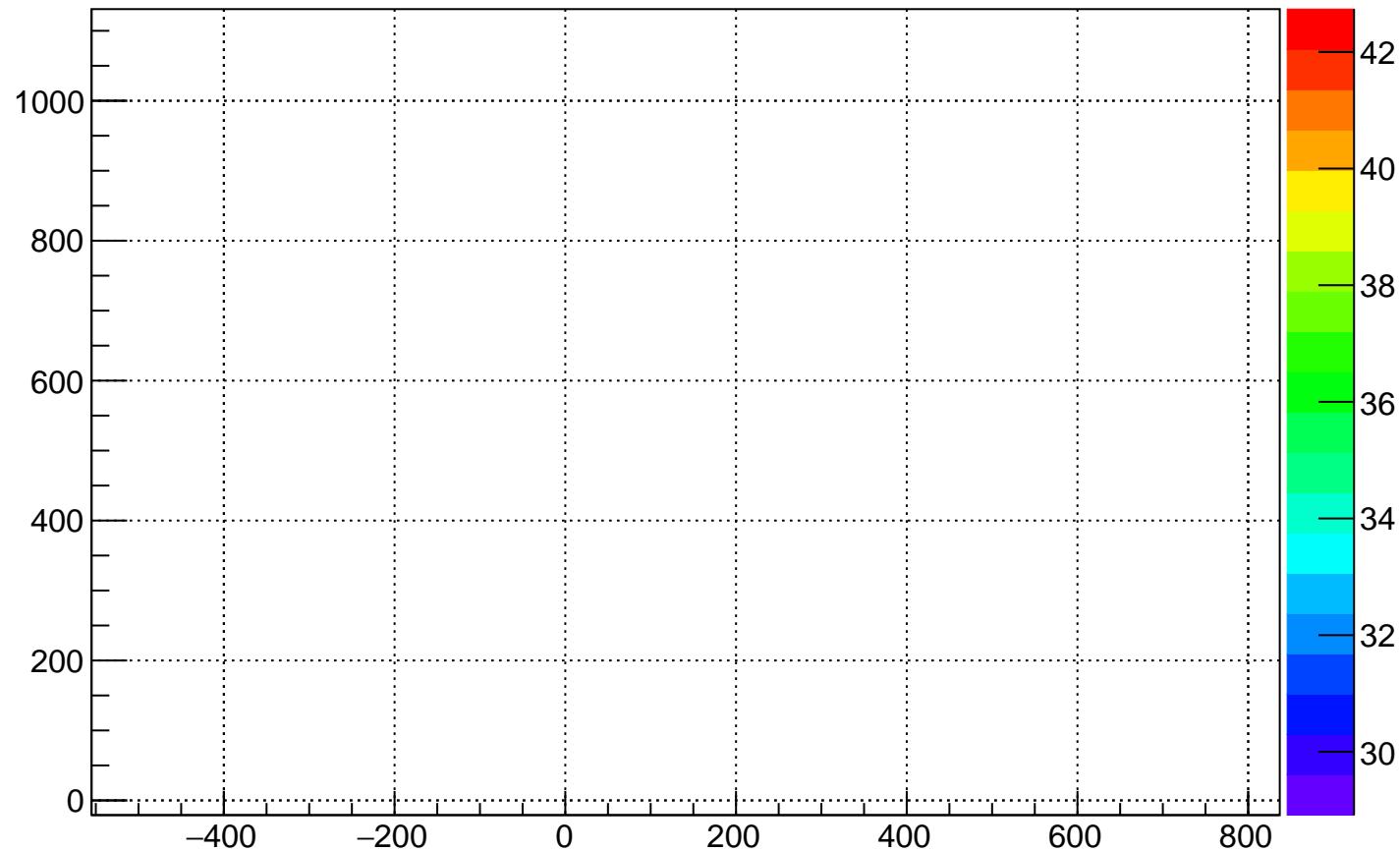
FPGA Temperature map, T= 80.0 sec



FPGA Temperature map, T= 85.0 sec



FPGA Temperature map, T= 90.0 sec



FPGA Temperature map, T= 95.0 sec

