Calculate_DI_Current - Ladder Diagram **Page 1** 9/26/2018 10:12:23 AM TEST_PROJECT:MainTask:Dipole Total number of rungs in routine: 9 C:\Users\campero\Desktop\TEST_PROJECT_v20_v1.ACD Calculates the current to be set in the MPS; formula used in this rung is based in the sample table provided (by Mike Fowler) from previous measuremnts in the Dipole. Formula can be modify as needed I_Cal_Enable Enable_Regulation Absolute Value Source DI_B_Target 1.56564 DI_I_ApproxTarget 1851.2543 Dest DI_B_Target_ABS 1.5772799 I_Cal_Enable Regulation_On DI_B_InLimits Compute Dest Absolute Value Source DI_B_Target
1.56564
Dest DI_B_Target_ABS
1.5772799 DI_I_ApproxTarget 1851.2543 Calculated value for current is sent to MPS. I_Cal_Enable Source DI_I_ApproxTarget
1851.2543
Dest MPS_Wa
1851 Enables the bolean only once to send the MPS_Wa value to the MPS. When MPS_current_send bit is enable in "MPS_Commands" routine (Currently running in HMS and SHMS) this send the command to the MPS When Enable_Regualtion is enable, the value for the current sent is based on setB+offsetB

When Disable_Regulation is enable, the vaule for the current sent is based only on set B MPS_current_send <Boolean_3[8]> Enable_Regulation Disable_Regaulation Readback current from MPS read at PLC analog input (Dipole:6:I.Ch13Data) is evaluated against upper/lower limits. This rung will help with the ocilations read from the I readback.

Limits can be modified as needed I_Cal_Enable MPS_In_Limits Multiply Source A DI_I_ApproxTarget 1851.2543 Limit Test (CIRC) Subtract Low Limit DI_I_Lower_Lim 1850.3286 Source A DI_I_ApproxTarget 1851.2543 Source A DI_I_ApproxTarget 1851.2543 DI_I_Lim_Cal 0.9256272 DI_I_Lim_Cal Test Source B 0.0005 Source B I_coarse 0.9256272 DI_I_Lim_Cal 0.9256272 DI_I_Upper_Lim 1852.1799 DI_I_Lower_Lim 1850.3286 High Limit DI_I_Upper_Lim 1852.1799 Dest Dest Dest MPS_Ramping MPS_In_Limits Waiting for MPS to settled down MPS_In_Limits Timer On Delay Timer Preset 10000 **DN** Accum 10000 Checks if MPS is at desired set current t3.DN MPS_Ramping MPS_at_SetPoint MPS_at_SetPoint Check status of the MPS. PSU_Ready is bit status read from MPS as digital input signal in PLC (Dipole:1:I.Data.14). MPS_Ramping MPS_Set PSU_Ready t3.DN

MPS_Set

(End)