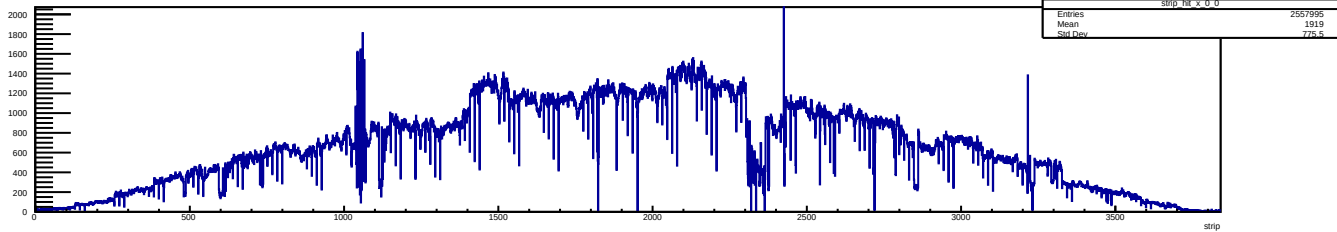
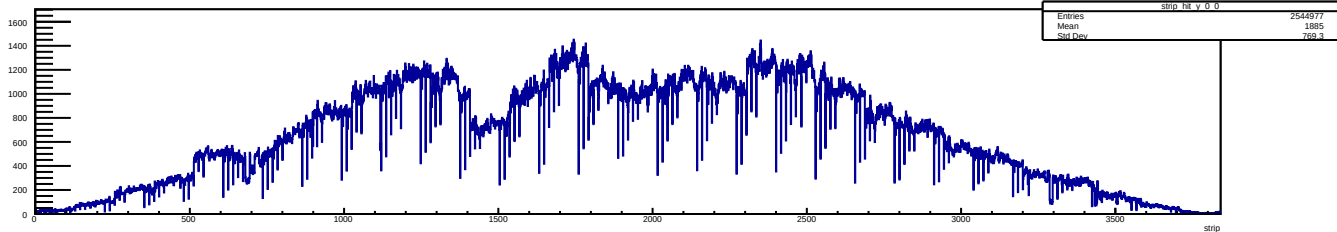


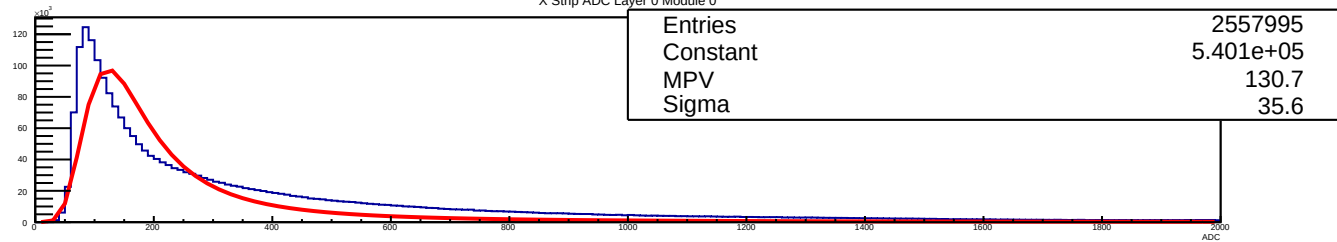
X Strip Pos Layer 0 Module 0



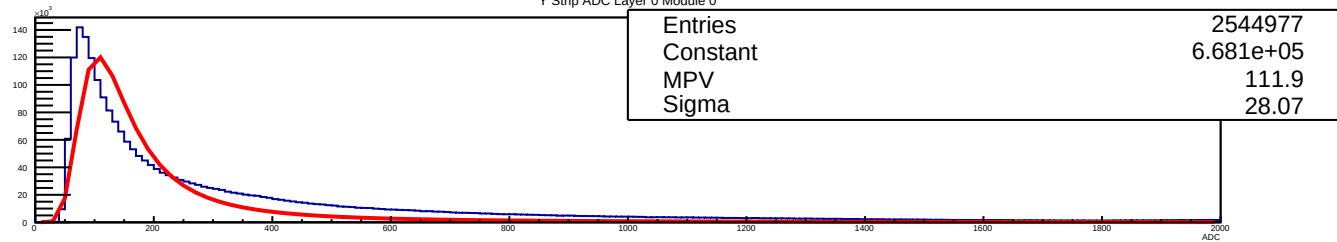
Y Strip Pos Layer 0 Module 0

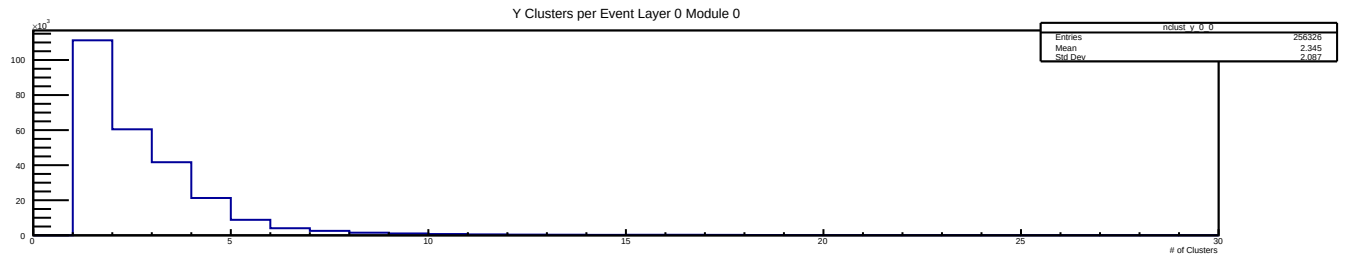
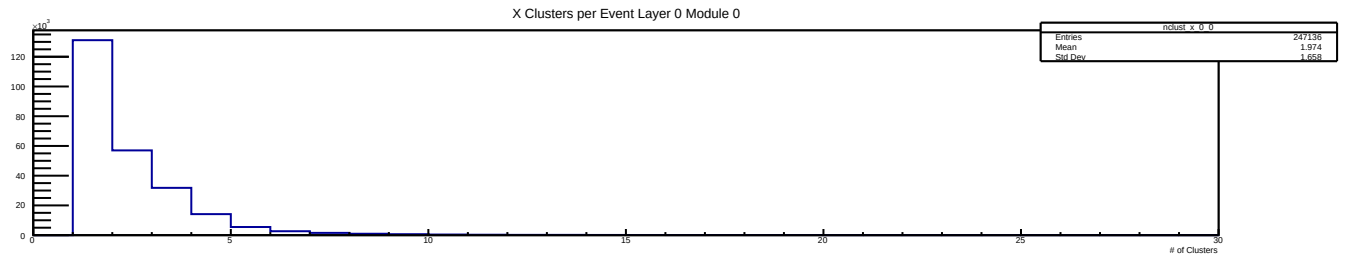
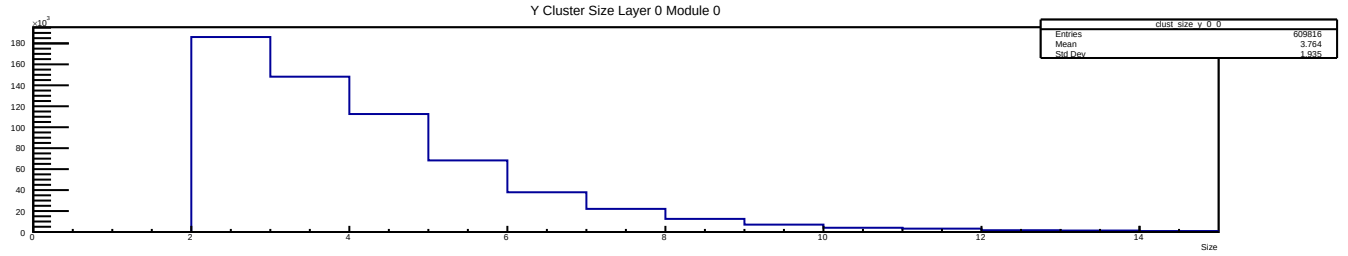
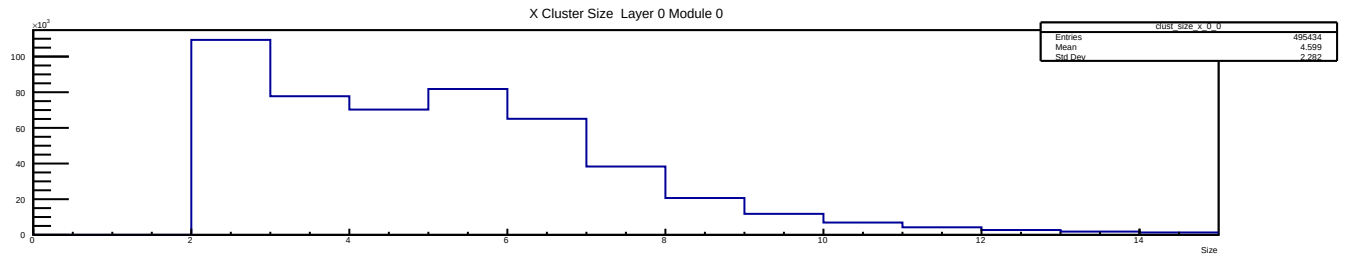


X Strip ADC Layer 0 Module 0

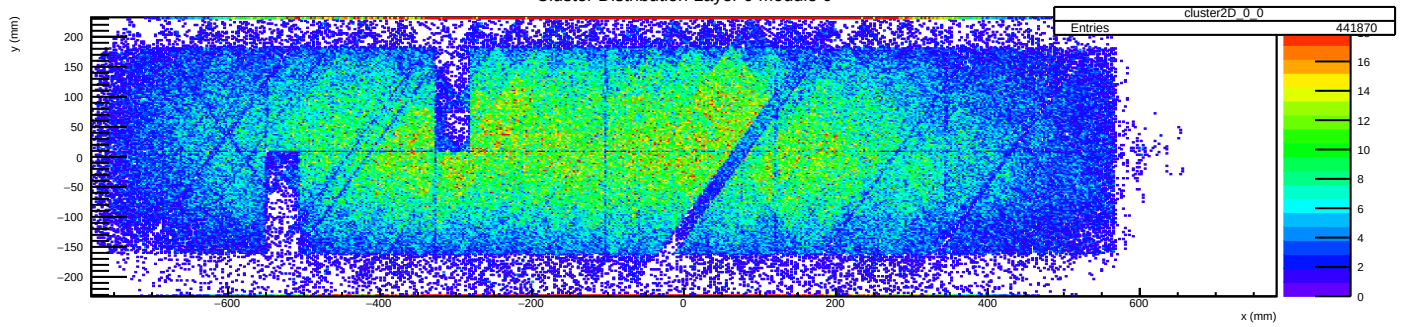


Y Strip ADC Layer 0 Module 0

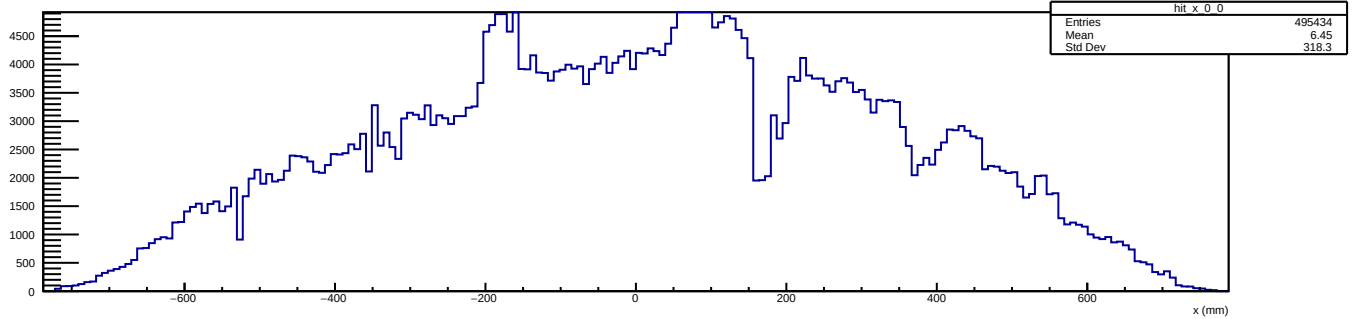




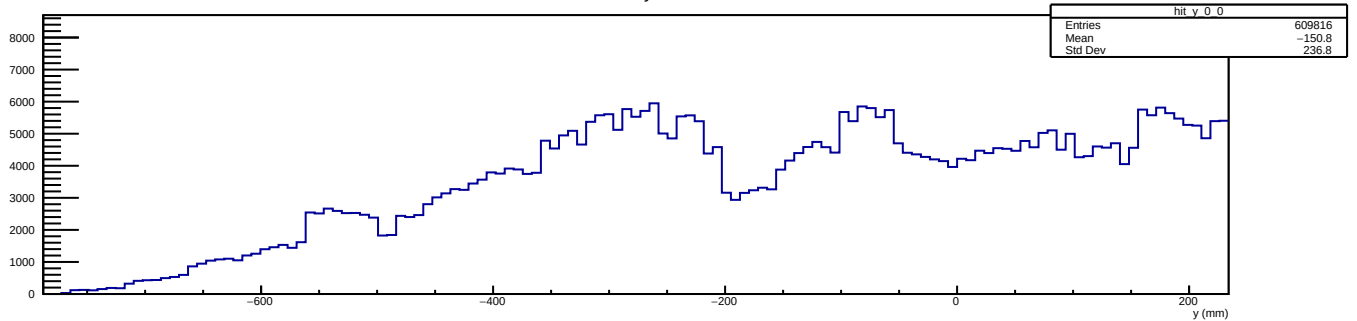
Cluster Distribution Layer 0 Module 0



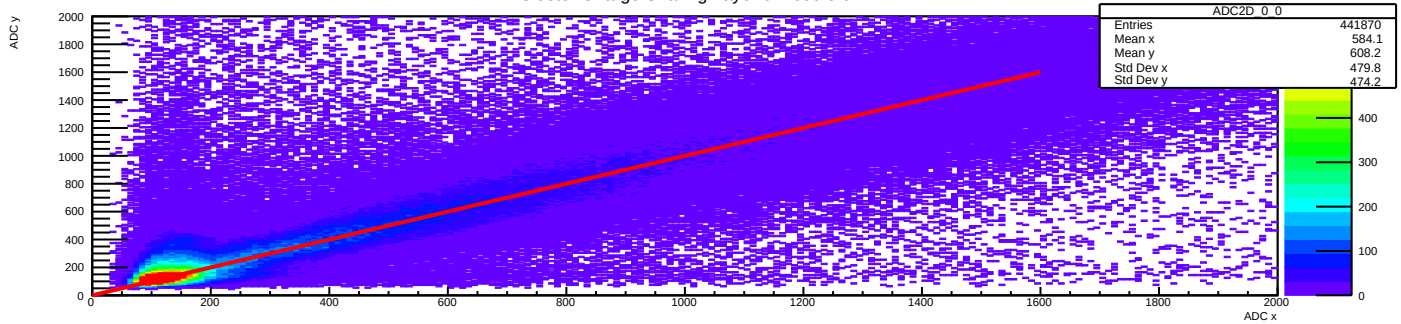
X Cluster Pos Layer 0 Module 0



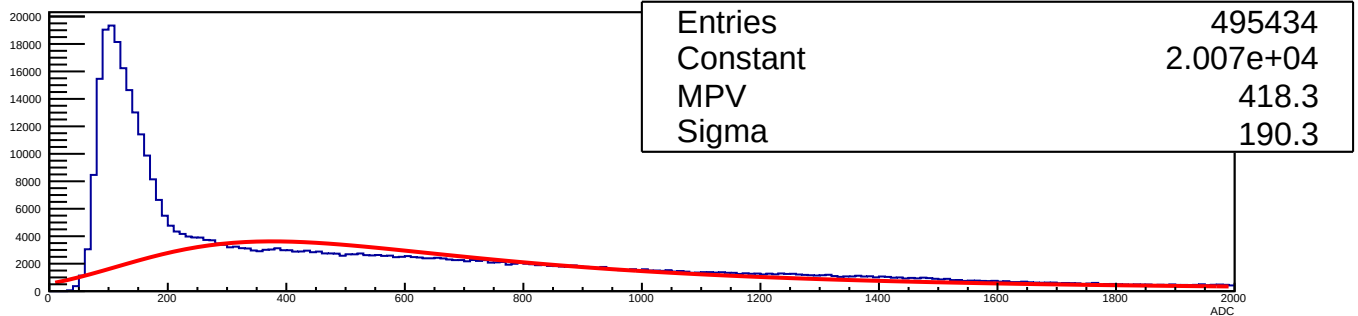
Y Cluster Pos Layer 0 Module 0



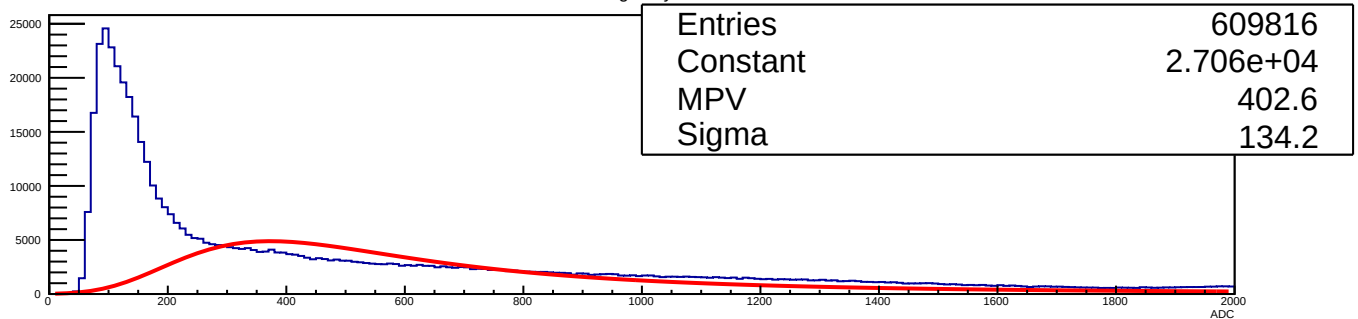
Cluster Charge Sharing Layer 0 Module 0



X Cluster Charge Layer 0 Module 0



Y Cluster Charge Layer 0 Module 0



Cluster Distribution Layer 0

