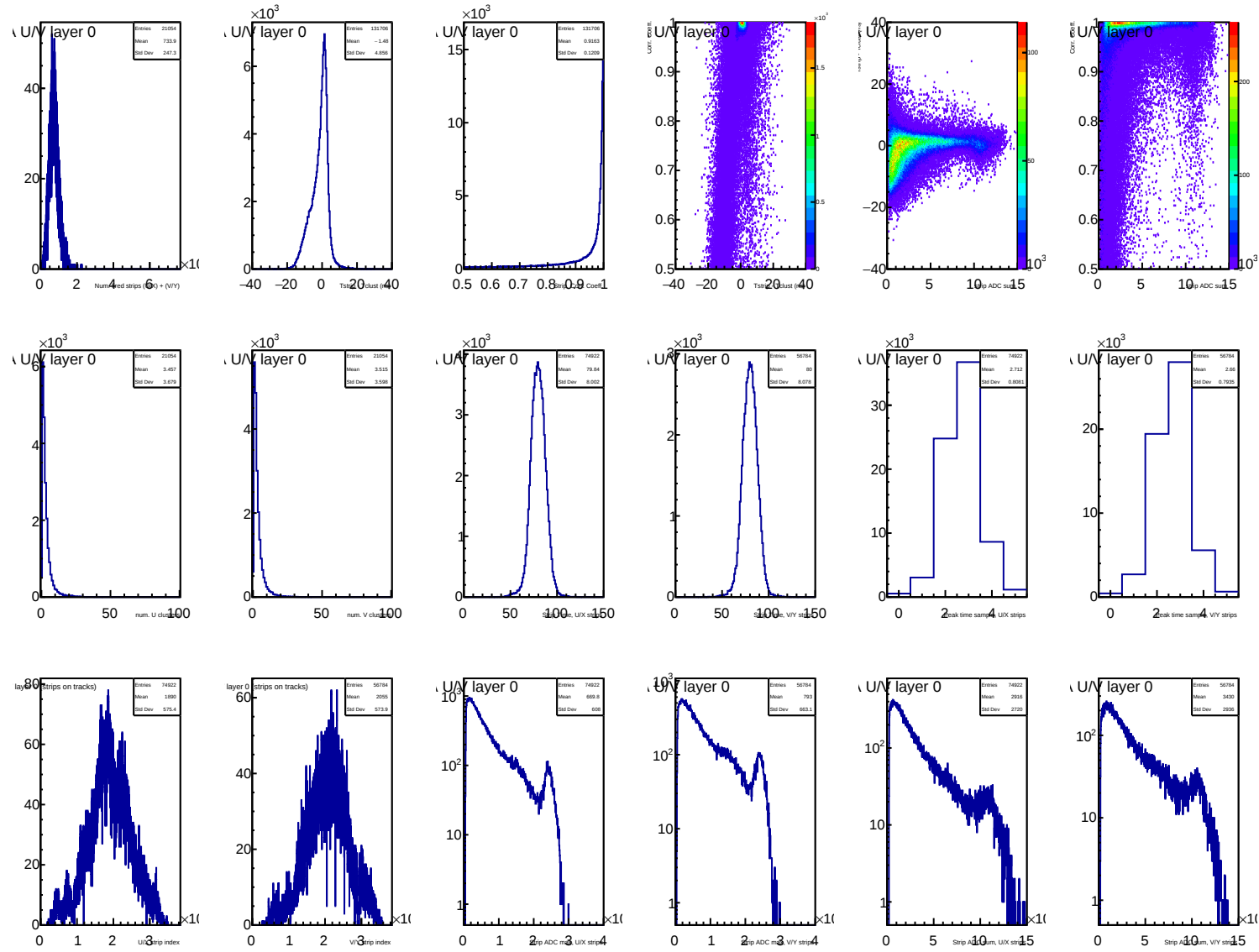
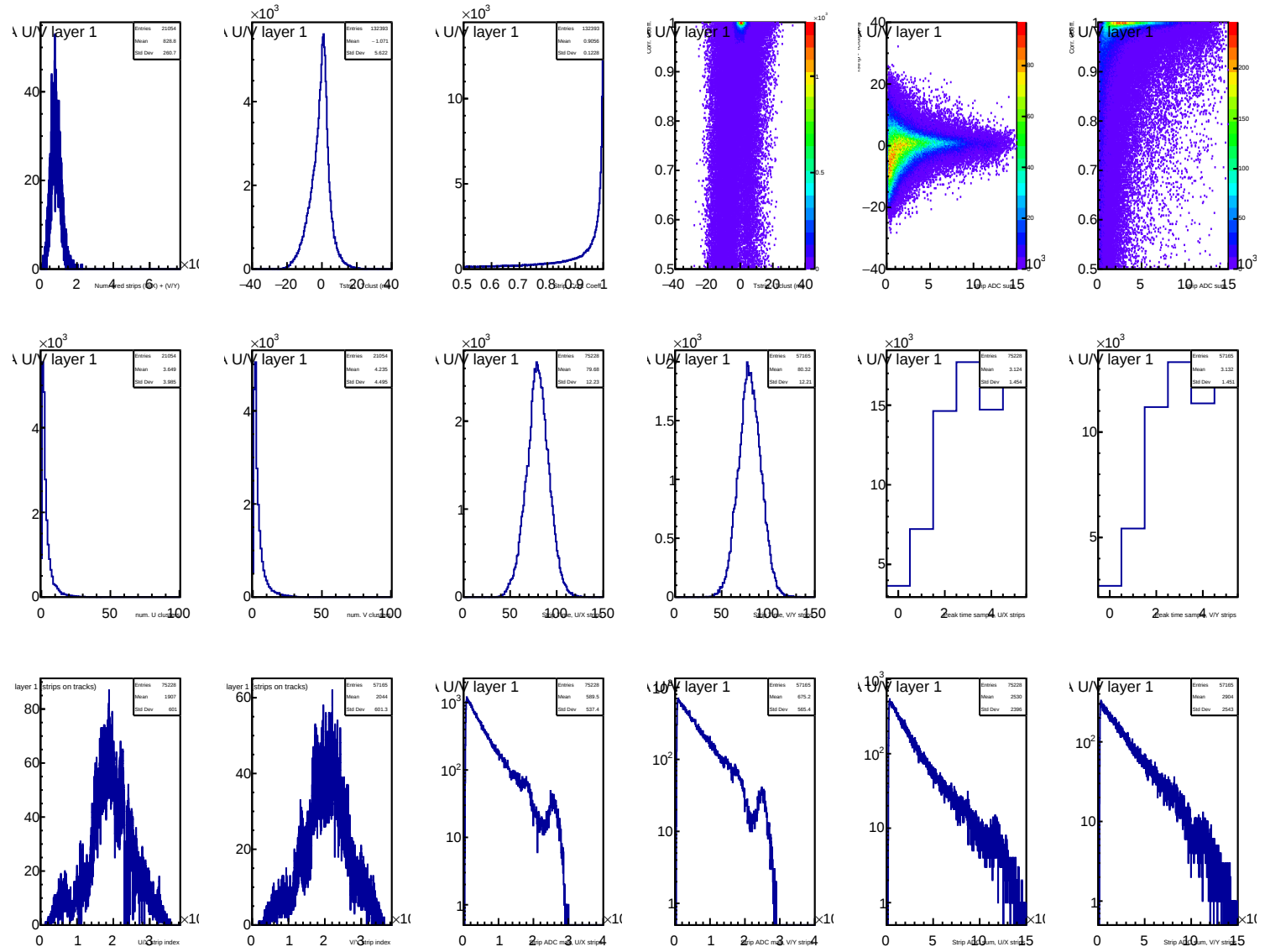


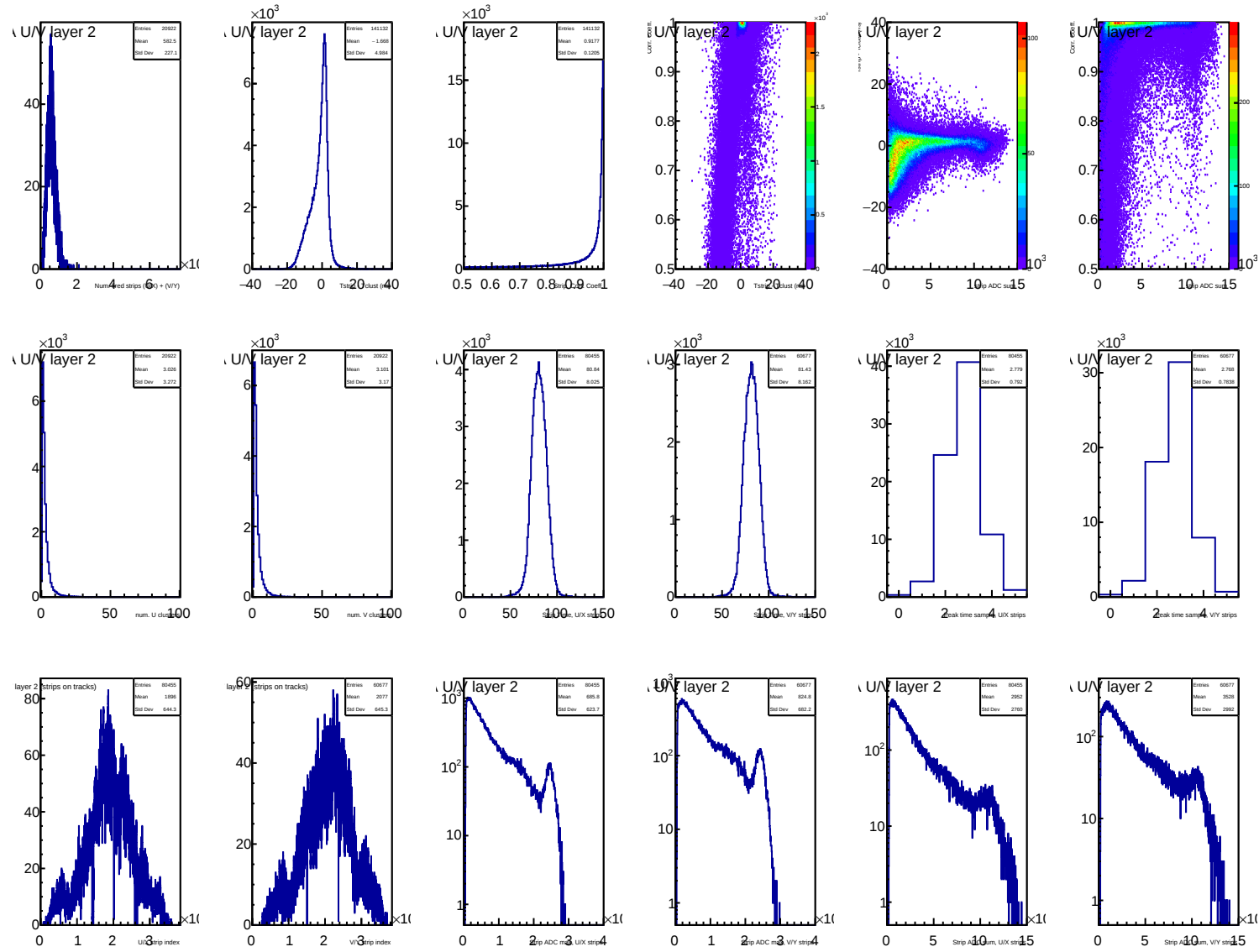
Summary Plots(Run #13240) 0: UVA U/V layer 0 low-level (with track cuts)



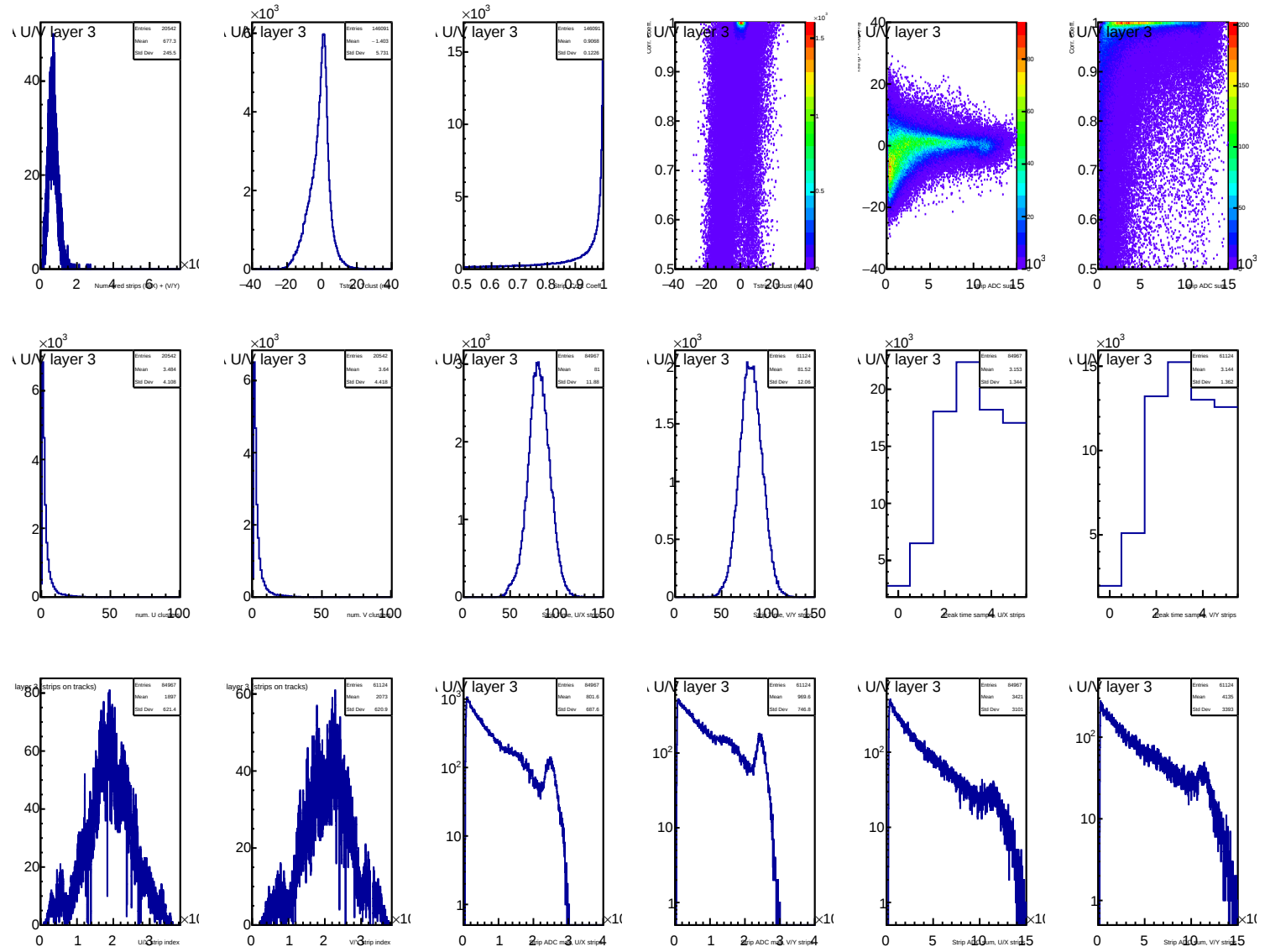
Summary Plots (Run #13240) 1: UVA U/V layer 1 (with track cuts)



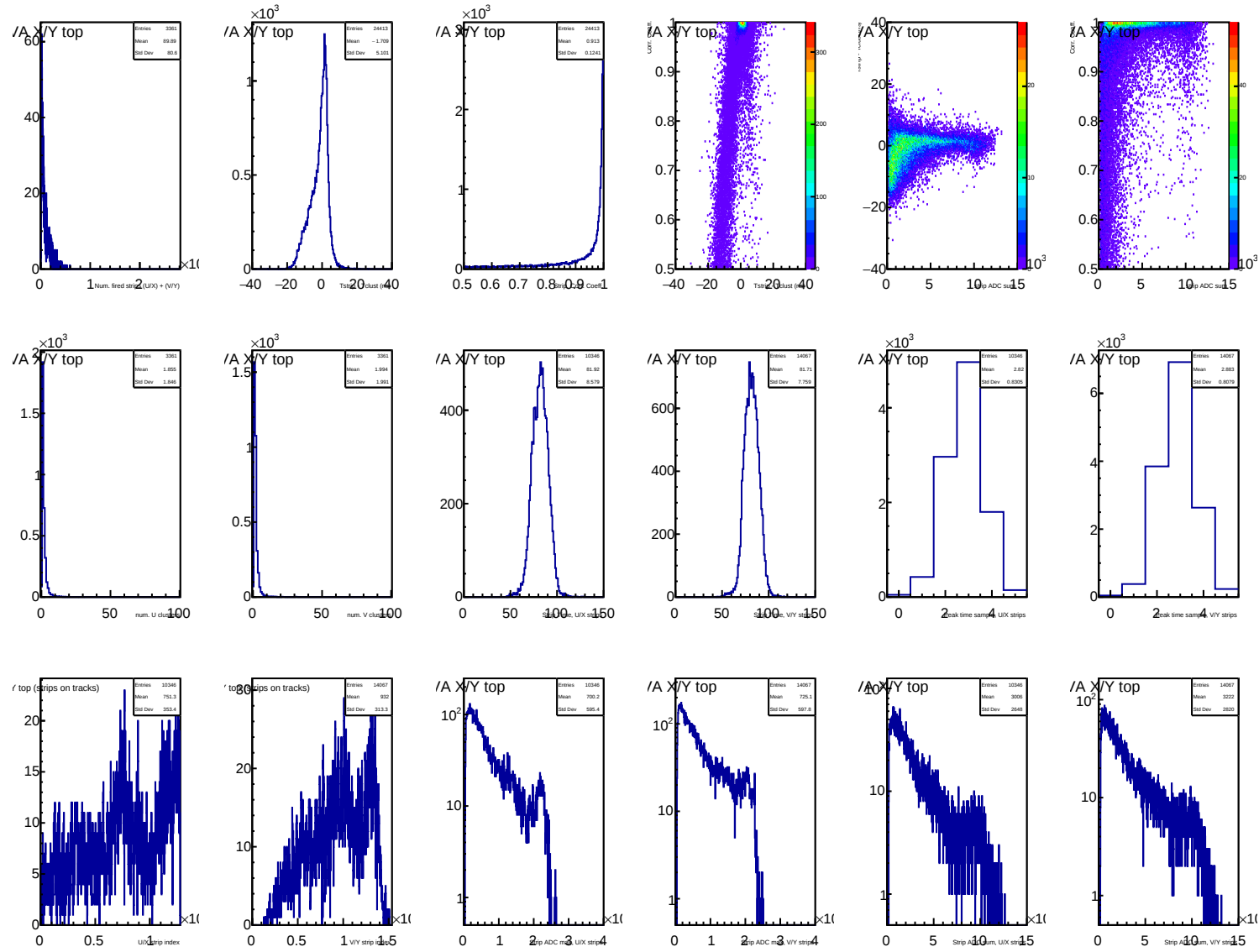
Summary Plots(Run #13240) 2: UVA U/V layer 2 low-level (with track cuts)



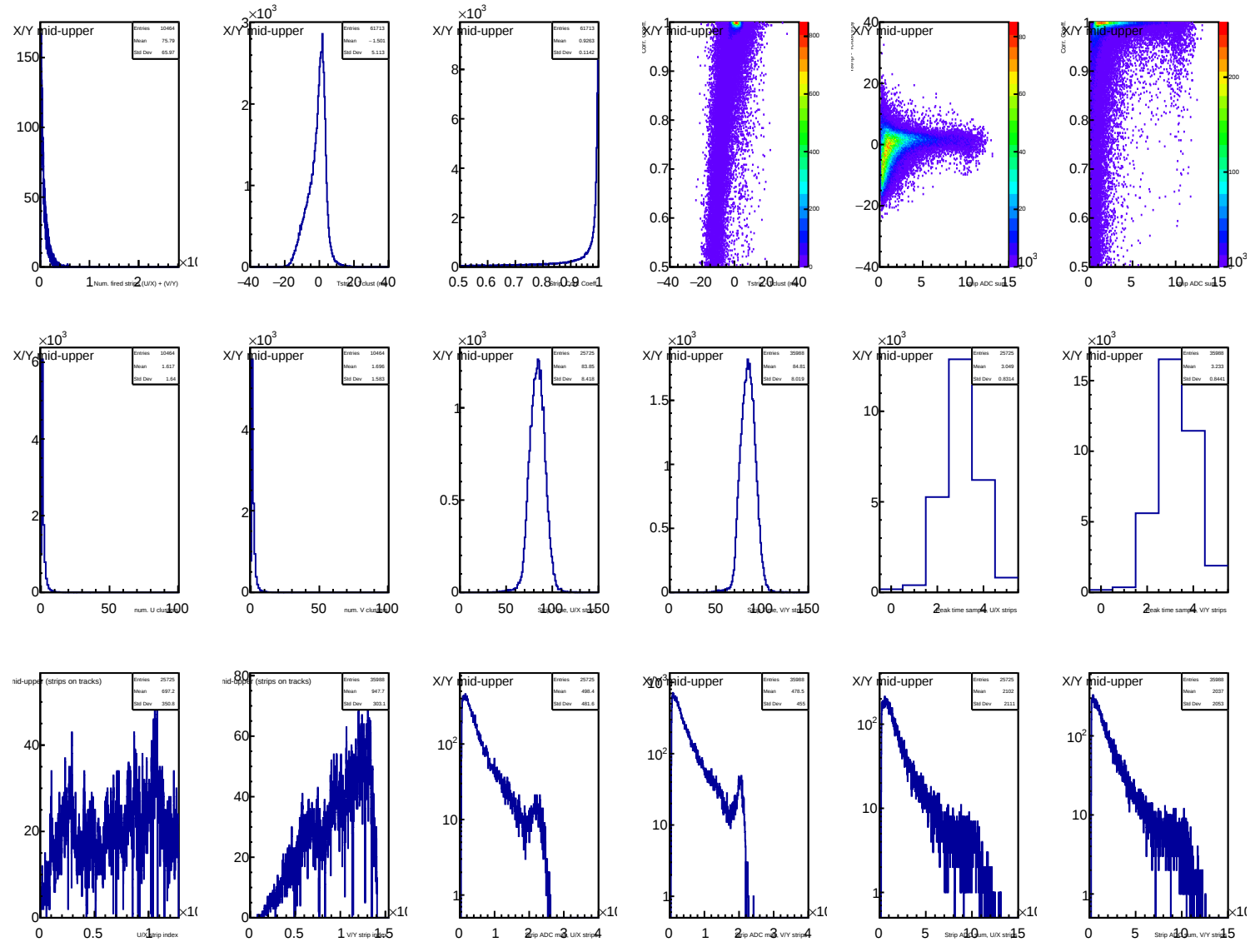
Summary Plots (Run #13240) 3: UVA U/V layer 3 low-level (with track cuts)



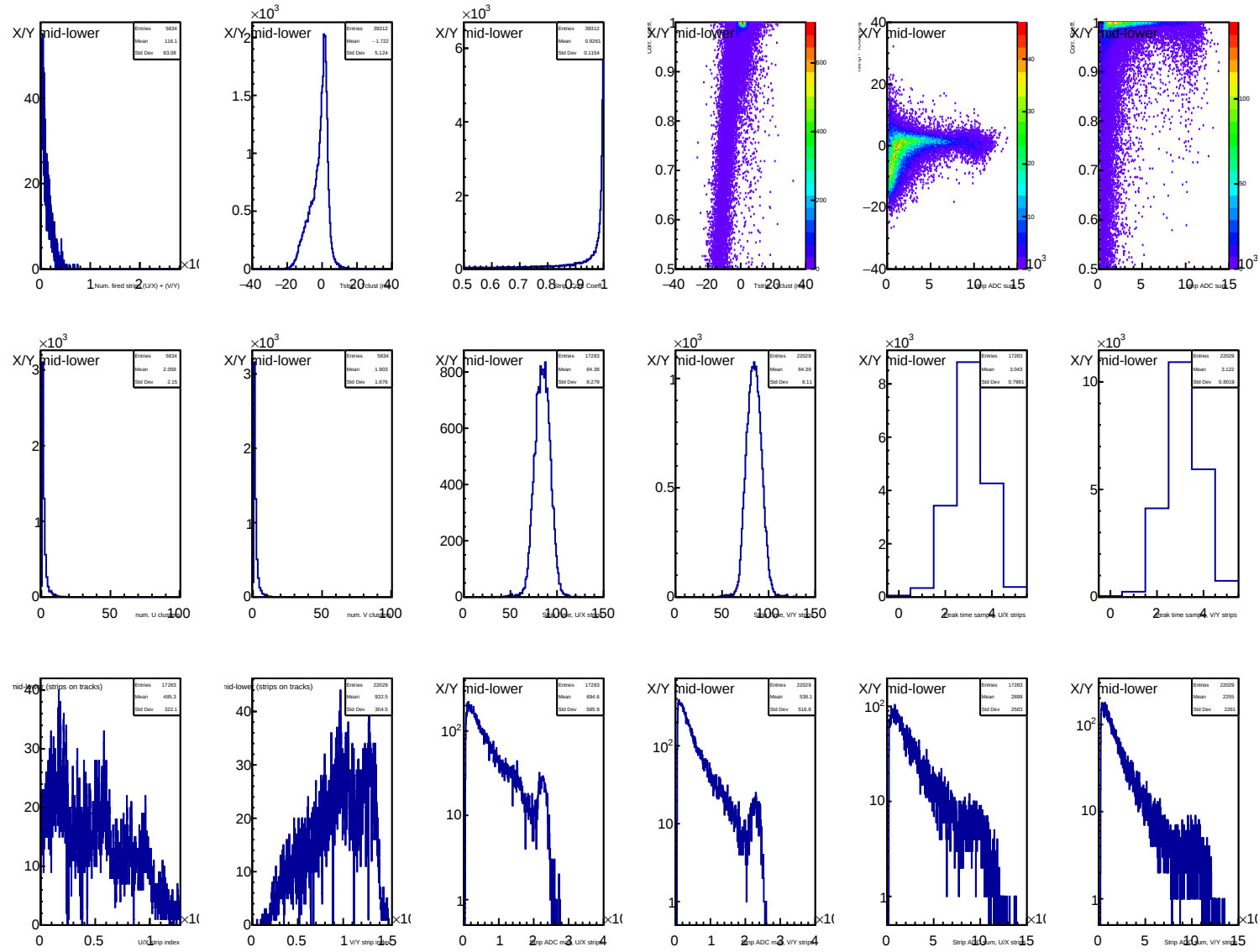
Summary Plots (Run #13240) 4: UVA X/Y top low-level (with track cuts)



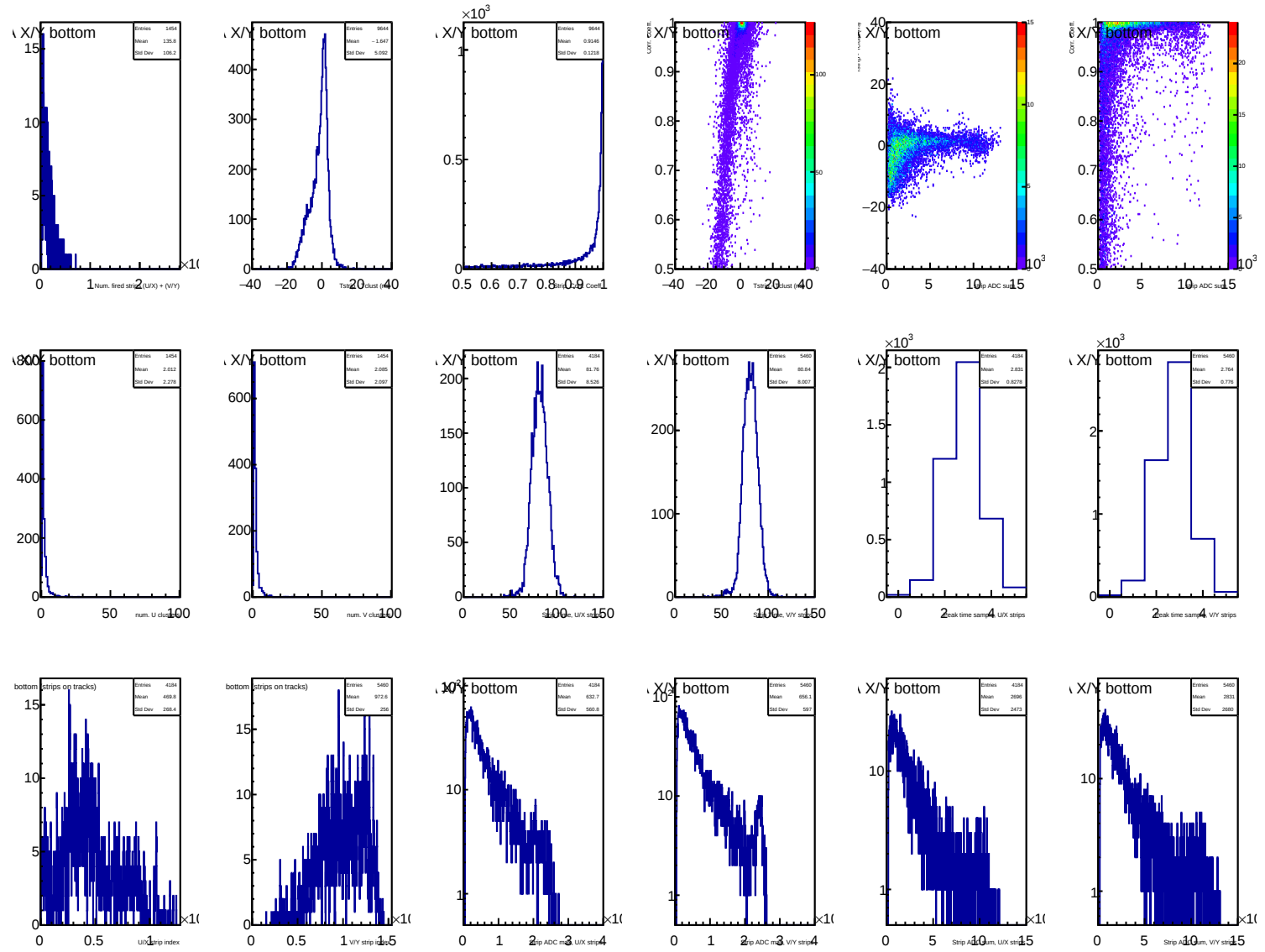
Summary Plots (Run #13240) 5: UVA X/Y mid-upper (with track cuts)



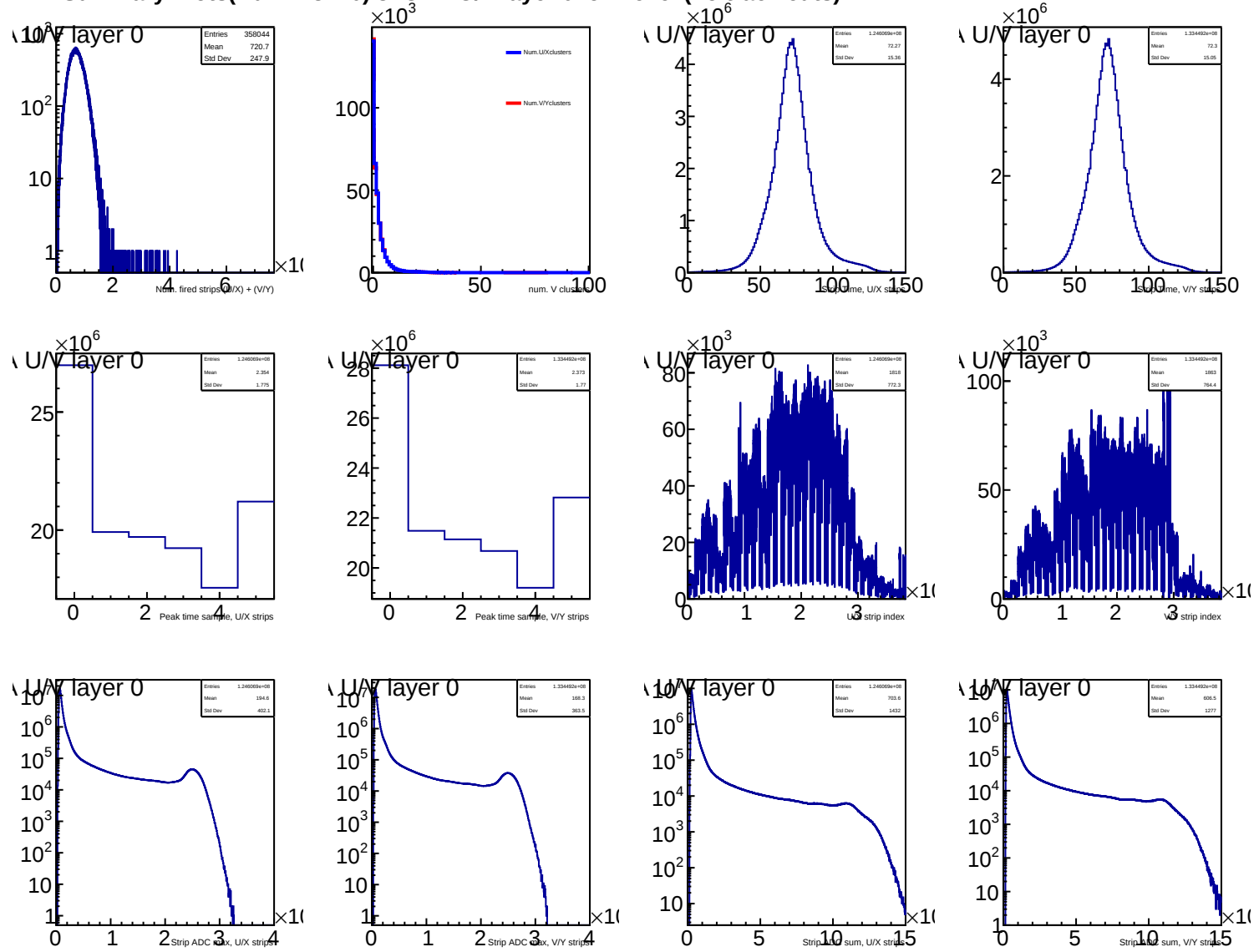
Summary Plots(Run #13240) 6: UVA X/Y mid-low level (with track cuts)



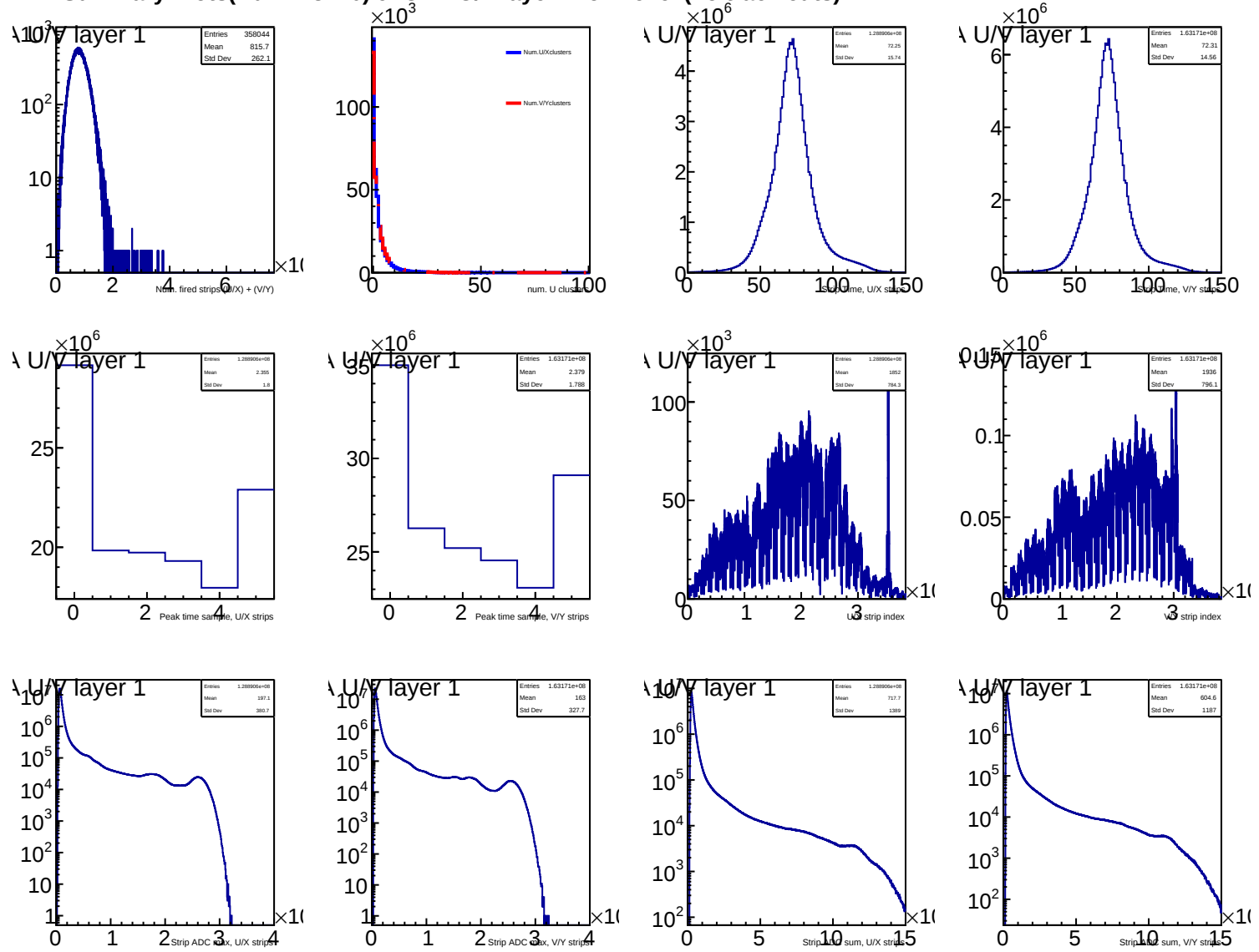
Summary Plots (Run #13240) 7: UVA X/Y bottom low-level (with track cuts)



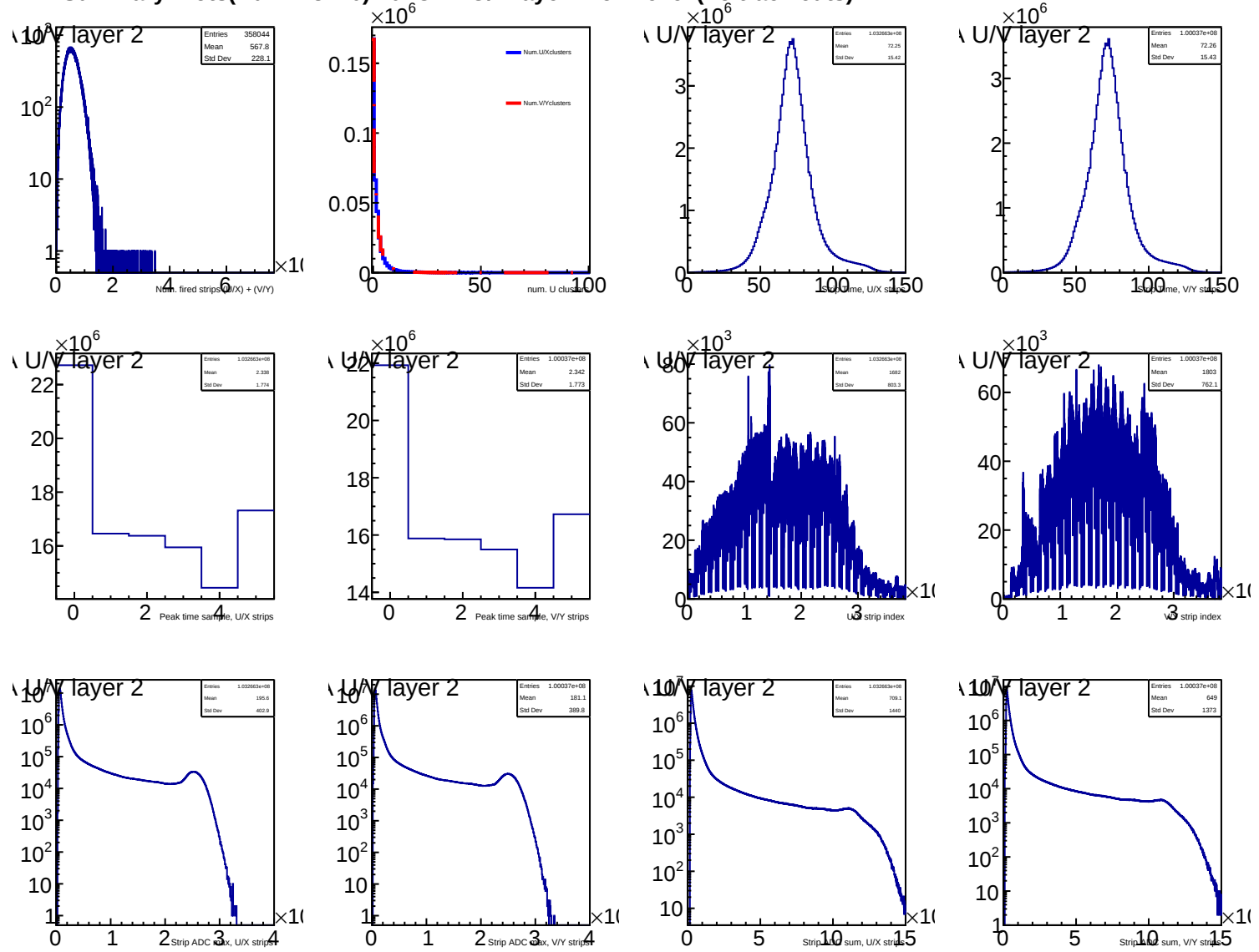
Summary Plots(Run #13240) 8: UVA U/V layer 0 low-level (no track cuts)



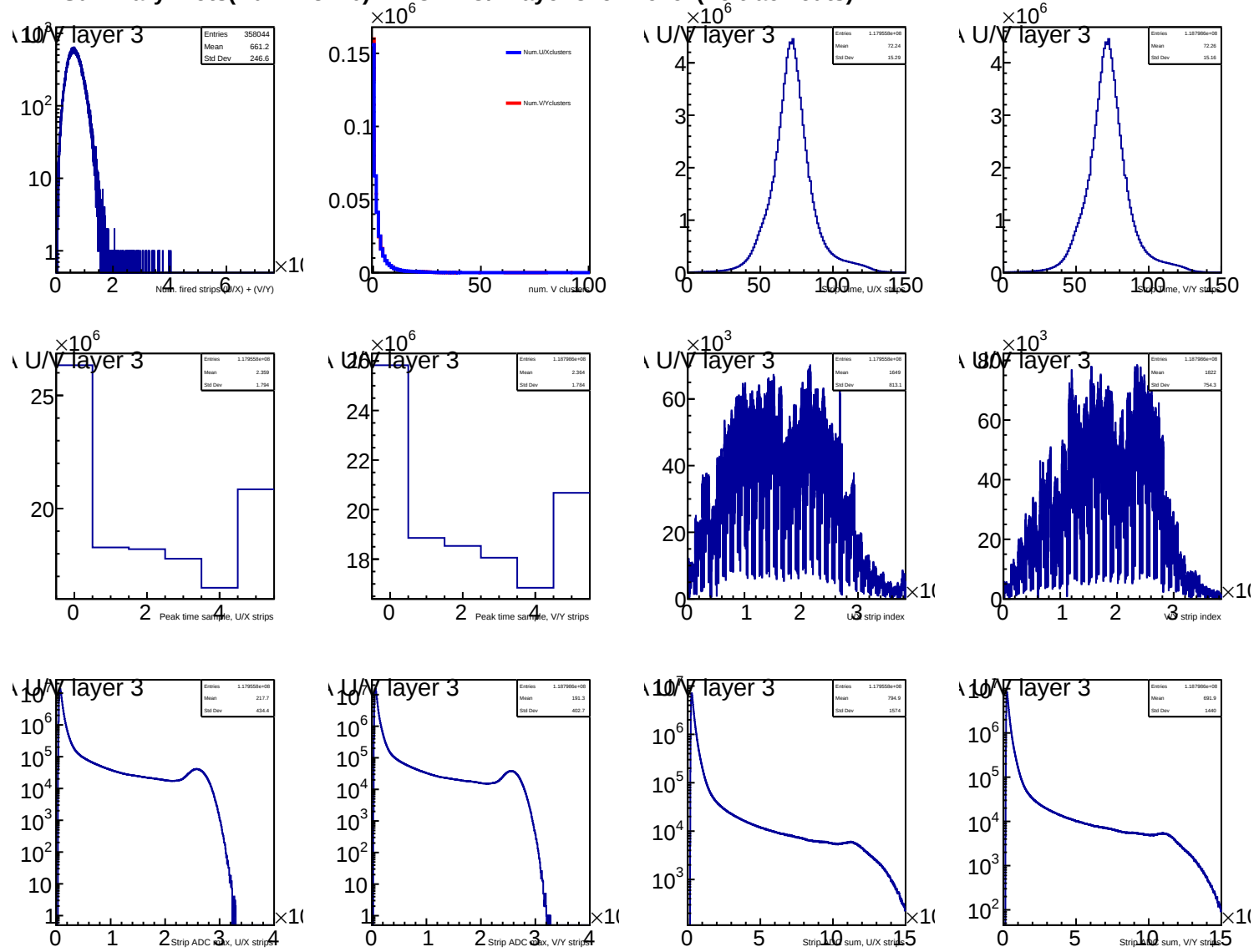
Summary Plots(Run #13240) 9: UVA U/V layer 1 low-level (no track cuts)



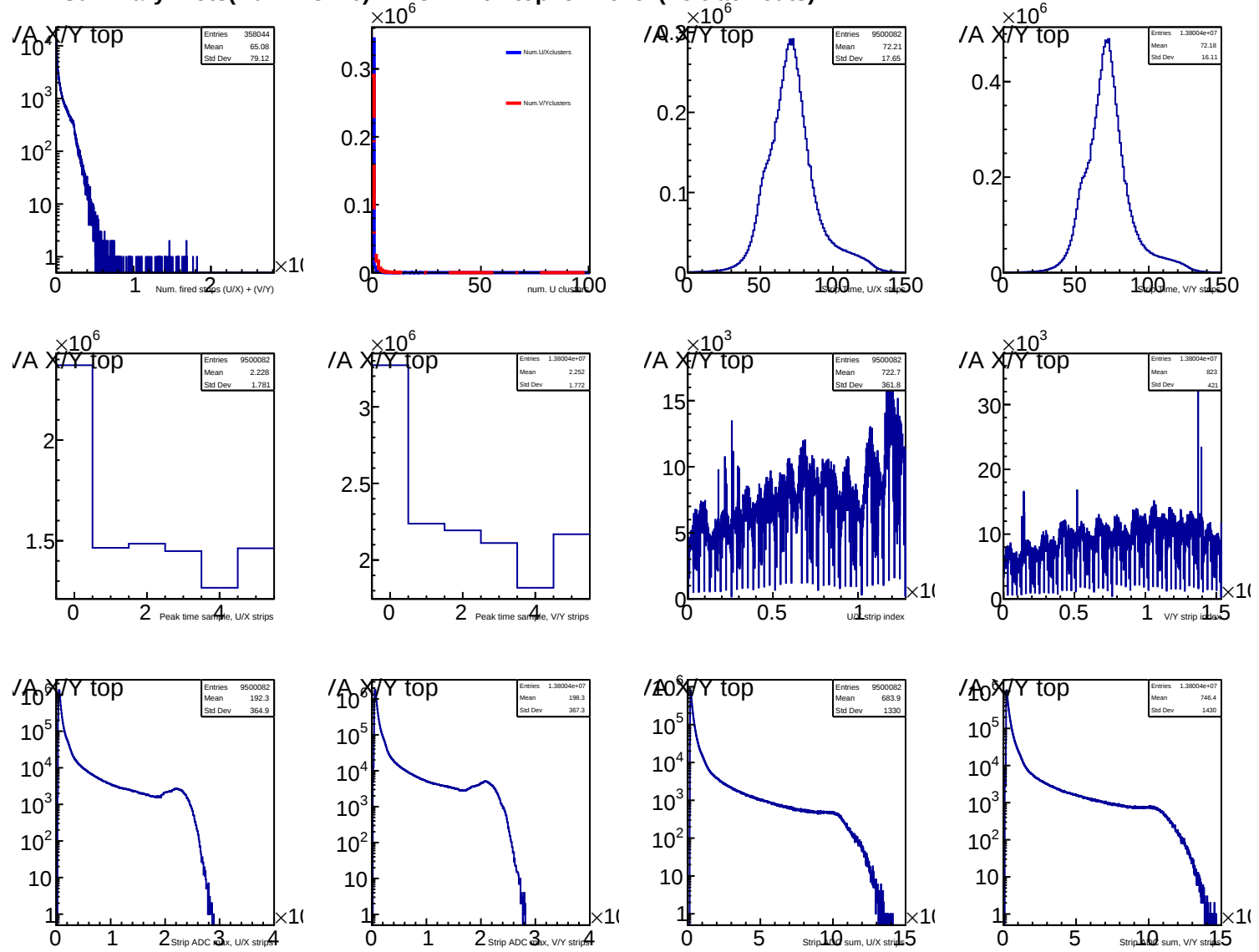
Summary Plots(Run #13240) 10: UVA U/V layer 2 low-level (no track cuts)



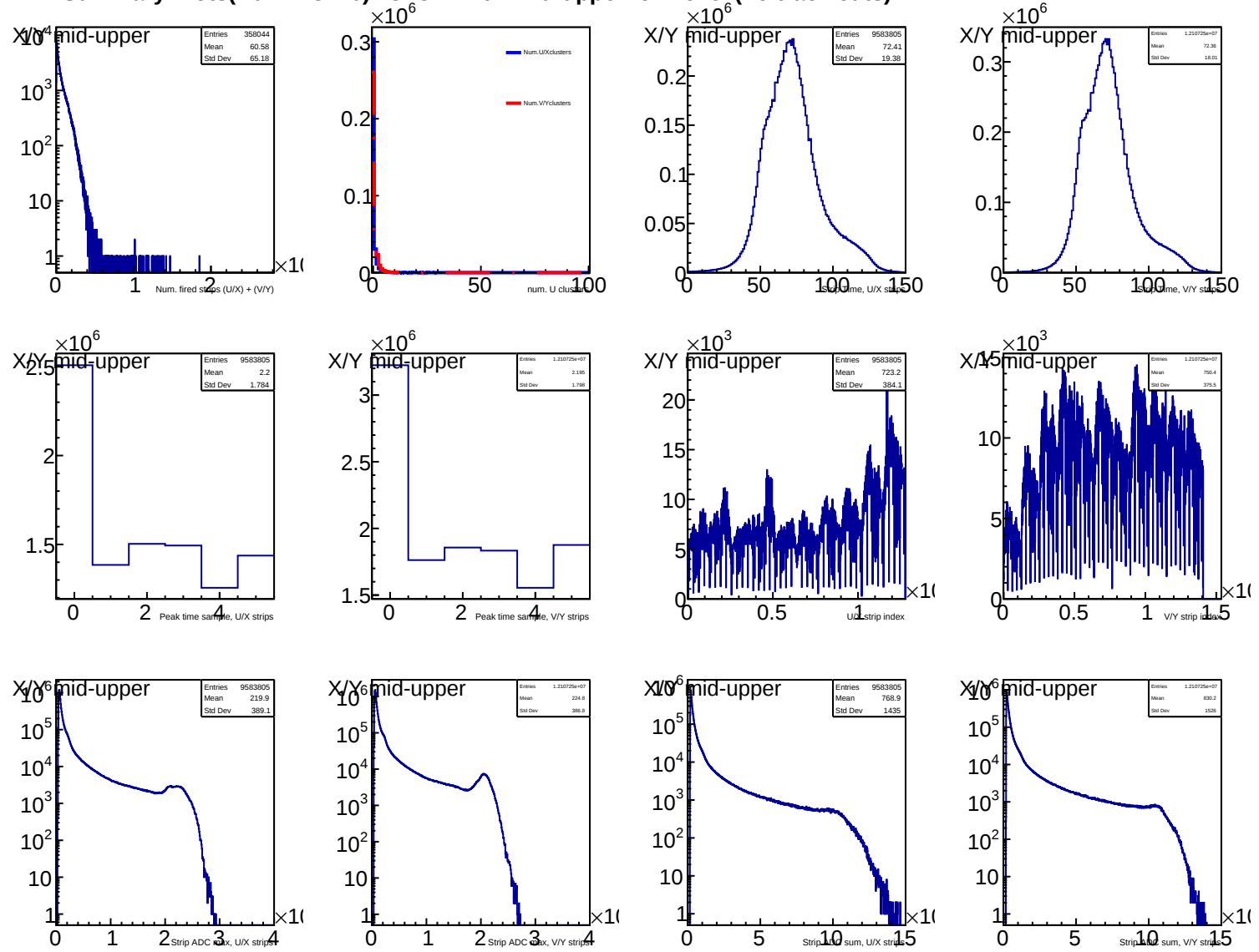
Summary Plots(Run #13240) 11: UVA U/V layer 3 low-level (no track cuts)



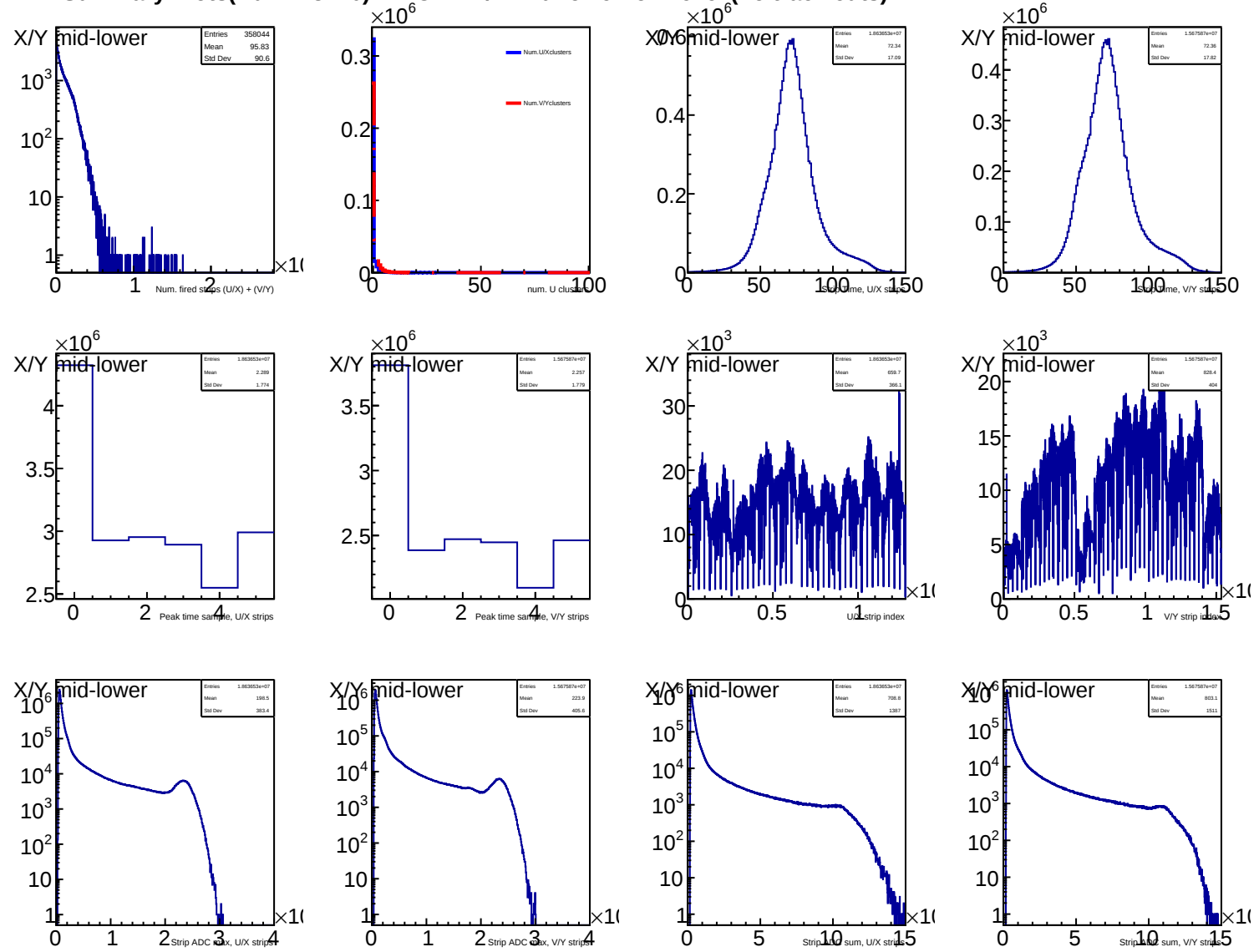
Summary Plots(Run #13240) 12: UVA X/Y top low-level (no track cuts)



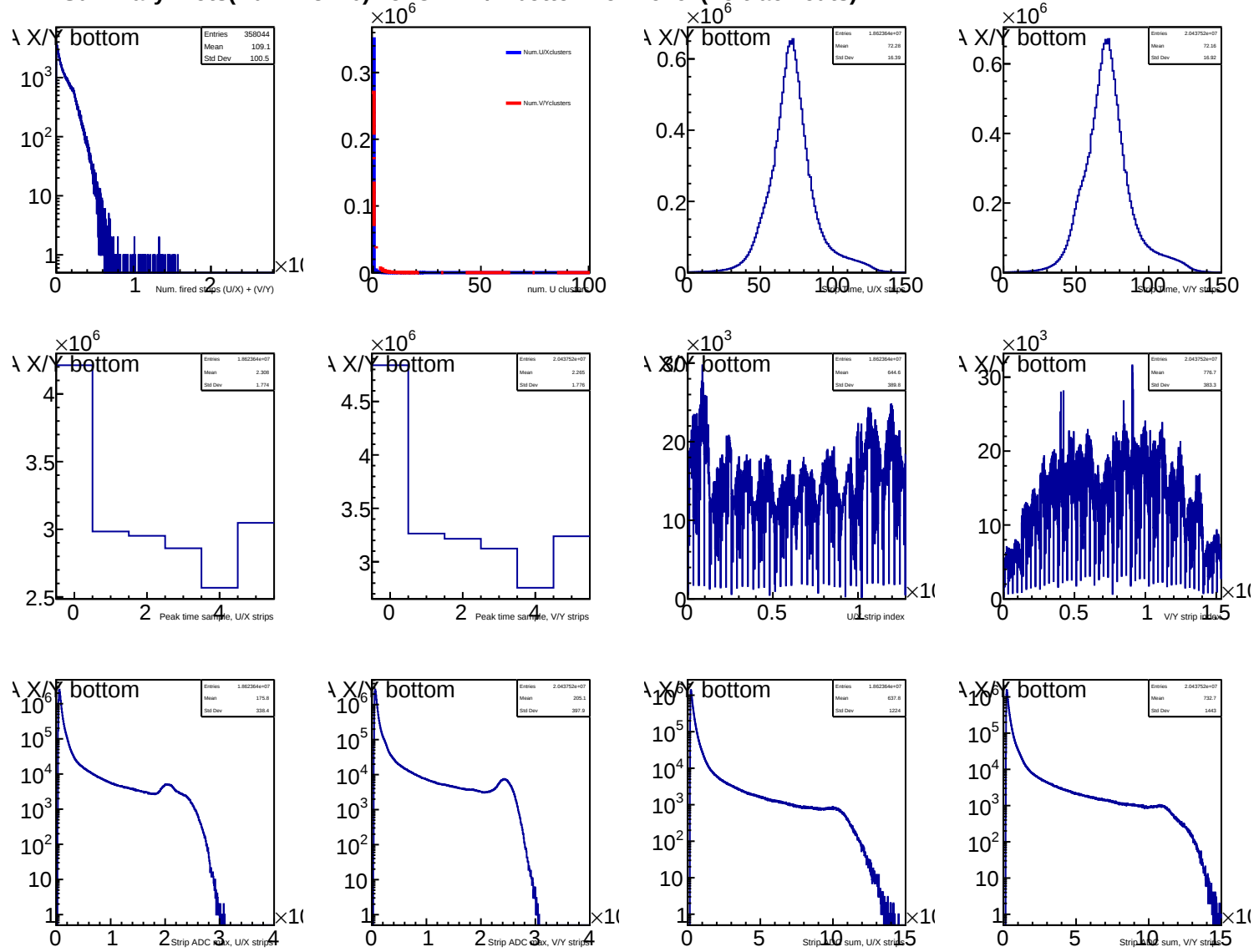
Summary Plots(Run #13240) 13: UVA X/Y mid-upper low-level (no track cuts)



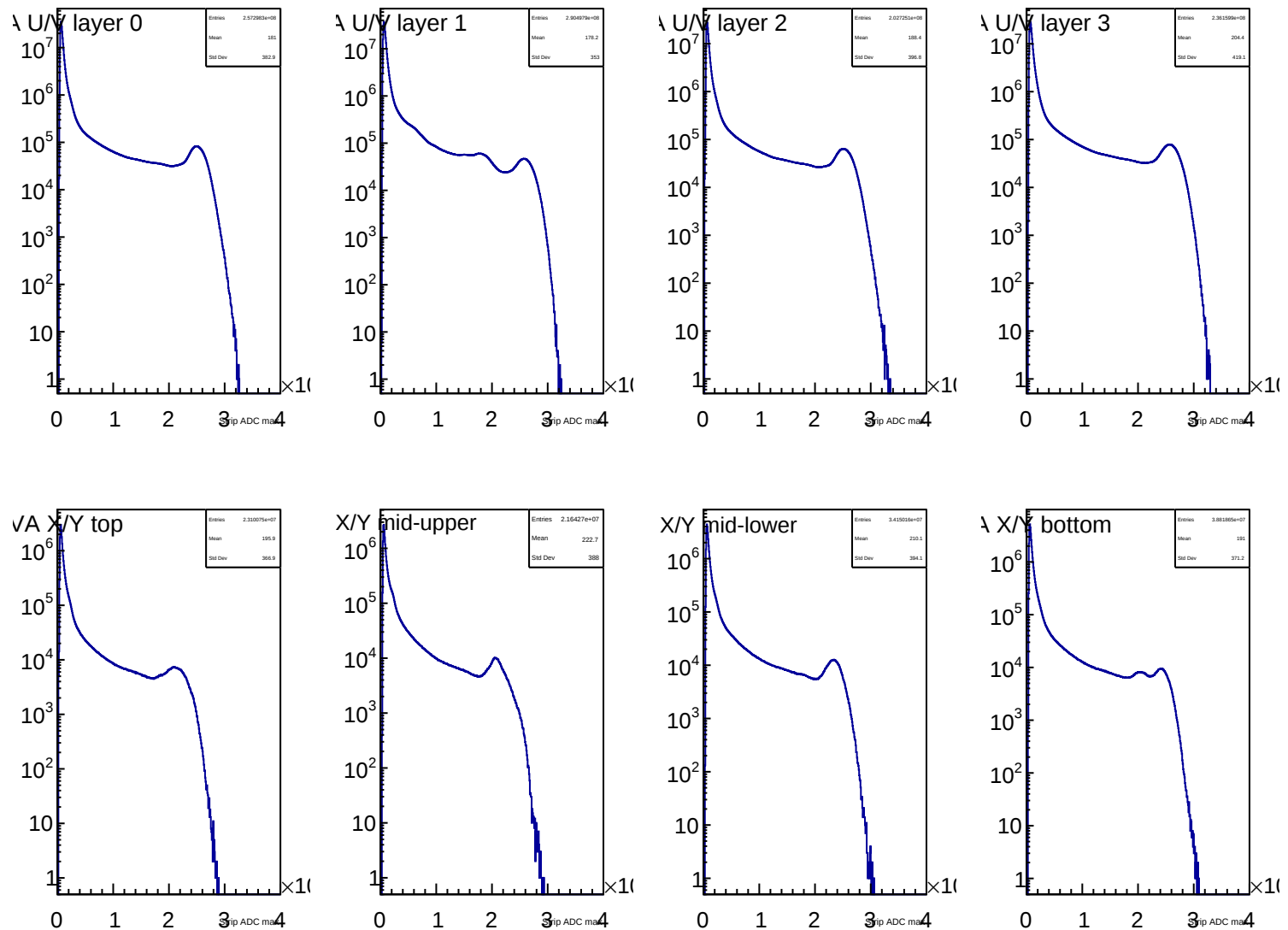
Summary Plots(Run #13240) 14: UVA X/Y mid-low-level (no track cuts)



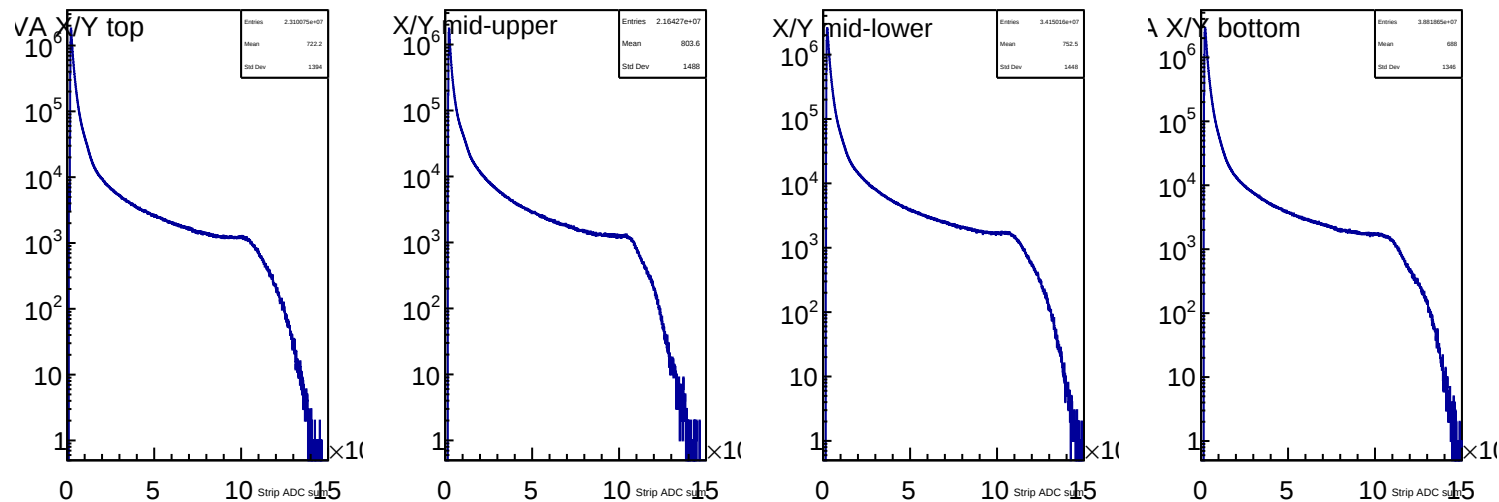
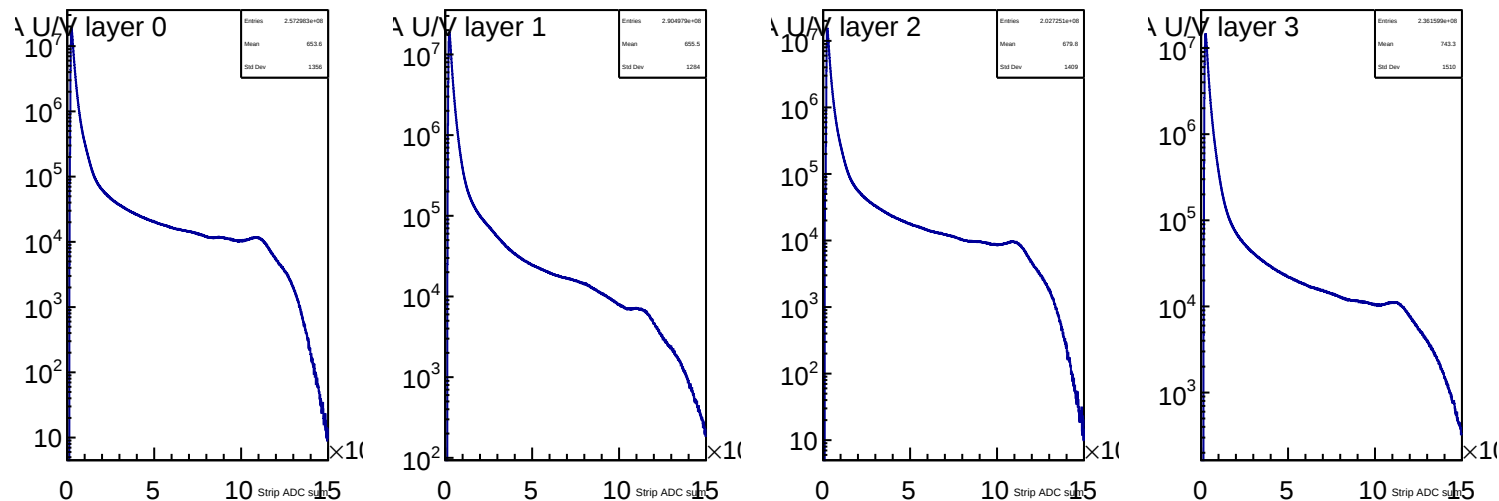
Summary Plots(Run #13240) 15: UVA X/Y bottom low-level (no track cuts)



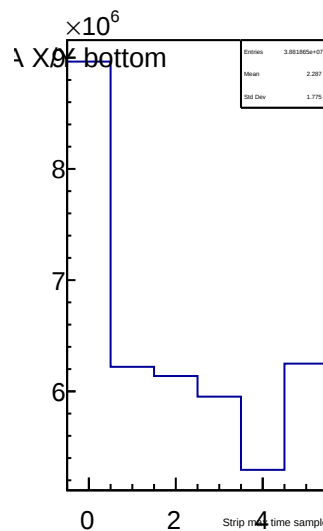
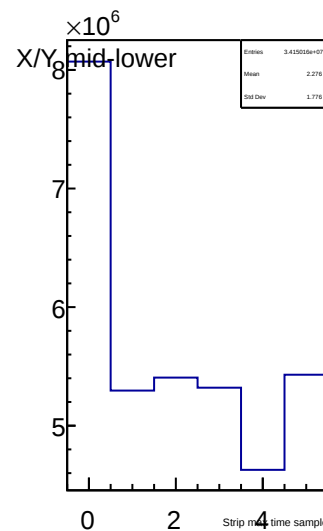
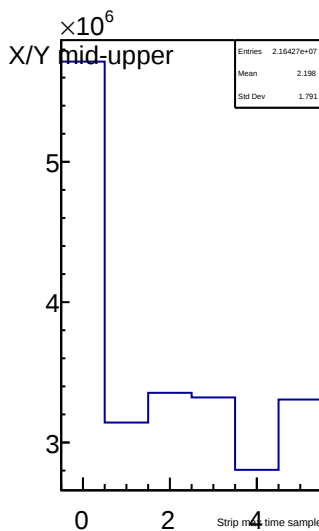
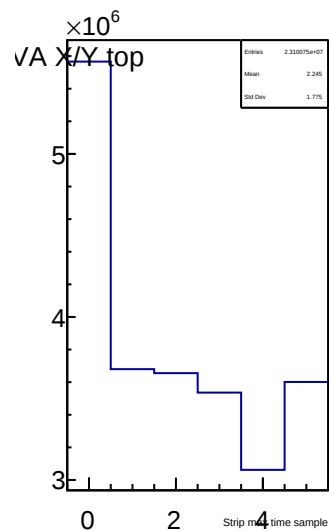
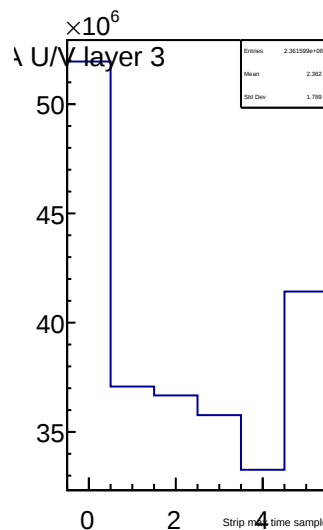
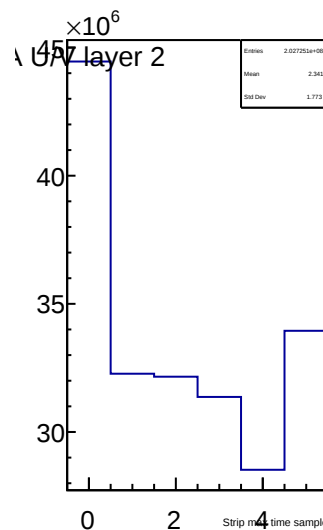
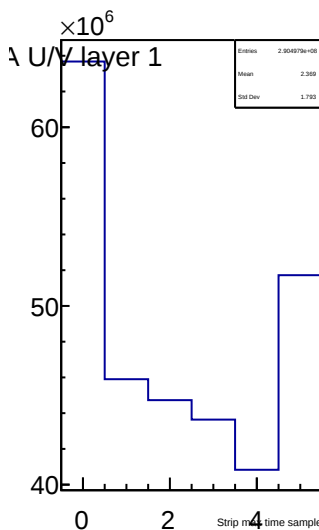
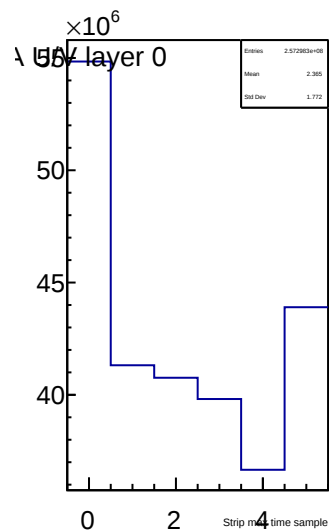
Summary Plots(Run #13240) 16: All modules strip ADC max



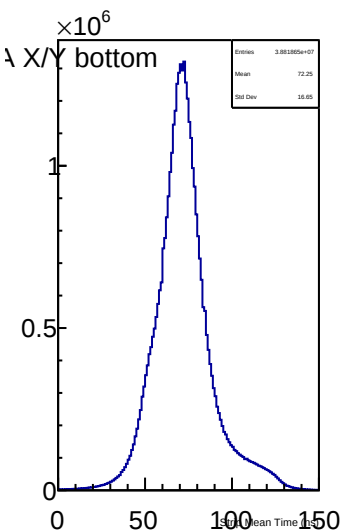
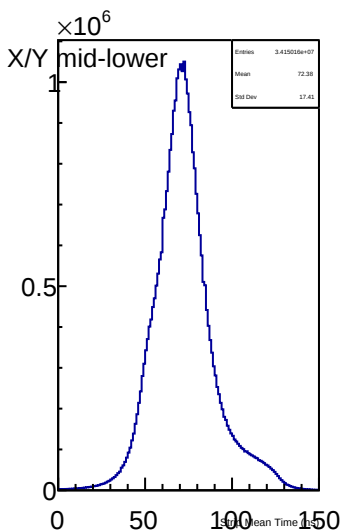
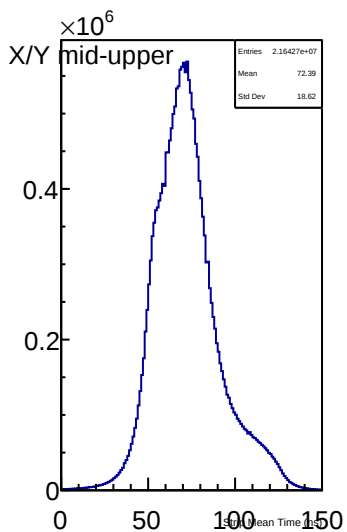
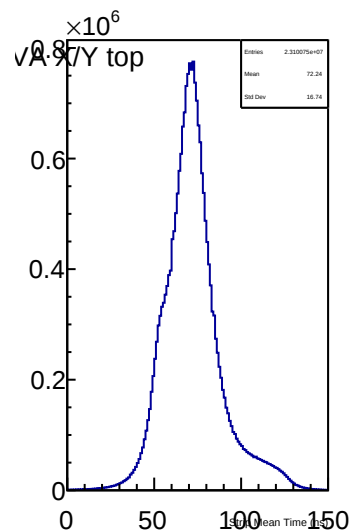
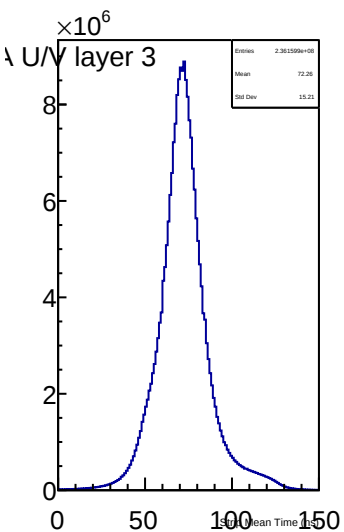
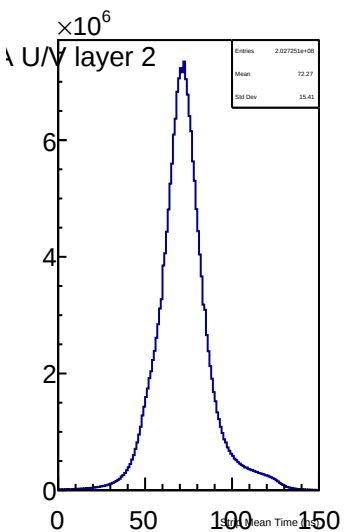
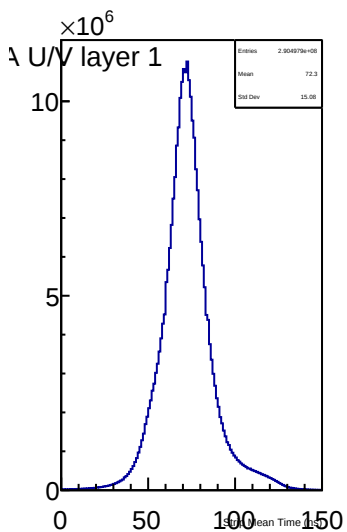
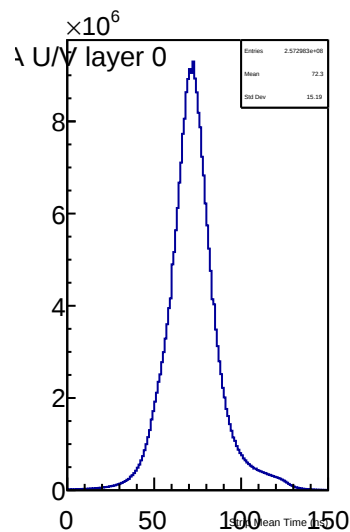
Summary Plots(Run #13240) 17: All modules strip ADC sum



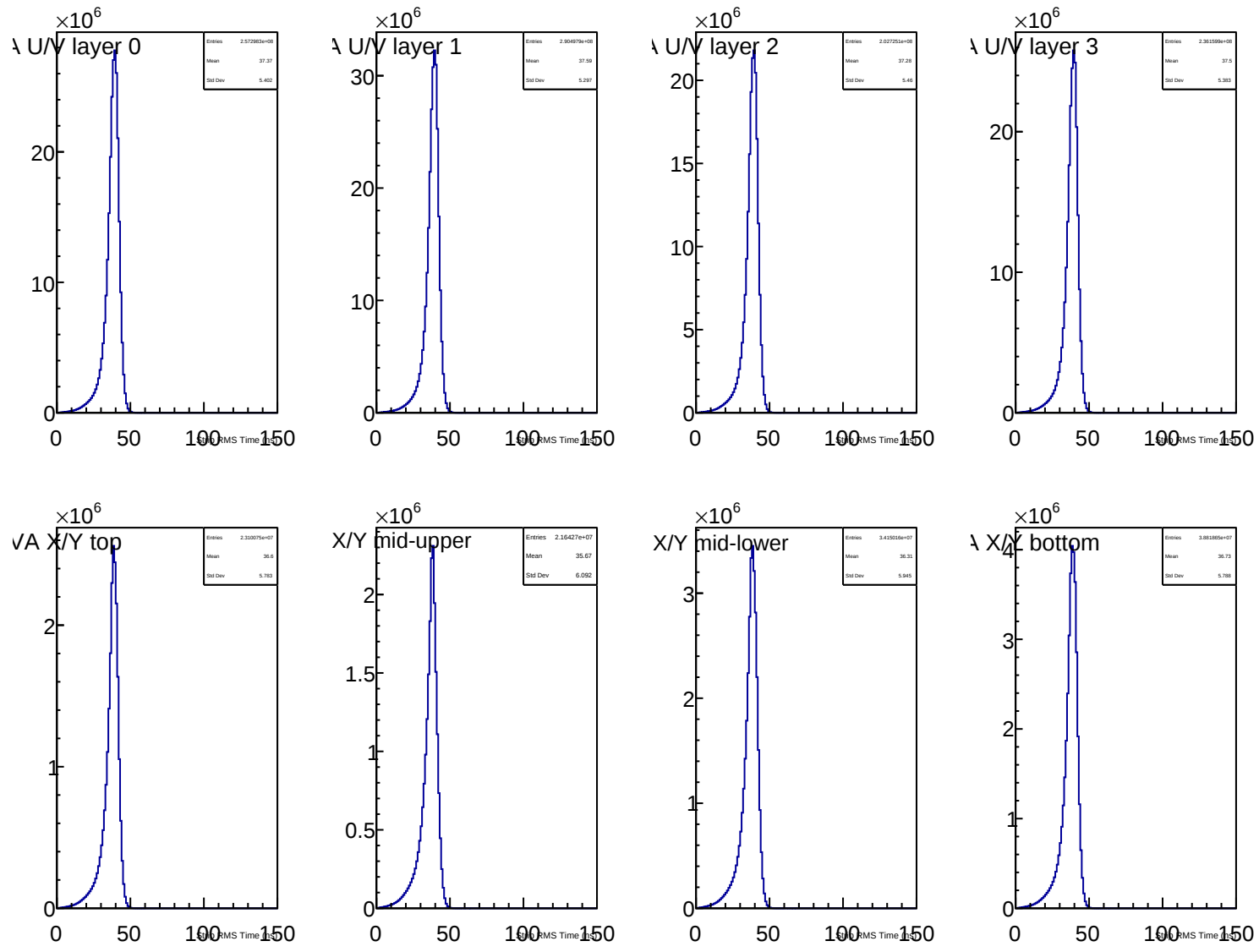
Summary Plots(Run #13240) 18: All modules max time sample



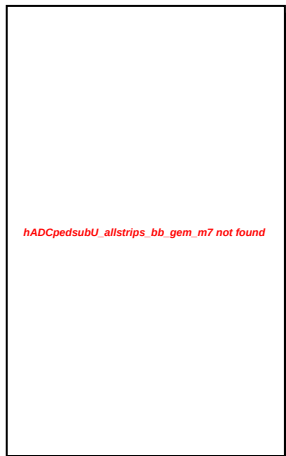
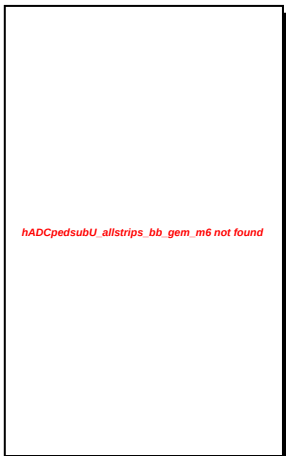
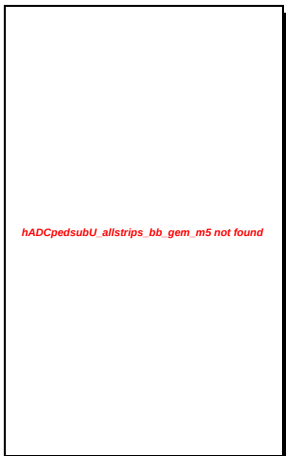
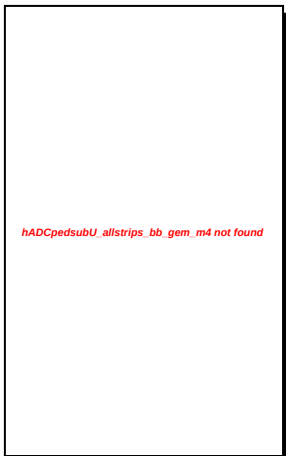
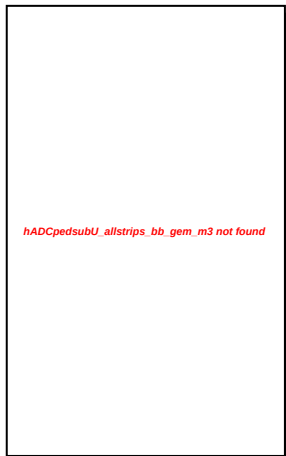
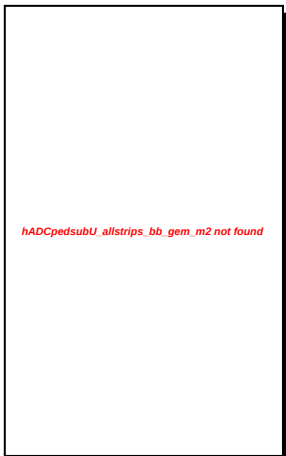
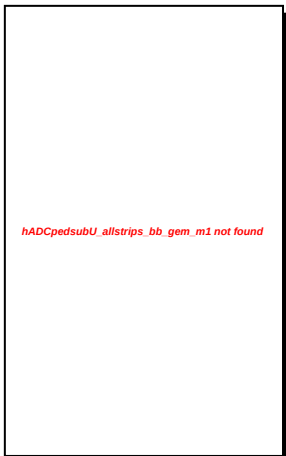
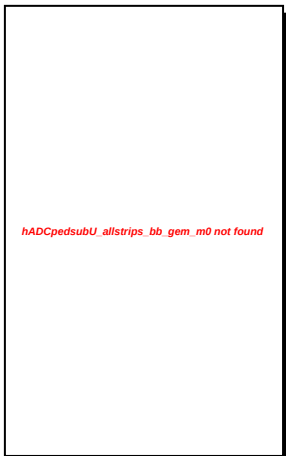
Summary Plots(Run #13240) 19: All modules strip time



Summary Plots(Run #13240) 20: All modules strip rms time



Summary Plots(Run #13240) 21: All modules U strip ADC no zero sup



Summary Plots(Run #13240) 22: All modules V strip ADC no zero sup

