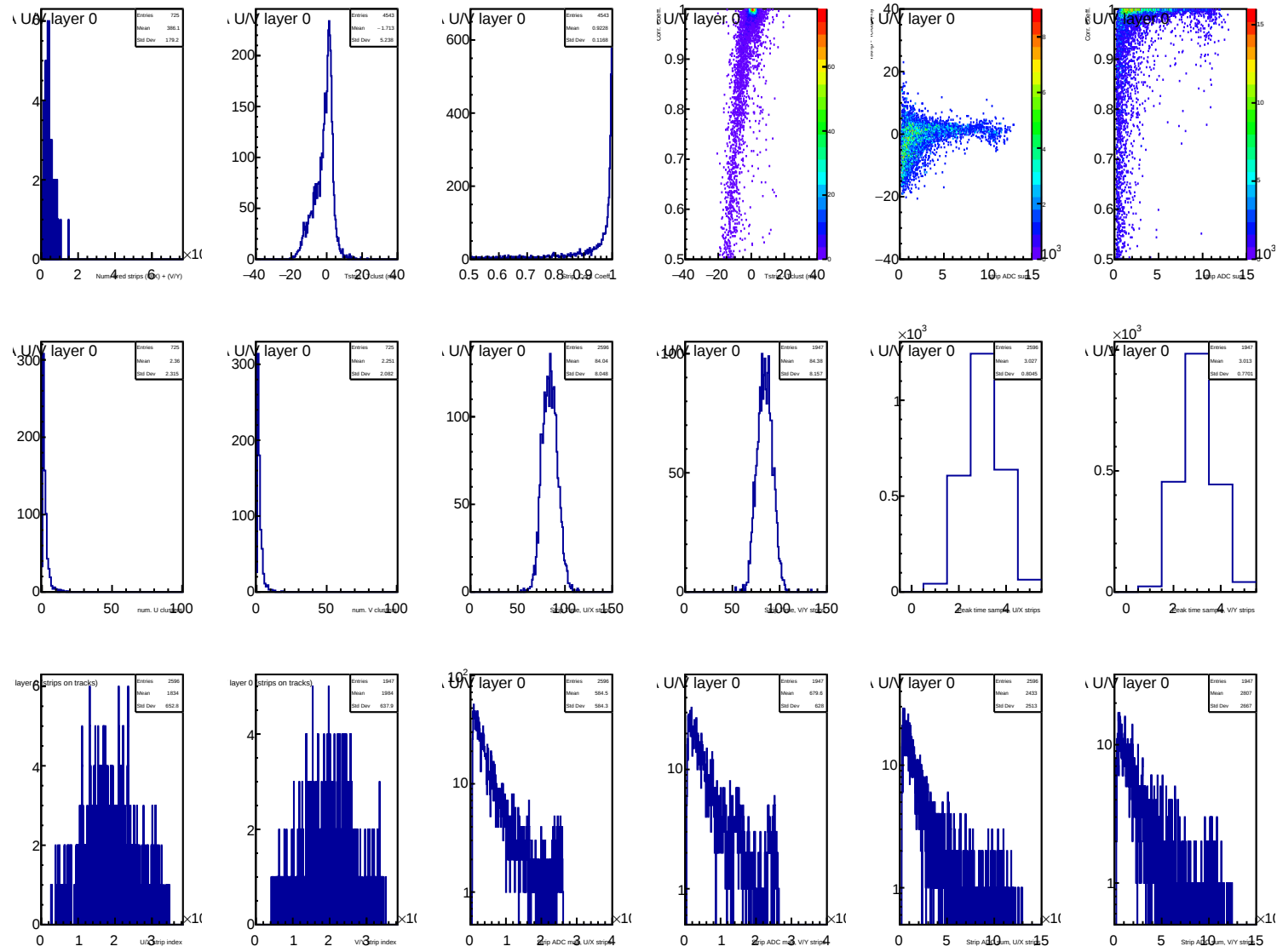
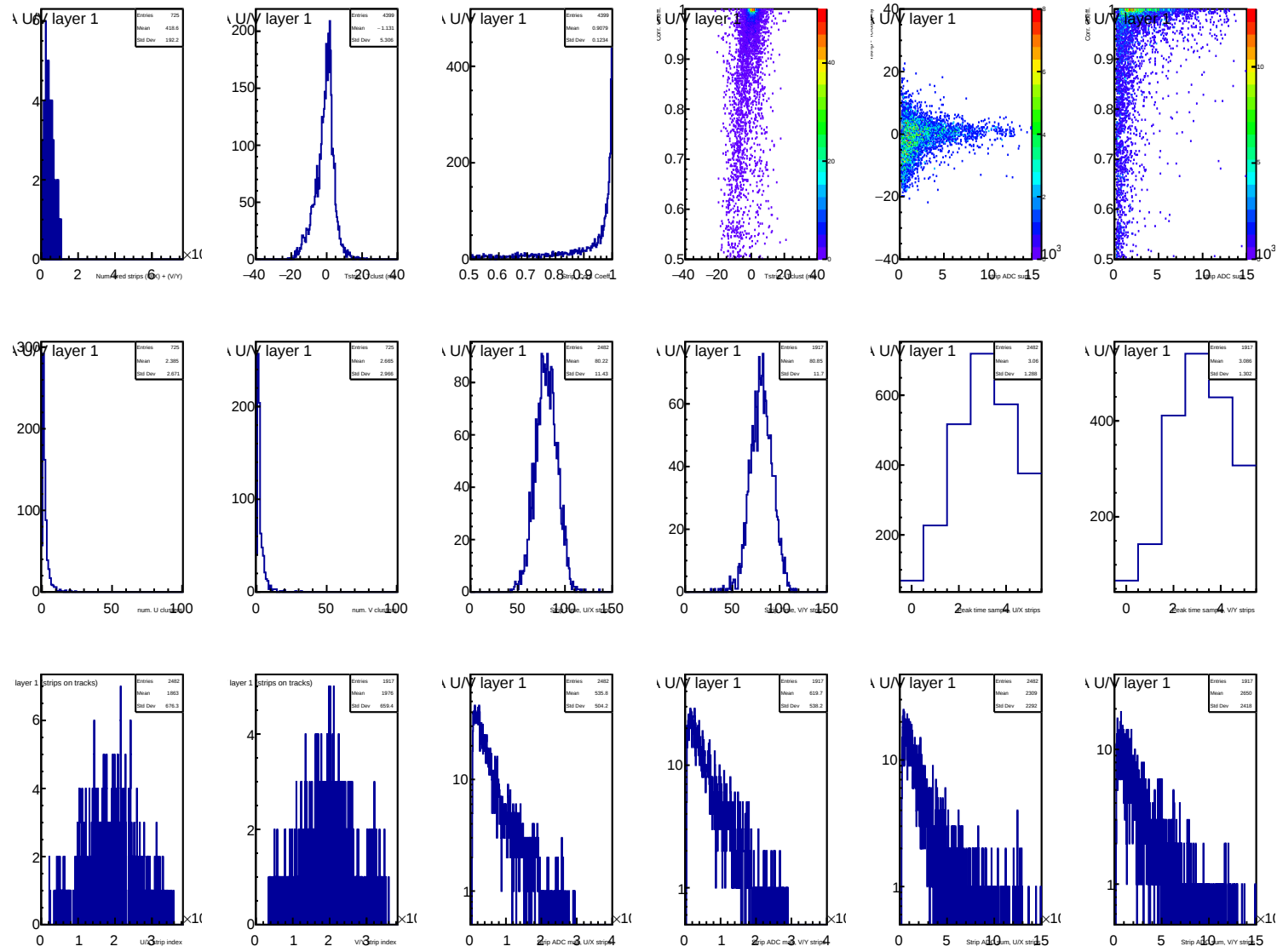


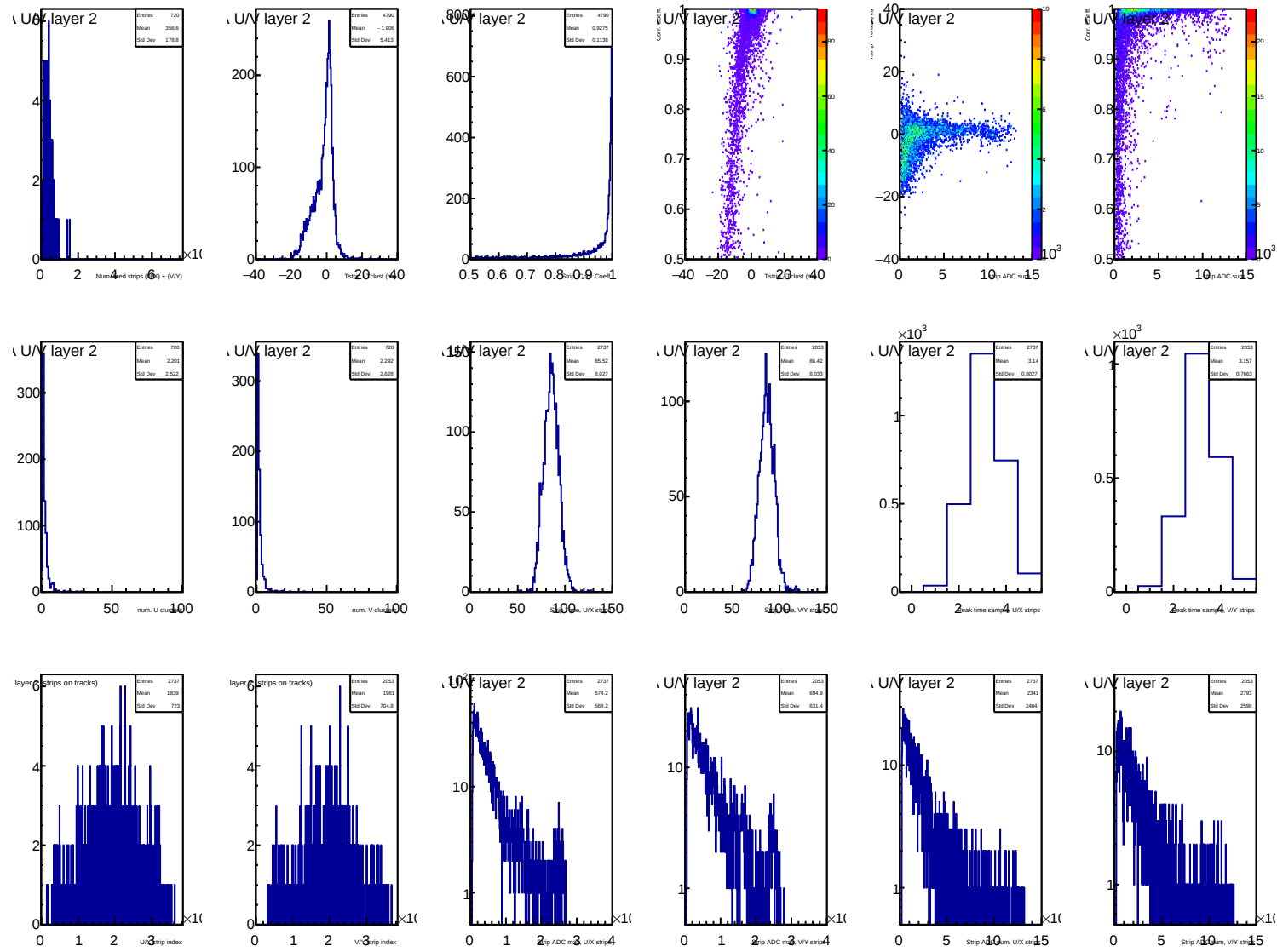
Summary Plots (Run #13297) 0: UVA U/V layer 0 low-level (with track cuts)



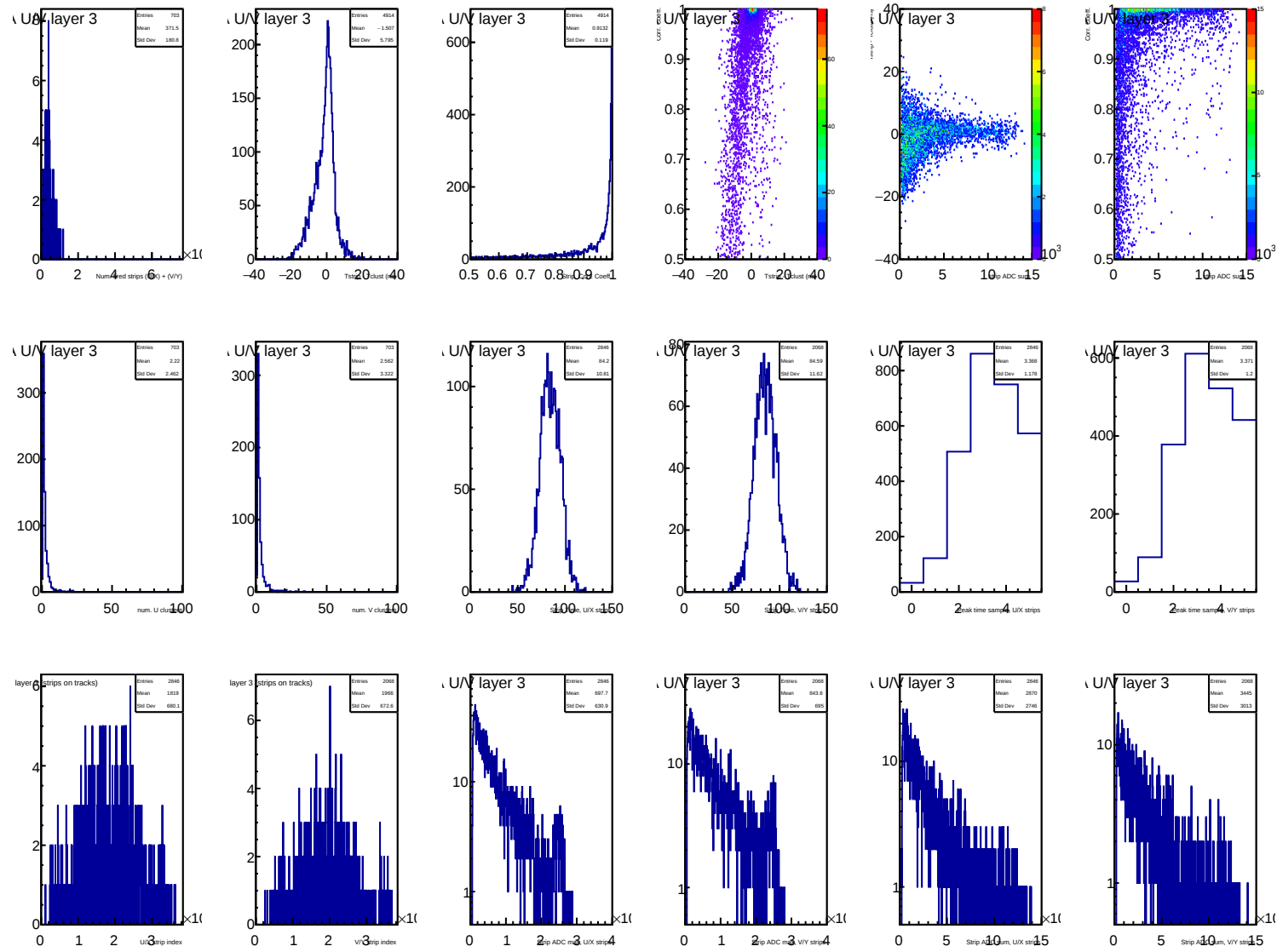
Summary Plots (Run #13297) 1: UVA U/V layer 1 low-level (with track cuts)



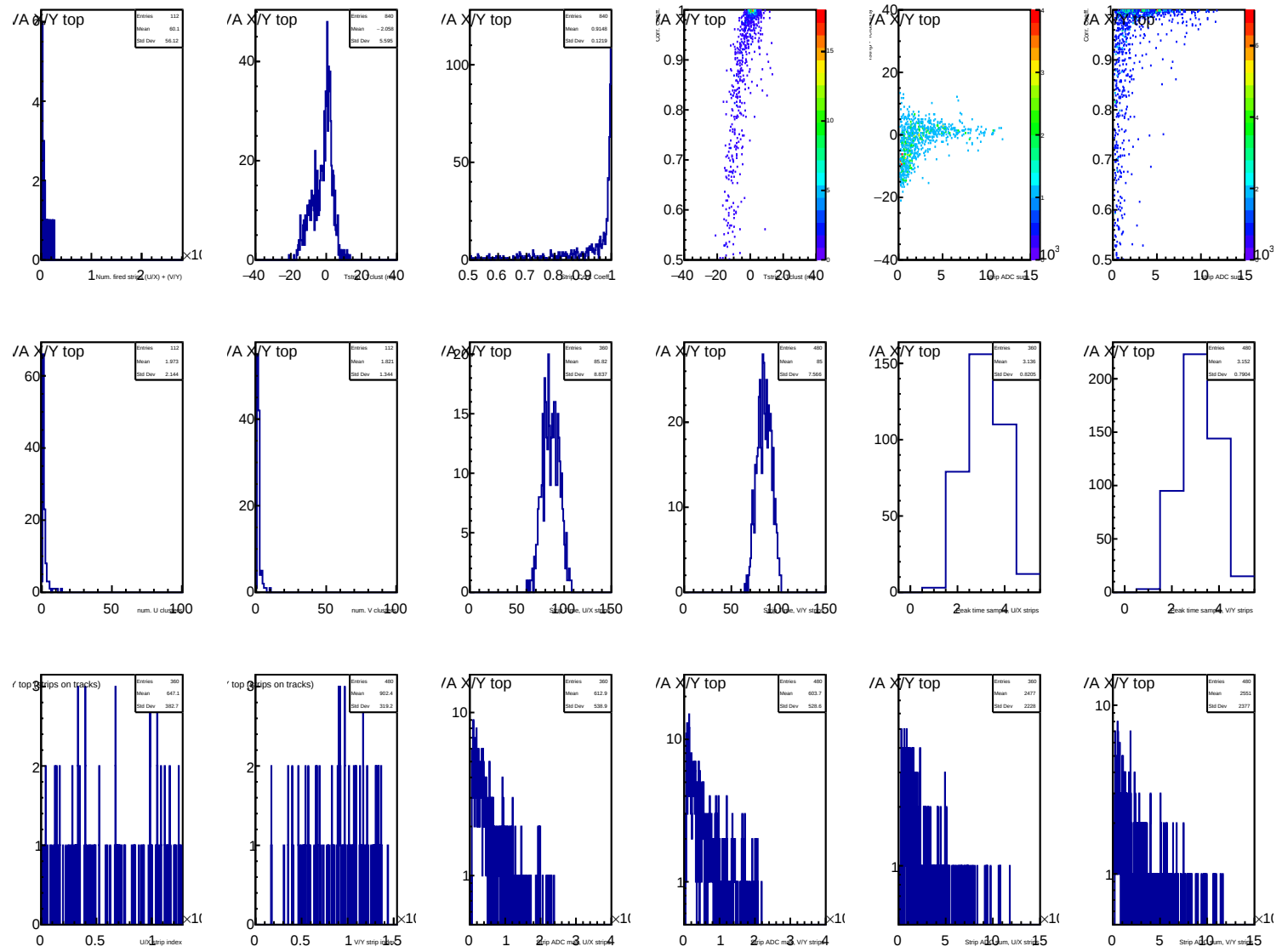
Summary Plots (Run #13297) 2: UVA U/V layer 2 low-level (with track cuts)



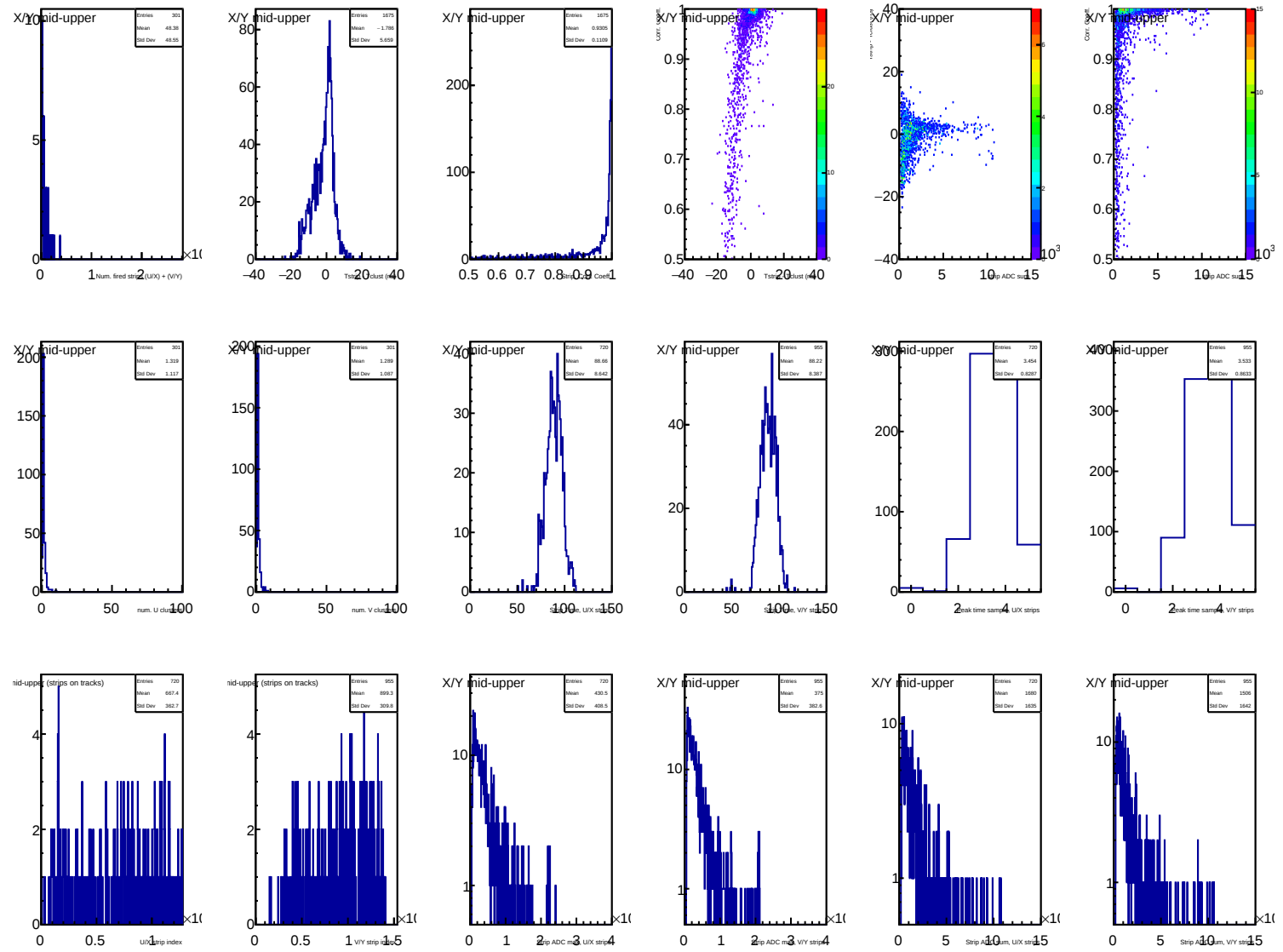
Summary Plots(Run #13297) 3: UVA U/V layer 3 low-level (with track cuts)



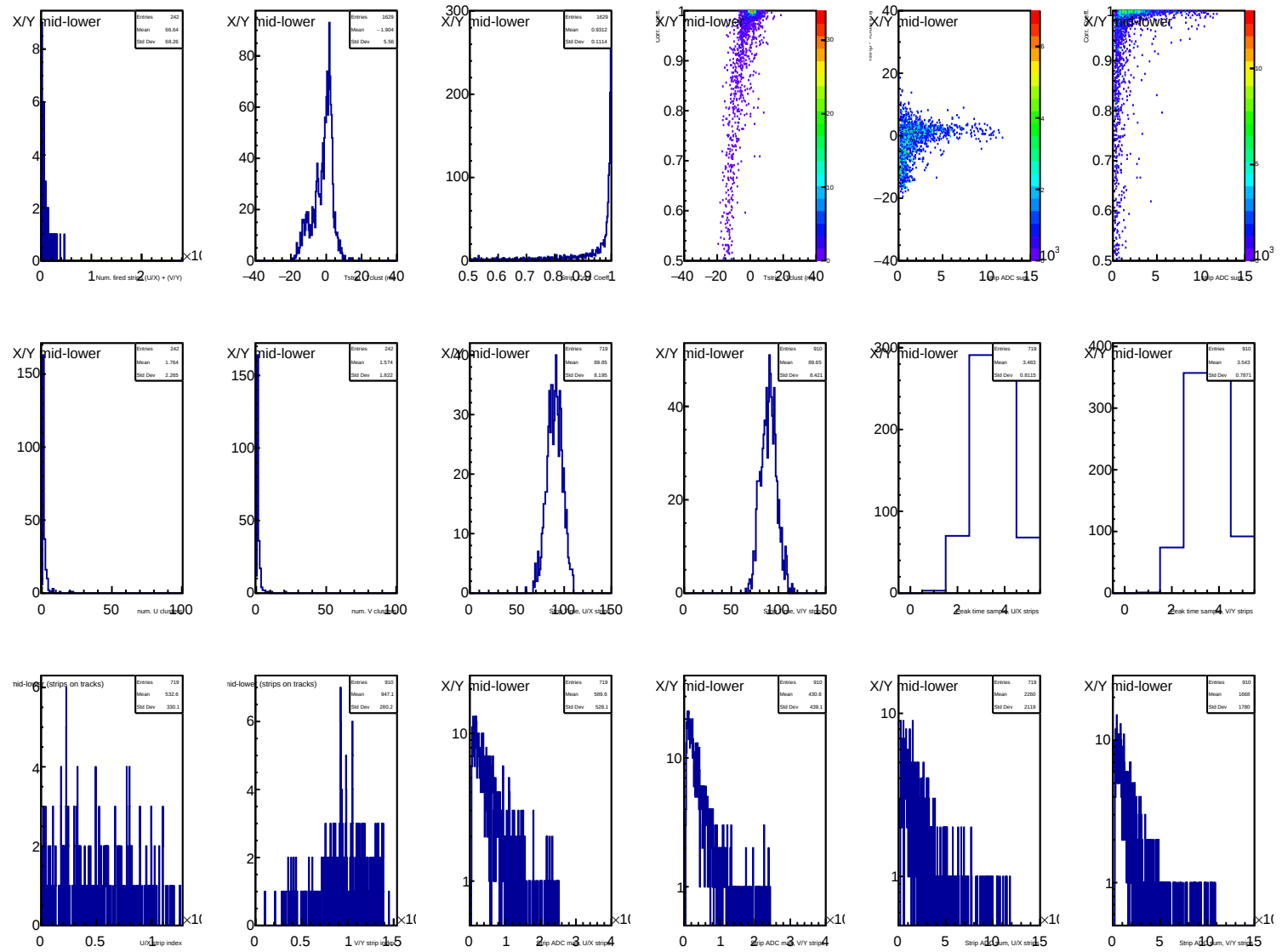
Summary Plots (Run #13297) 4: UVA X/Y top low-level (with track cuts)



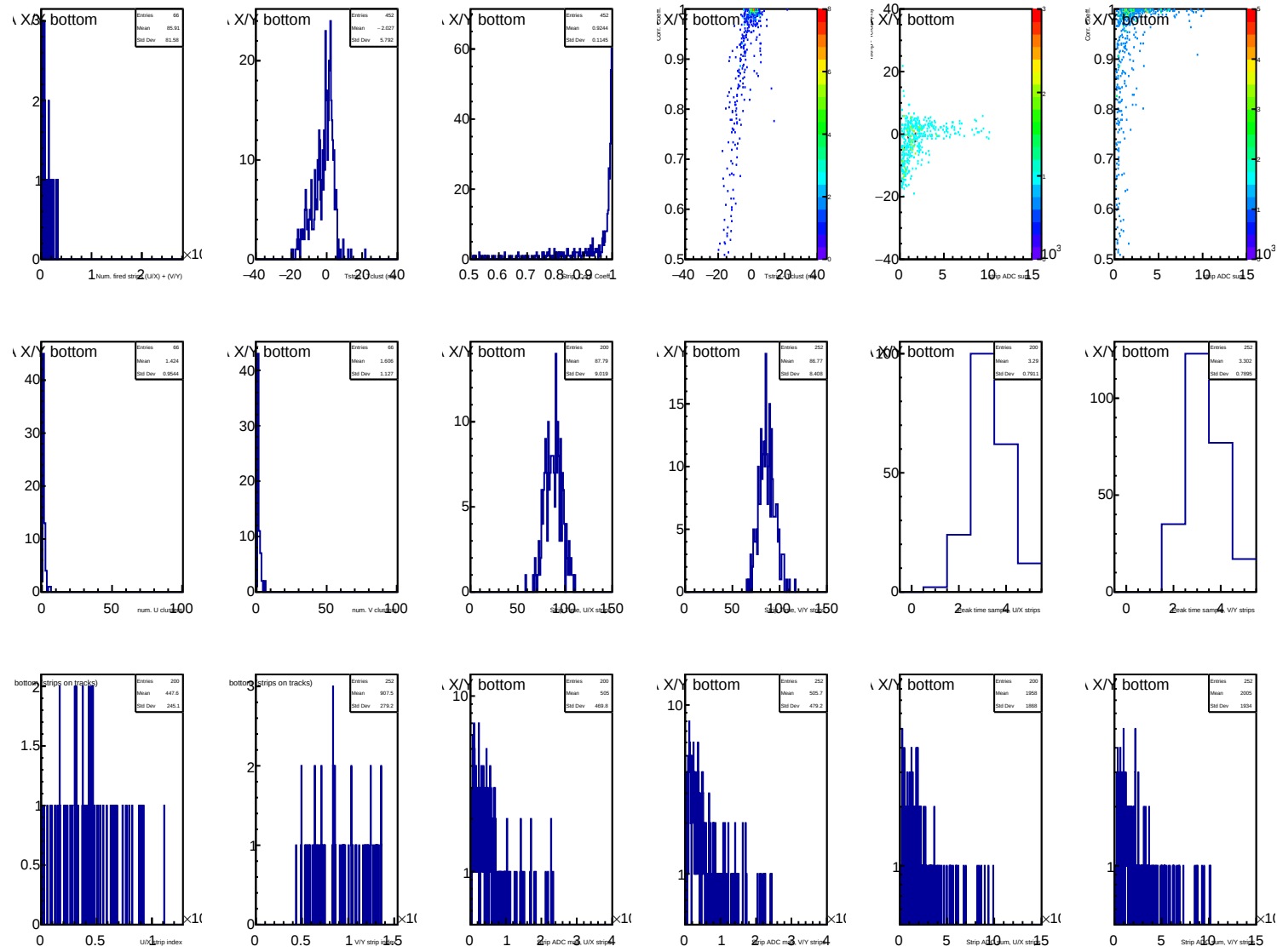
Summary Plots(Run #13297) 5: UVA X/Y mid-upper low-level (with track cuts)



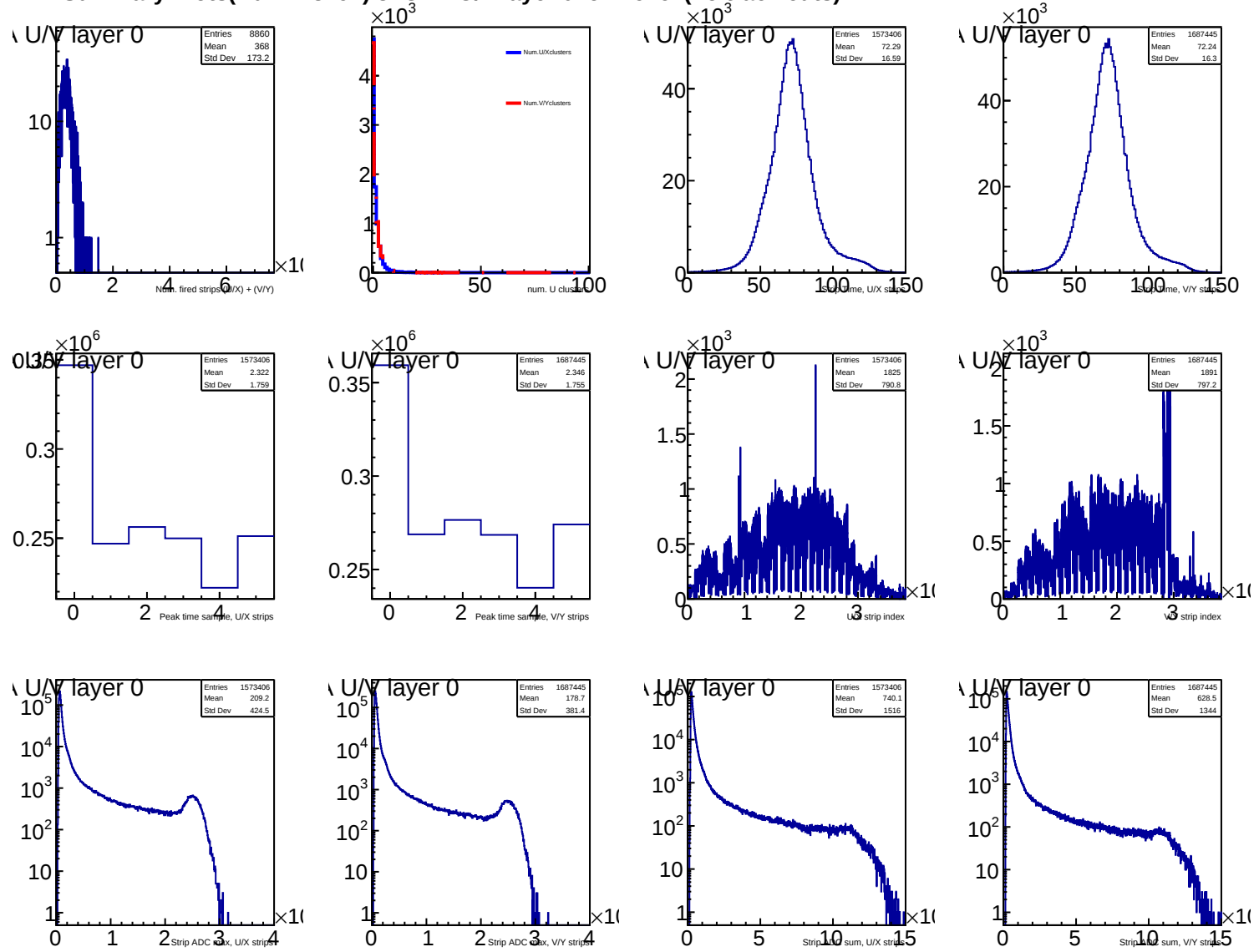
Summary Plots(Run #13297) 6: UVA X/Y mid-lowler low-level (with track cuts)



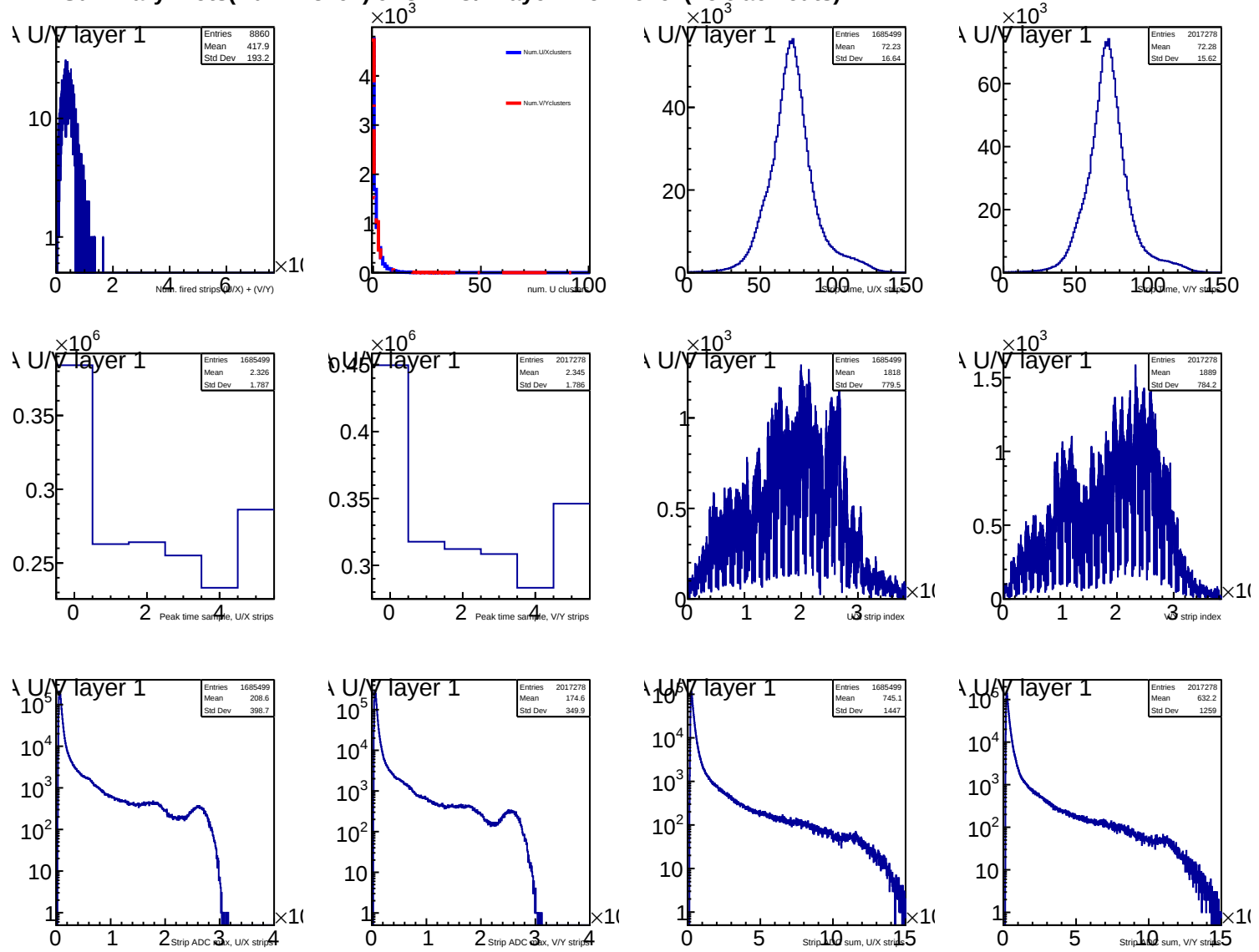
Summary Plots(Run #13297) 7: UVA X/Y bottom low-level (with track cuts)



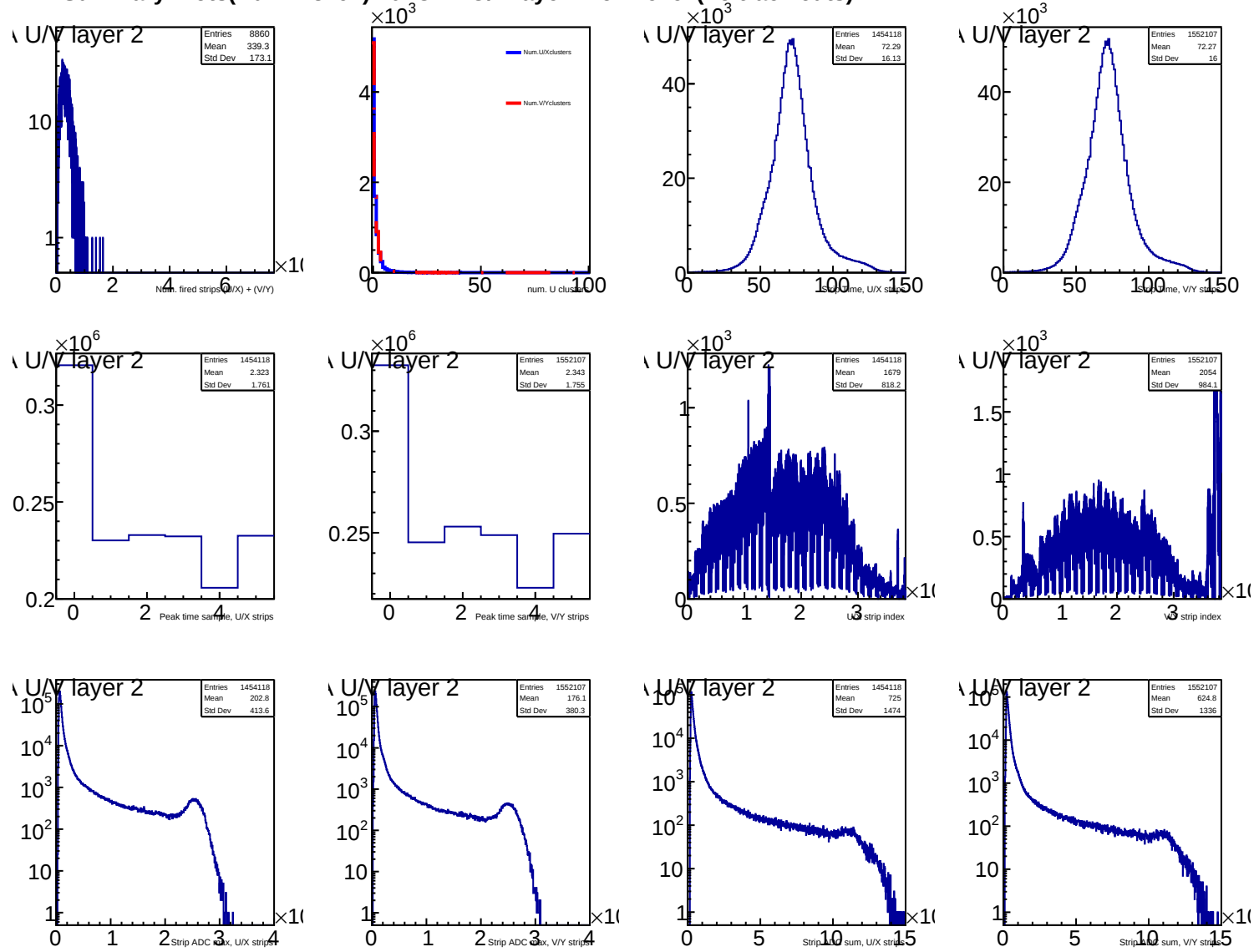
Summary Plots(Run #13297) 8: UVA U/V layer 0 low-level (no track cuts)



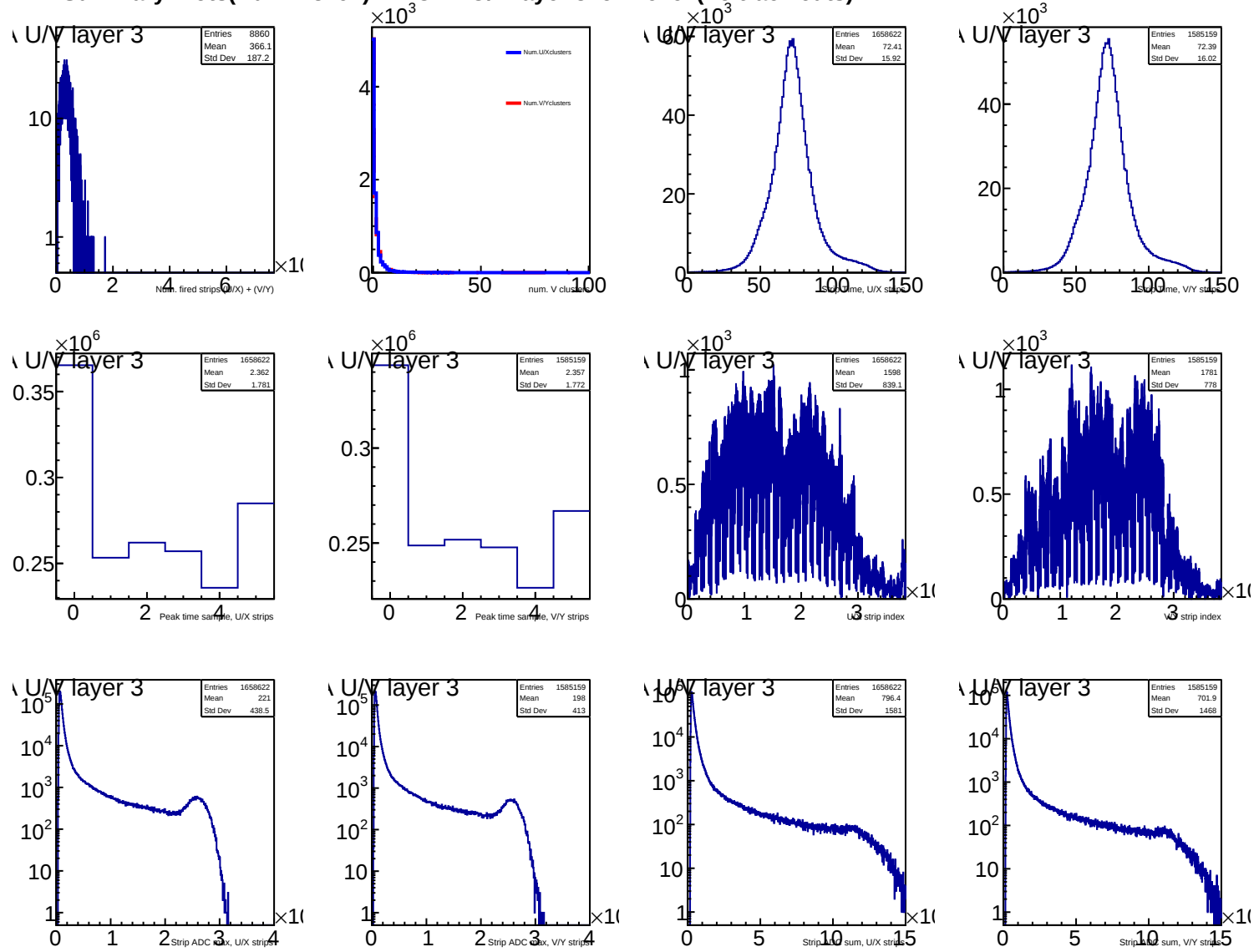
Summary Plots(Run #13297) 9: UVA U/V layer 1 low-level (no track cuts)



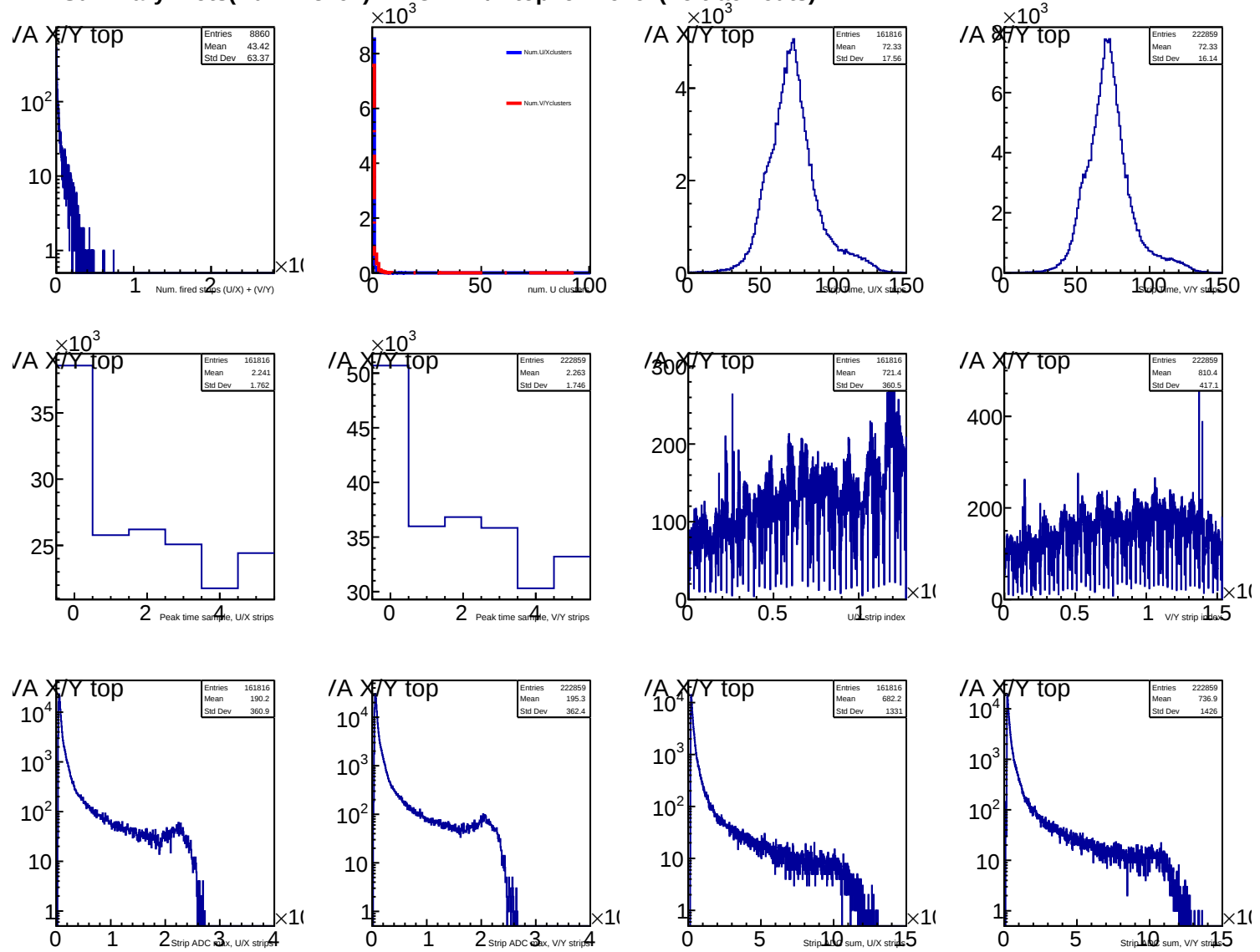
Summary Plots(Run #13297) 10: UVA U/V layer 2 low-level (no track cuts)



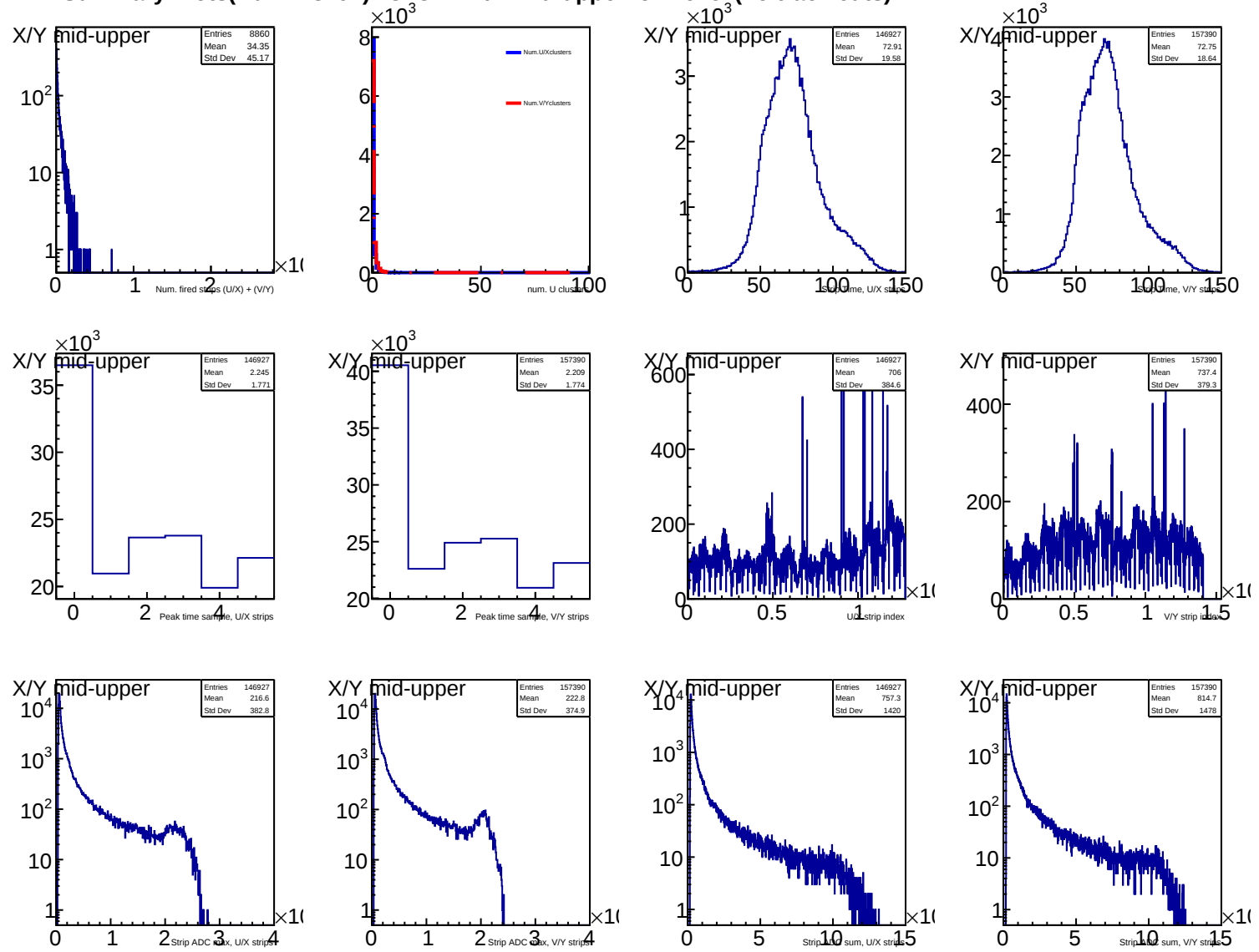
Summary Plots(Run #13297) 11: UVA U/V layer 3 low-level (no track cuts)



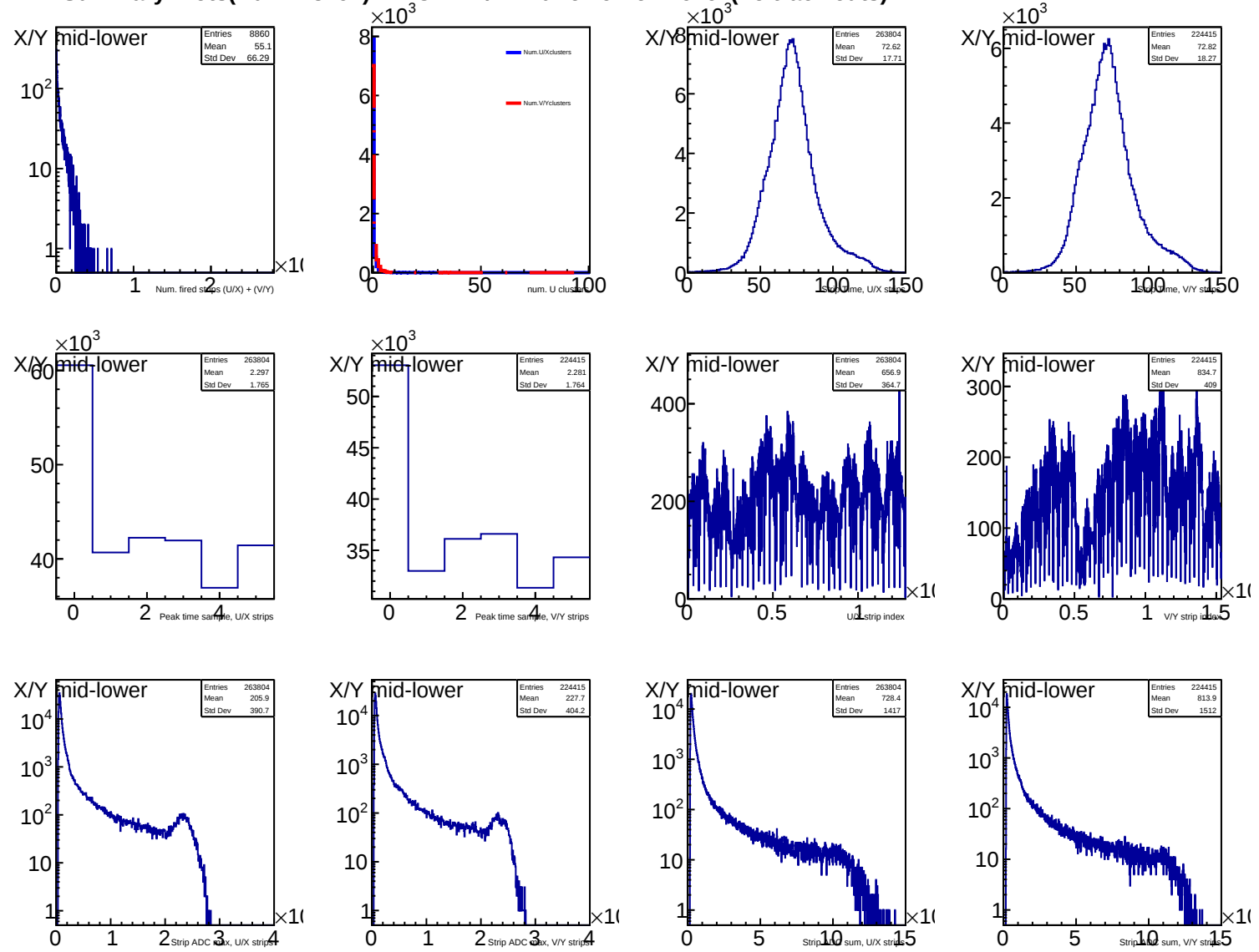
Summary Plots(Run #13297) 12: UVA X/Y top low-level (no track cuts)



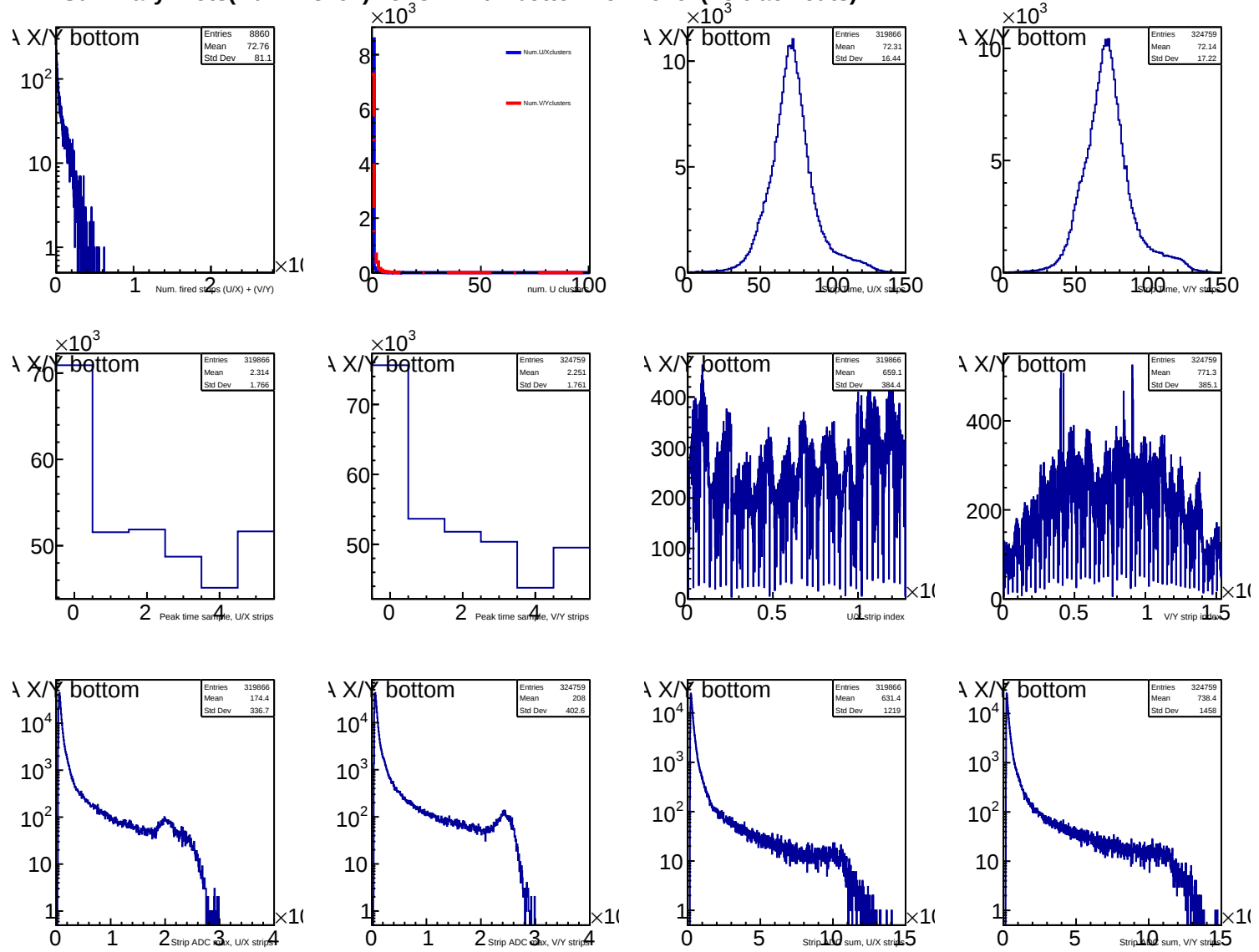
Summary Plots(Run #13297) 13: UVA X/Y mid-upper low-level (no track cuts)



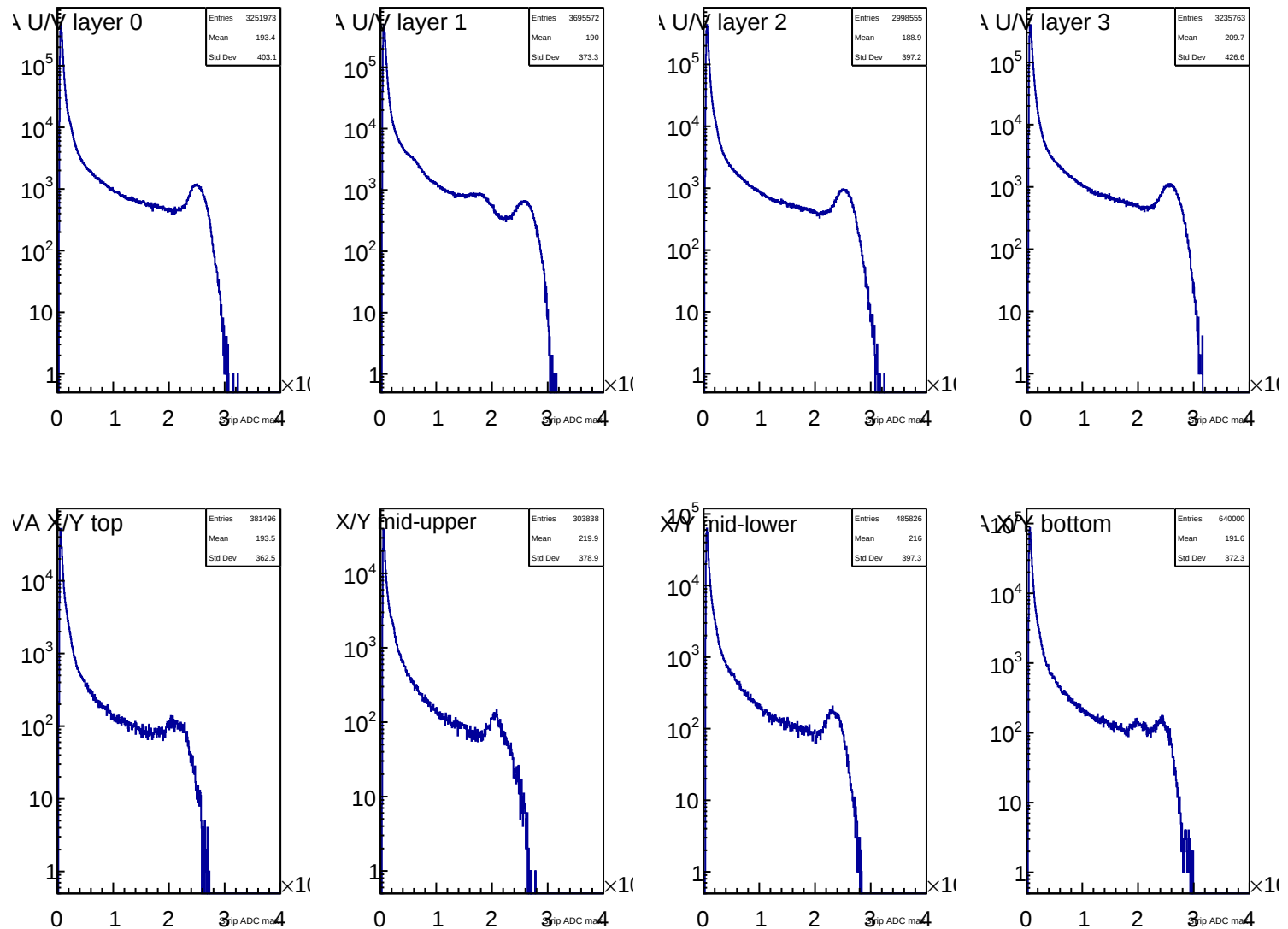
Summary Plots(Run #13297) 14: UVA X/Y mid-low-level low-level (no track cuts)



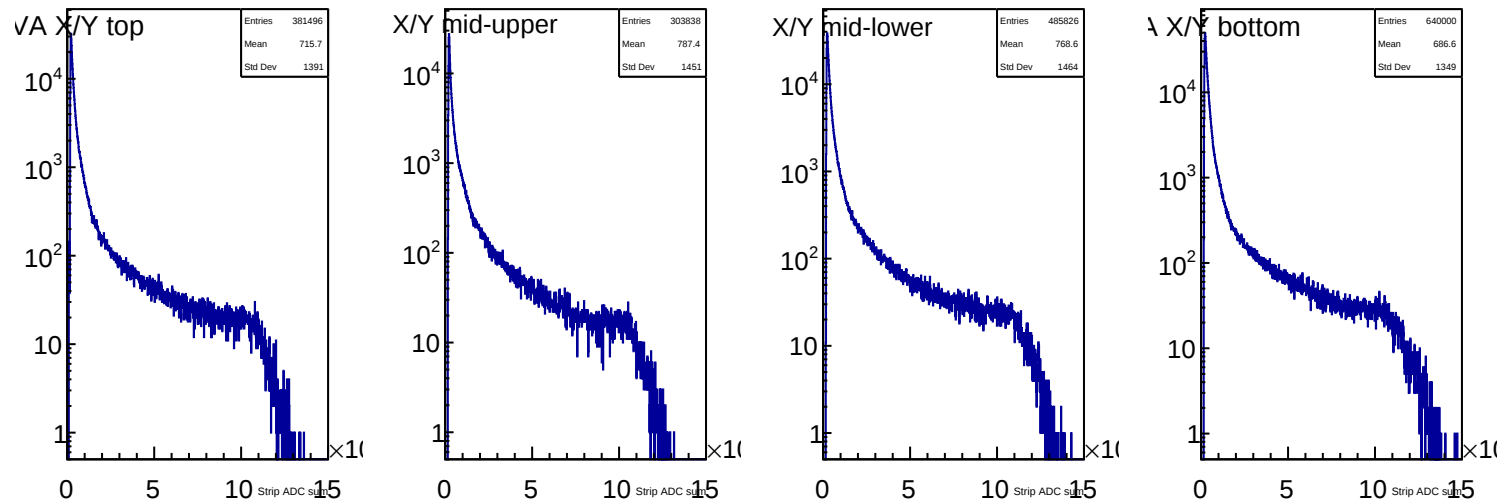
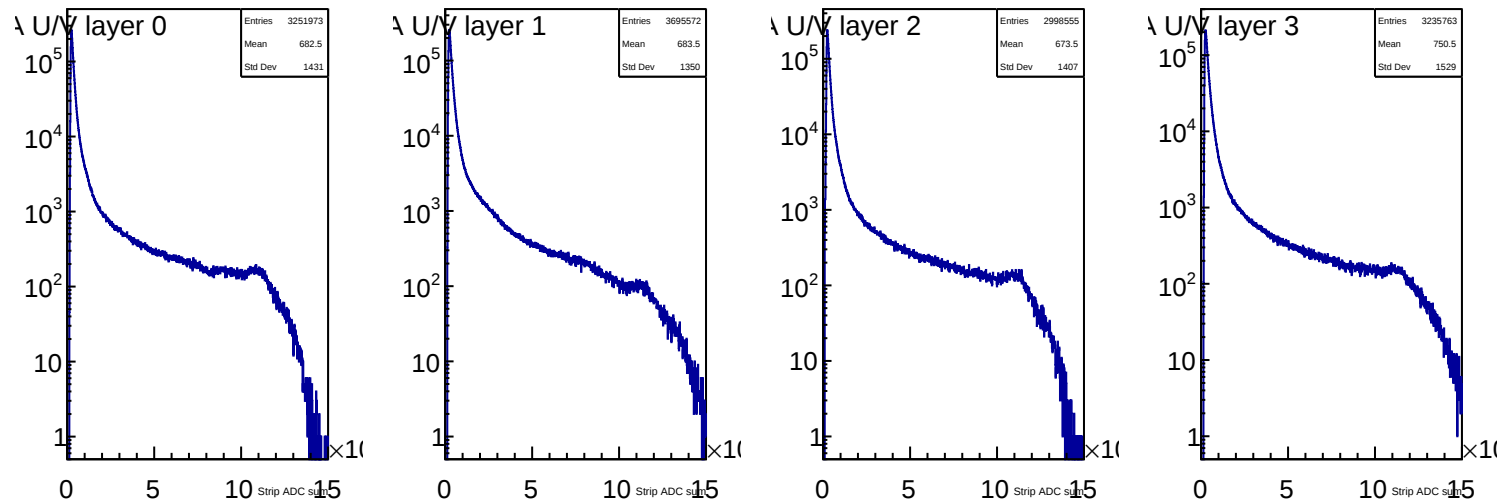
Summary Plots(Run #13297) 15: UVA X/Y bottom low-level (no track cuts)



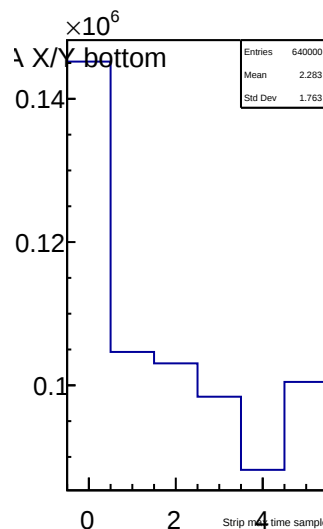
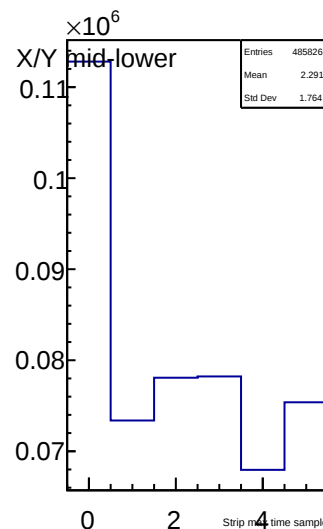
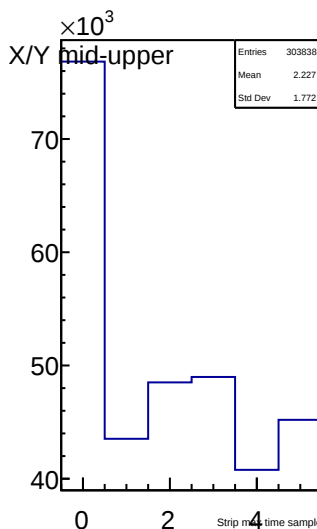
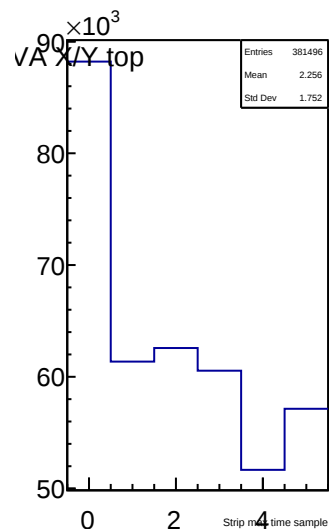
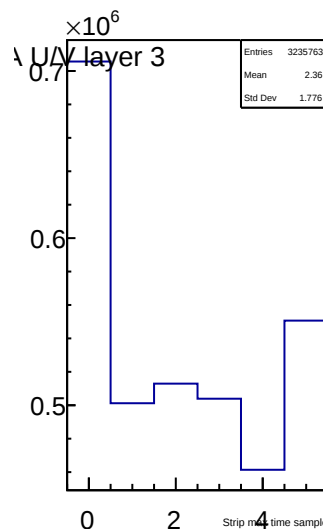
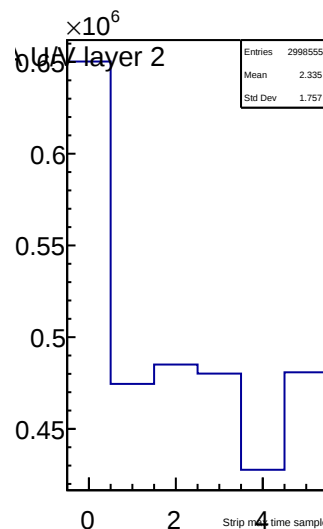
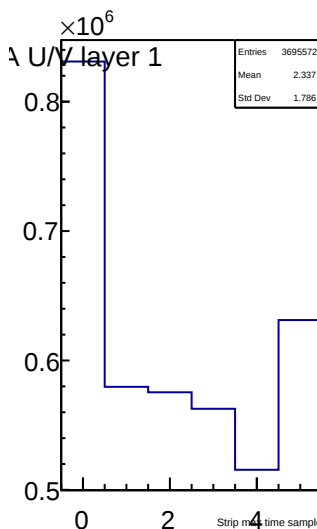
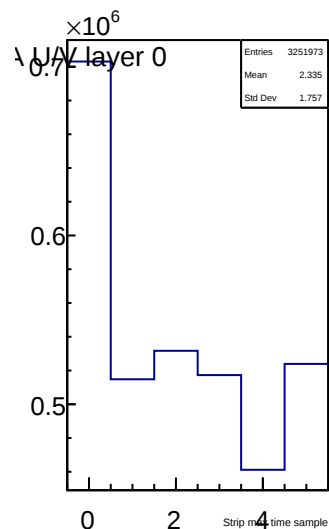
Summary Plots(Run #13297) 16: All modules strip ADC max



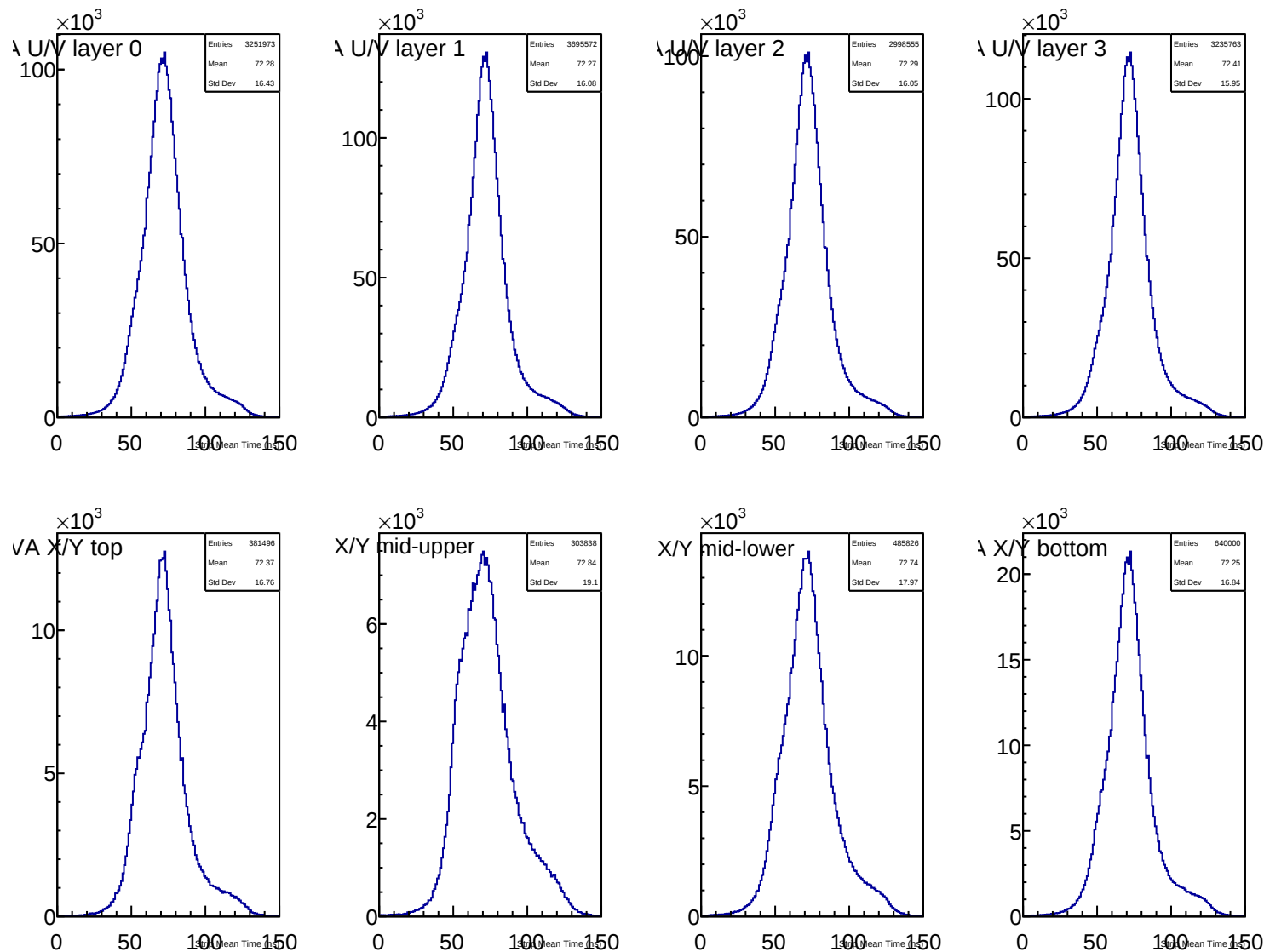
Summary Plots(Run #13297) 17: All modules strip ADC sum



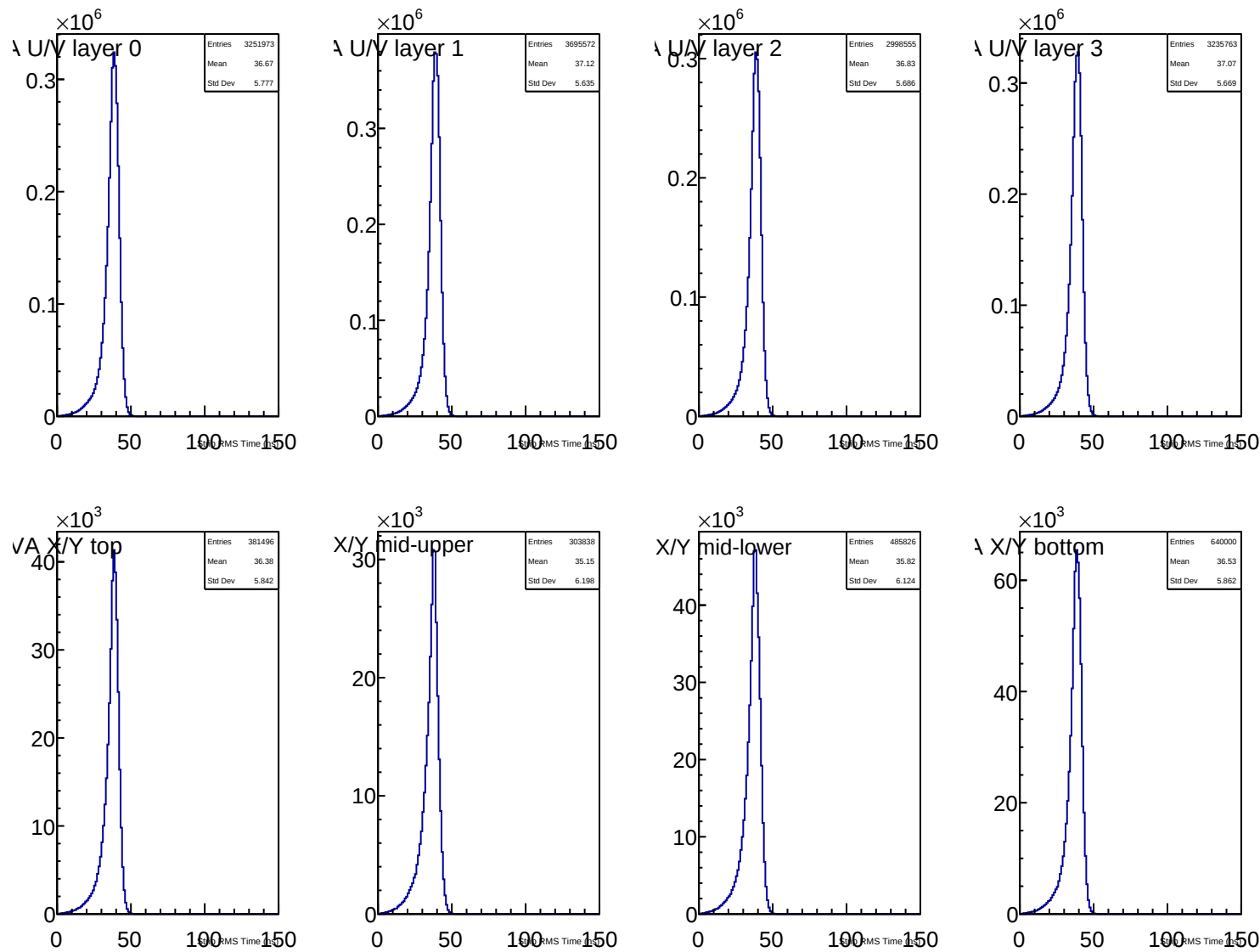
Summary Plots(Run #13297) 18: All modules max time sample



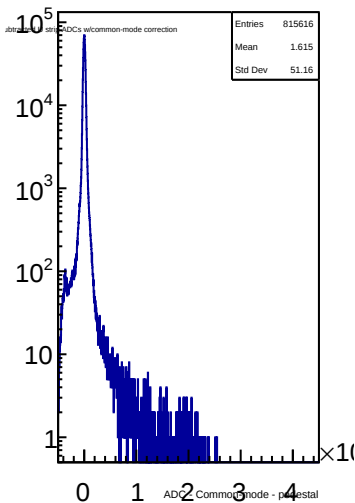
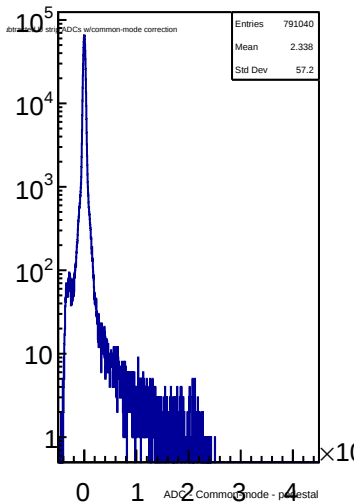
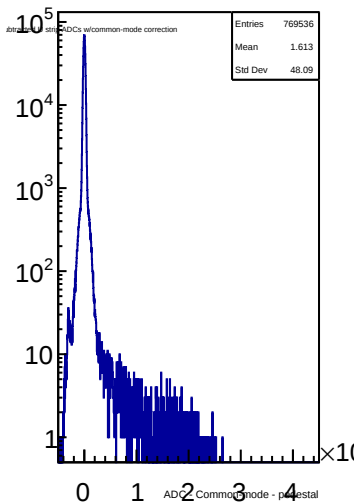
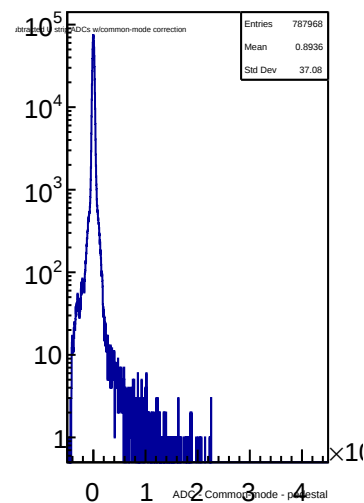
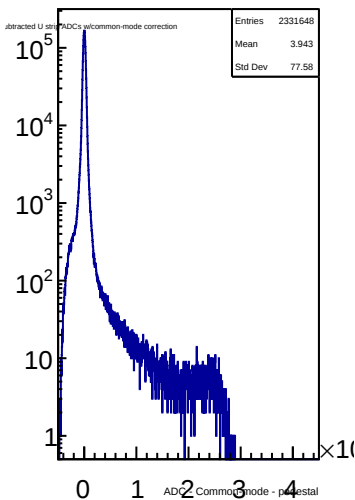
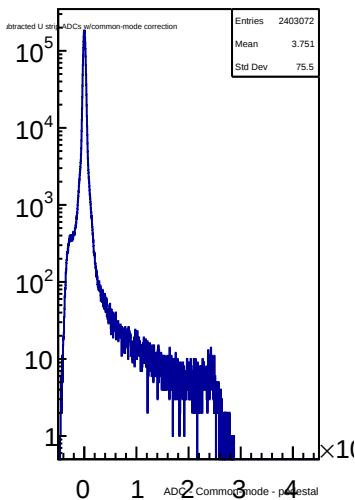
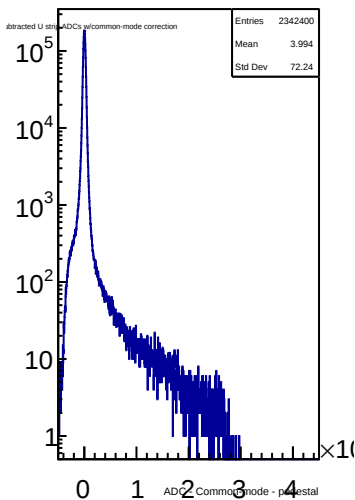
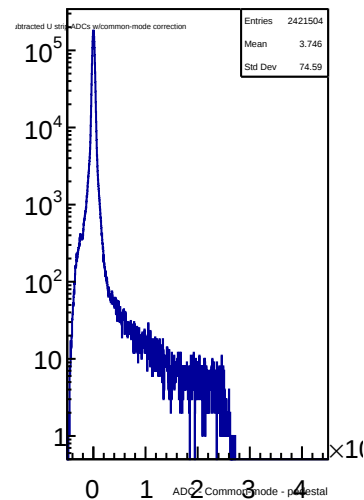
Summary Plots(Run #13297) 19: All modules strip time



Summary Plots(Run #13297) 20: All modules strip rms time



Summary Plots(Run #13297) 21: All modules U strip ADC no zero sup



Summary Plots(Run #13297) 22: All modules V strip ADC no zero sup

