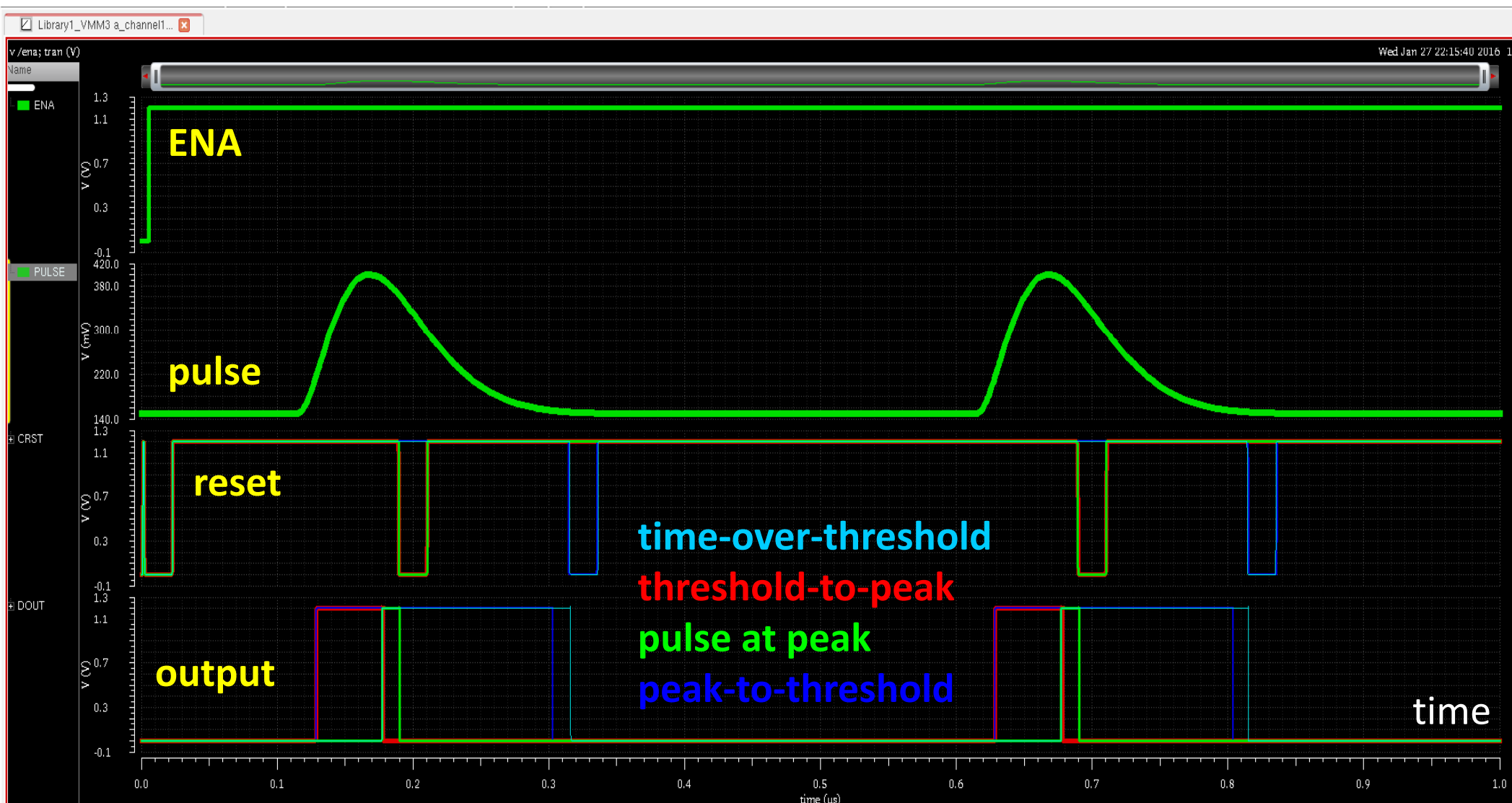


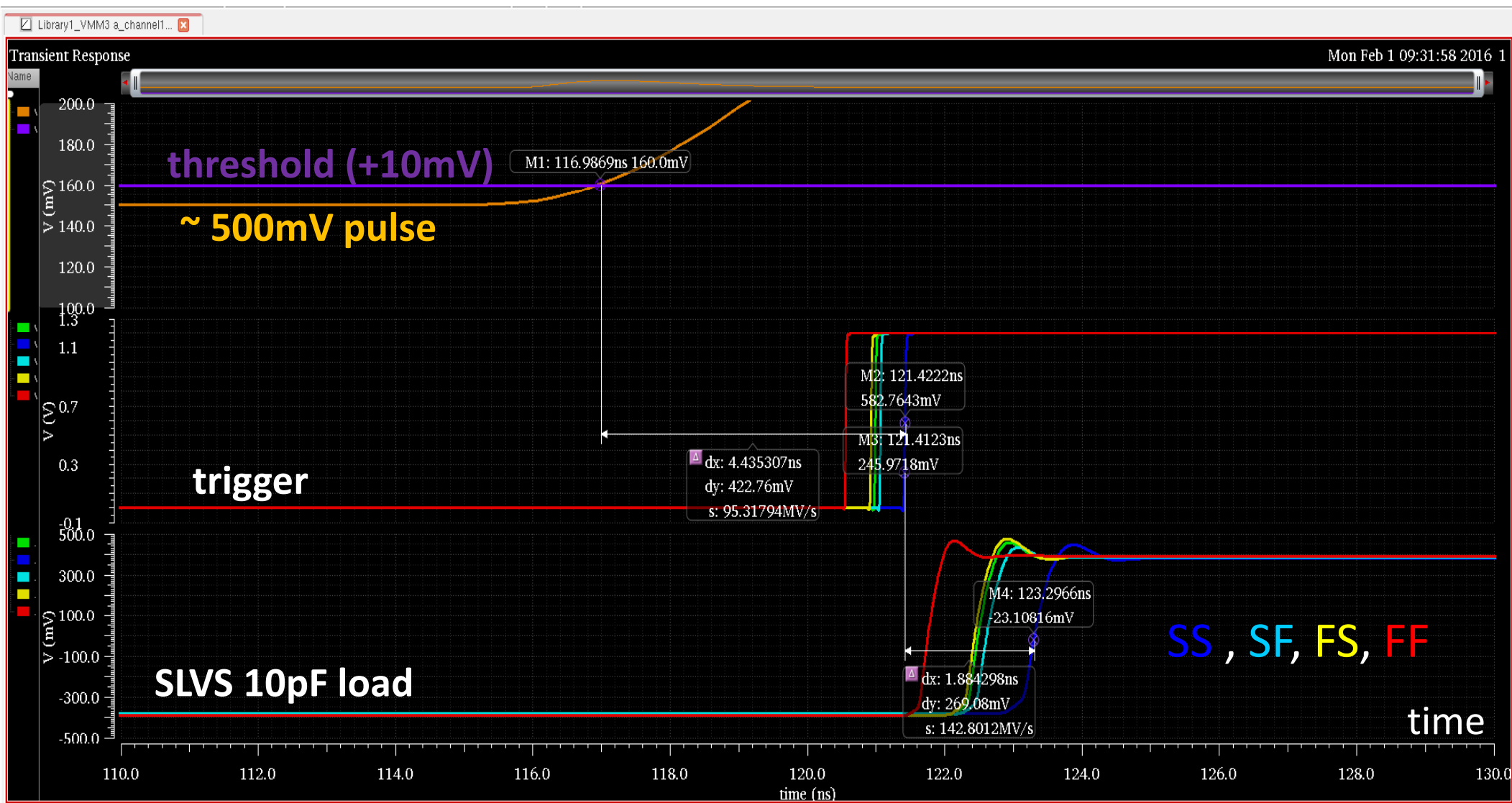
# Direct (Prompt) Timing



Configuration: sttt = 1 (prompt enabled)      stpp,stot = 0,0 threshold-to-peak (TtP)  
 s6b = 0 (6-bit disabled)      0,1 time-over-threshold (ToT)  
 s10b = X (high-resolution)      1,0 pulse-at-peak (PtP)\*  
 slvs = 1 (prompt pads enabled)      1,1 peak-to-threshold (PtT)\*

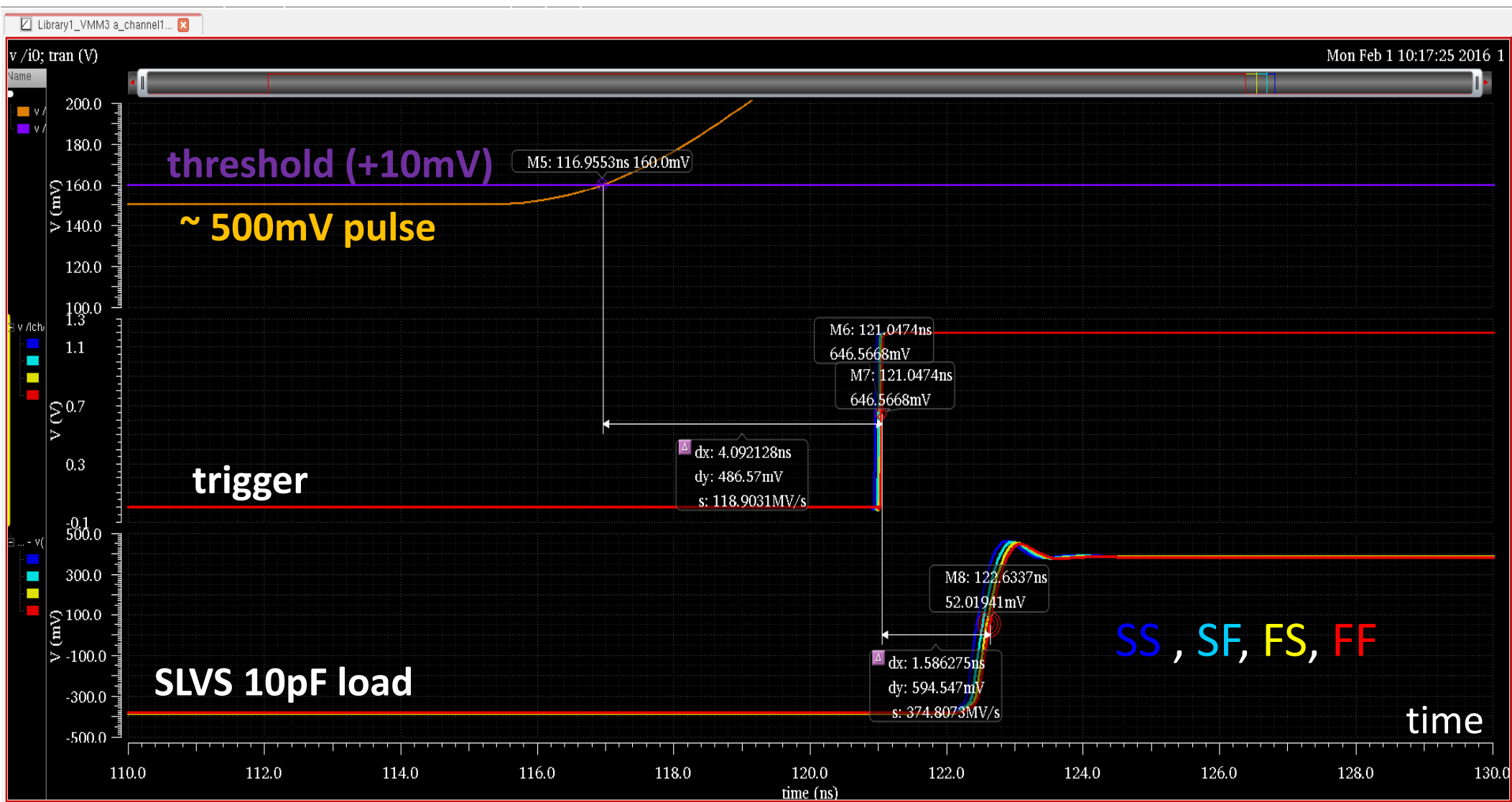
\* not available with s10b

# ToT Latency vs Process Corners



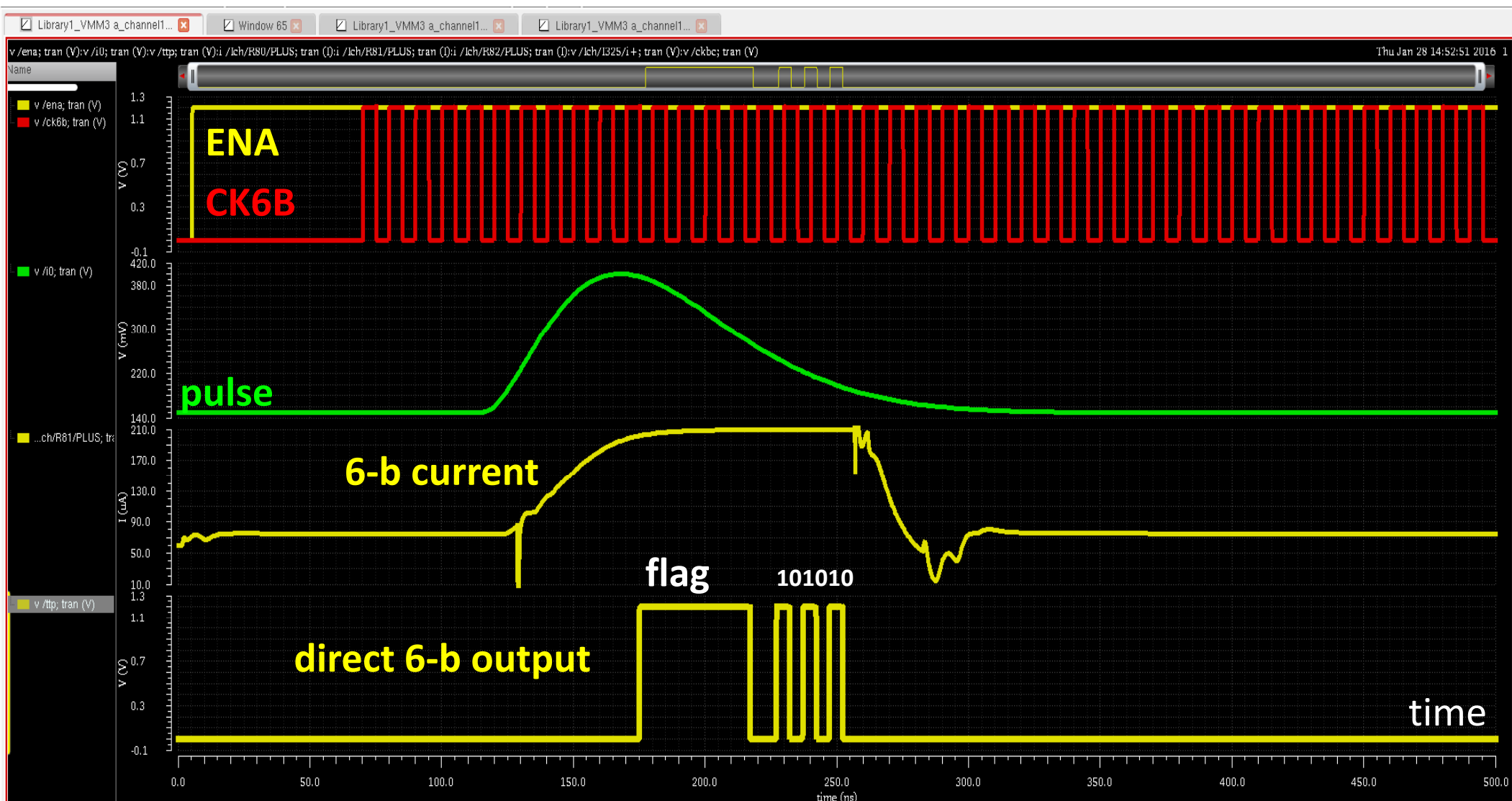
- Maximum latency: threshold-to-trigger ~4.5ns  
trigger to SLVS output ~ 1.9ns

# ToT Latency vs Temperature



- Maximum latency: threshold-to-trigger ~4.1ns  
trigger to SLVS output ~ 1.6ns

# Low-Resolution Output



Configuration: sttt = 1 (prompt enabled)  
s6b = 1 (6-bit enabled)  
s10b = 0 (high-res disabled)  
slvs = 1 (prompt pads enabled)

srat = 0 (ramp-at-threshold disabled)  
sfrst = 0 (fast-reset disabled)  
sdck6b = 1 (DDR enabled)